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REGULATORY UNIT

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September 6, 2001

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VIA HAND DELIVERY

David Waddell, Executive Secretary
Tennessee Regulatory Authority
460 James Robertson Parkway
Nashville, TN 37238

Re: *BellSouth Telecommunications, Inc.'s Entry Into Long Distance
(InterLATA) Service in Tennessee Pursuant to Section 271 of
the Telecommunications Act of 1996*
Docket No. 97-00309

Dear Mr. Waddell:

As permitted by the August 10, 2001 Initial Order of Hearing Officer, BellSouth is submitting its updated performance measurements data for June. This updated data is presented in the same format as the May data, which was attached to the testimony of David A. Coon as Exhibit DAC-3. The updated data includes both actual results for June of this year and associated analysis of the updated data. BellSouth intends to include this type of analysis of performance data in affidavit form with its 271 filing to the FCC. The enclosed does not contain any updates or modifications to the regional SQMs.

Fourteen copies of the updated Coon Exhibit DAC-3 are enclosed. Electronic copies are available upon request. Copies of the enclosed are being provided to counsel of record.

Very truly yours,

Guy M. Hicks

GMH:ch
Enclosure

1
2 **ANALYSIS OF PERFORMANCE MEASUREMENTS DATA**
3
4

5
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1 **ANALYSIS OF PERFORMANCE MEASUREMENTS DATA**

2

3 **I. INTRODUCTION**

4

5 This Supplemental Exhibit presents BellSouth's performance measurements

6 data in Tennessee for June 2001. The performance data for Tennessee is

7 provided in Attachment 1A. In addition, Attachments 2 and 3 to Exhibit DAC-

8 3, filed on July 30, 2001, have been updated for June 2001 data and are

9 attached to this supplemental exhibit as Attachments 2A and 3A.

10 Attachments 4, 5, and 6 to Exhibit DAC-3 have not been updated, and are,

11 therefore, not included in this supplemental filing.

12

13 **II. ANALYSIS OF PERFORMANCE MEASUREMENTS**

14

15 **A. Introduction**

16

17 Attachment 1A is the Monthly State Summary (MSS) for Tennessee for June

18 2001. The June MSS, similar to the May MSS filed on July 30, 2001,

19 contains 2,252 sub-metrics. BellSouth met or exceeded the benchmark/retail

20 analogue criteria for 430 of 513 sub-metrics, or 84%, for which there were

21 both established benchmarks/retail analogues and CLEC activity. The

22 remainder of the 2,252 sub-metrics were either diagnostic (913), had no

23 CLEC activity (717), were parity by design (10), are still under development

1 (2) or are excluded (97) due to data calculation deficiencies. This structure is
2 compliant with the previously listed GPSC order. All measures and sub-
3 metrics are included in these calculations except three measures that are
4 currently under investigation that have known deficiencies in their
5 calculations. They are Average Jeopardy Notice Interval, FOC & Reject
6 Completeness, and LNP Disconnect Timeliness. Even though these
7 measures are included in the MSS and in the total number of measurements
8 calculation (2,252), they are excluded from the "Made/Total" percentage
9 calculations (430/513).

10

11 Each sub-metric designated as having not satisfied the benchmark or
12 BellSouth retail analogue requirement for May and/or June 2001 is included in
13 this Exhibit. Each sub-metric discussed is labeled as being missed in either
14 one or both of the months.

15

16 The following paragraphs will address specific performance measurements
17 associated with each checklist item.

18

19 **B. CHECKLIST ITEM 1 – INTERCONNECTION**

20

21 **1. Collocation**

22 BellSouth provides three separate collocation reports: 1) Average Response
23 Time; 2) Average Arrangement Time; and 3) Percent of Due Dates Missed.

1 Section E in Attachment 1A, Items E.1.1.1 through E.1.3.3, provides these
2 results. BellSouth met the approved benchmarks for all sub-metrics with
3 CLEC activity in both May and June 2001.

4

5 **2. Local Interconnection Trunking**

6 Trunking Reports

7 Attachment 1A, Section C, Items C.1.1 to C.4.2 of the MSS contains data for
8 ordering, provisioning, maintenance and repair, and billing associated with
9 Local Interconnection Trunks.

10

11 In May 2001, BellSouth met 11 of 13 sub-metrics or 85% of the applicable
12 benchmarks/analogues for all local interconnection trunking measures having
13 CLEC activity. In June, BellSouth satisfied the criteria for 14 of the 18 sub-
14 metrics with CLEC activity. The sub-metrics that did not meet the
15 benchmarks/retail analogues for May and/or June 2001 are as follows:

16

17 Service Order Accuracy / Local Interconnection Trunks / < 10 Circuits /
18 Dispatch (C.2.11.1.1) (June)

19 BellSouth met the standard for 3 of the 4 orders reviewed for June 2001. The
20 95% benchmark set a requirement of all 4 orders based on the quantity of
21 orders for this sub-metric. The small universe size for this sub-metric does
22 not produce a conclusive benchmark comparison.

23

1 Service Order Accuracy / Local Interconnection Trunks / >= 10 Circuits /
2 Dispatch (C.2.11.2.1) (May/June)

3 BellSouth met the standard for 16 of the 17 (94.12%) orders reviewed in this
4 sub-metric for May and for 5 of the 6 orders reviewed in June 2001. With
5 sample sizes of only 17 and 6 orders in May and June, respectively and a
6 95% benchmark, any error at all causes a miss for the entire sub-metric.

7

8 Service Order Accuracy / Local Interconnection Trunks / >= 10 Circuits / Non
9 Dispatch (C.2.11.2.2) (May/June)

10 BellSouth met the standard for 9 of the 10 (90%) orders reviewed in this sub-
11 metric for May and for 12 of the 13 (92%) orders reviewed in June 2001. With
12 sample sizes of only 10 and 13 in May and June, respectively orders and a
13 95% benchmark, any error at all causes a miss for the entire sub-metric.

14

15 Invoice Accuracy / Local Interconnection Trunks (C.4.1) (June)

16 The CLECs experienced Local Interconnection invoice accuracy rates that
17 were slightly less than the invoices BellSouth sends to its customers during
18 June 2001 (98.83% accuracy for BellSouth versus 98.43% for the CLEC
19 invoices). The difference in performance was negligible and provides the
20 CLECs with a meaningful opportunity to compete with BellSouth.

21

22 Trunk Blockage

1 Attachment 3A, Item C.5.1 (TGP), shows the actual trunk blocking
2 percentages by hour for June 2001. The Analogue/Benchmark for the Trunk
3 Group Performance measure is any two-hour period in 24 hours where CLEC
4 blockage exceeds BellSouth blockage by more than 0.5%. BellSouth met the
5 approved benchmark for this measure for both May and June 2001.

6

7 **C. CHECKLIST ITEM 2 – UNBUNDLED NETWORK ELEMENTS (UNE)**

8

9 This section addresses the measures associated with UNEs under checklist
10 item 2. Attachment 1A, Sections B.1 – B.3, provides data that is divided into
11 Ordering, Provisioning and Maintenance & Repair operations. The Ordering
12 function is disaggregated into 17 sub-metrics. The Provisioning function has
13 19 sub-metrics, and there are 12 sub-metrics for the Maintenance & Repair
14 function. All Ordering measures will be included in this checklist item
15 because of the overall relationship of the mechanized, partially mechanized
16 and manual processing of Local Service Requests (LSRs). The Provisioning
17 and Maintenance & Repair measures for the following products are included
18 in the checklist item as shown below:

	<u>Product</u>	<u>Checklist Item:</u>
20	Combo (Loop & Port)	#2 – Unbundled Network Elements
21	Combo (Other)	#2 – Unbundled Network Elements
22	Other Design	#2 – Unbundled Network Elements
23	Other Non-Design	#2 – Unbundled Network Elements

- 1 xDSL Loop #4 – Unbundled Local Loops
- 2 UNE ISDN Loop #4 – Unbundled Local Loops
- 3 Line Sharing #4 – Unbundled Local Loops
- 4 2w Analog Loop Design #4 – Unbundled Local Loops
- 5 2w Analog Loop Non Design #4 – Unbundled Local Loops
- 6 2w Analog Loop w/INP Design #4 – Unbundled Local Loops
- 7 2w Analog Loop w/INP Non Design #4 – Unbundled Local Loops
- 8 2w Analog Loop w/LNP Design #4 – Unbundled Local Loops
- 9 2w Analog Loop w/LNP Non Design #4 – Unbundled Local Loops
- 10 Digital Loop < DS1 #4 – Unbundled Local Loops
- 11 Digital Loop => DS1 #4 – Unbundled Local Loops
- 12 Local Interoffice Transport #5 – Unbundled Local Transport
- 13 Switch Ports #6 – Unbundled Local Switching
- 14 INP Standalone #11 – Local Number Portability
- 15 LNP Standalone #11 – Local Number Portability
- 16
- 17 An overall review of the UNE sub-metrics for Ordering, Provisioning, Maintenance & Repair and Billing indicates that BellSouth met the benchmark/retail analogue criteria for 83% of the sub-metrics during the month of May and 81% of the sub-metrics in June 2001.
- 21
- 22 **1. UNE Ordering Measures**
- 23

1 Items B.1.1 – B.1.19 in Attachment 1A show data for Percent Rejected
2 Service Requests, Reject Interval, FOC Timeliness and FOC & Reject
3 Response Completeness. These reports are disaggregated by interface type
4 (electronic, partial electronic and manual), as well as product type.

5

6 **Percent Rejected Service Requests**

7 Results for individual CLECs in this measure vary. Some CLECs have few
8 rejected service requests, while some CLECs have many. Of the CLECs
9 submitting LSRs in June 2001, fifteen of the twenty CLECs that submitted the
10 largest volumes of fully mechanized LSRs had rejection rates ranging from
11 1% to 10%.

12

13 In order to lower the rejection rate for individual CLECs, BellSouth has
14 developed an action plan template to be used in conjunction with an analysis
15 of the pre-order and order activity of a CLEC who is performing at less than
16 90% on flow-through on mechanically submitted orders and has a clarification
17 rate of 20% or higher. So far, seven CLECs in the BellSouth region have
18 agreed to utilize this template. Five CLECs have had presentations
19 concerning their individual results and are currently reviewing the proposals.
20 Meetings are being scheduled with two additional CLECs and twenty-two
21 others are either in the final stages of the action plan preparation or data
22 analyzation. The initial results after implementation indicates a 5% overall
23 reduction in clarifications and rejected requests.

1

2 **Reject Interval**

3 Items B.1.4 - B.1.8 in Attachment 1A examine the Reject Interval for the
4 month of June 2001. For orders submitted electronically, the benchmark is
5 97% within one hour. In May, 56% of the rejected service requests were
6 delivered within the one-hour time period. In June, BellSouth returned 96% of
7 fully mechanized rejects for UNE LSRs within the 1-hour benchmark period.
8 (See the write-up for Items B.1.4.2 – B.1.4.15 below for further discussion
9 concerning electronically submitted orders.)

10

11 For partially mechanized orders, which are LSRs submitted electronically but
12 require service representative intervention, the current benchmark is 85%
13 within 18 hours. In both May and June, BellSouth exceeded this benchmark,
14 with over 98% and 97%, respectively, of partially mechanized rejects being
15 returned to the CLECs within the 18-hour time period.

16

17 For manual orders, the current benchmark is 85% within 24 hours. BellSouth
18 also exceeded this requirement in both May and June 2001, with 95% and
19 98%, respectively, of the LSRs submitted manually being returned to the
20 CLECs within the 24-hour time period.

21

22 The following sub-metrics did not meet the established benchmarks in May
23 and/or June 2001:

1

2 Reject Interval / Local Interoffice transport / Electronic (B.1.4.2) (June)

3 Reject Interval / Combo (Loop & Port) / Electronic (B.1.4.3) (May/June)

4 Reject Interval / ISDN Loop / Electronic (B.1.4.6) (May)

5 Reject Interval / 2w Analog Loop Design / Electronic (B.1.4.8) (May)

6 Reject Interval / 2w Analog Loop w/LNP Design / Electronic (B.1.4.12) (June)

7 Reject Interval / Other Design / Electronic (B.1.4.14) (May/June)

8 Reject Interval / Other Non-Design / Electronic (B.1.4.15) (May/June)

9 The current benchmark for these sub-metrics is \geq 97% within one hour.

10 BellSouth is conducting a detailed root cause analysis of the process for
11 electronic rejects. This analysis addresses the ordering systems (EDI, TAG,
12 and LENS) used by the CLECs and the back-end legacy applications, such
13 as SOCS, that are accessed by the ordering systems.

14

15 Thus far, the analysis has determined that many of the LSRs that did not
16 meet the one-hour benchmark were issued between 11:00 p.m. and 4:30 a.m.
17 Between these hours, the system is unable to process LSRs because certain
18 of the back-end legacy systems are out of service. LSRs submitted during
19 these periods should be excluded from the measurement. BellSouth is
20 currently reviewing the scheduled down time for all systems and how that
21 down time affects the ordering capability of the CLECs.

22

1 With the implementation of May data, BellSouth was directed to change the
2 time stamp identification for the start and complete times of the interval for
3 this measurement from the Local Exchange Ordering (LEO) System to the
4 CLEC ordering interface system (TAG or EDI). However, with this change,
5 BellSouth is currently unable to identify multiple issues of the same version of
6 LSRs that have been rejected (fatal rejects). These rejected LSRs should be
7 excluded from the measurement. If there are multiple issues of the same
8 version, the measure currently calculates the interval from the initial issue to
9 the final issue of the LSR returned to the CLEC, Reject or FOC.
10 Consequently, BellSouth's performance level is inappropriately understated.
11 BellSouth is currently working to determine a fix for this issue.

12

13 With the May and June updates, the data for the UNE Loop & Port
14 Combination is being included in the UNE Other Non-Design sub-metric.
15 BellSouth is currently changing the programming to remove the UNE Loop &
16 Port combination from the UNE Other Non-Design sub-metric and expects the
17 update to be complete with the release of August data.

18

19 Reject Interval / Local Interoffice Transport / Partially Mechanized (B.1.6.2)
20 (May)

21 There were only six orders in this sub-metric for May 2001 with BellSouth
22 meeting the benchmark for five of them. Such a small universe does not

1 produce a statistically conclusive benchmark comparison. There were no
2 CLEC LSRs rejected for this sub-metric in June 2001.

3

4 Reject Interval / LNP (Standalone) / Partially Mechanized (B.1.6.17) (June)
5 BellSouth returned 61 of the 77 rejects for LSRs in this sub-metric within the
6 18-hour benchmark period in June 2001. The benchmark requires that 65 of
7 the 77 LSRs be returned in 18 hours to make the 85% level. There were no
8 distinct patterns or systemic issues revealed from the rejects that missed the
9 time periods.

10

11 Reject Interval / Local Interoffice Transport / Manual (B.1.8.2) (May)
12 There were only two orders in this sub-metric for May 2001 with BellSouth
13 meeting the benchmark for one of them. Such a small universe does not
14 produce a statistically conclusive benchmark comparison. BellSouth met the
15 benchmark for this sub-metric in June 2001.

16

17 Reject Interval / Combo (Loop & Port) / Manual (B.1.8.3) (May)
18 Reject Interval / Other Non-Design / Manual (B.1.8.15) (May)
19 BellSouth met the benchmark for 10 of the 12 LSRs rejected in these sub-
20 metrics for May 2001. The benchmark requires that 11 of the 12 LSRs be
21 returned in 24 hours to make the 85% level. BellSouth met the benchmark for
22 these sub-metrics in June 2001.

23

1 Reject Interval / 2w Analog Loop w/INP Design / Manual (B.1.8.10) (May)

2 There were only four orders in this sub-metric for May 2001 with BellSouth
3 meeting the benchmark for two of them. Such a small universe does not
4 produce a statistically conclusive benchmark comparison. BellSouth met the
5 benchmark for this sub-metric in June 2001.

6

7 Reject Interval / Other Design / Manual (B.1.8.14) (May)

8 There were only two orders in this sub-metric for May 2001 with BellSouth
9 meeting the benchmark for one of them. Such a small universe does not
10 produce a statistically conclusive benchmark comparison. BellSouth met the
11 benchmark for this sub-metric in June 2001.

12

13 FOC Timeliness

14 For LSRs submitted electronically, the benchmark is 95% of the FOCs
15 returned within 3 hours. For partially mechanized LSRs, the benchmark is
16 85% returned within 18 hours. For LSRs submitted manually, the benchmark
17 is 85% returned within 36 hours. In May 2001, BellSouth met the benchmark
18 for 9,675 of the 9,931 LSRs that received a FOC. In June 2001, the
19 benchmark time period was met or exceeded for 8,860 of the 9,214 (96%) of
20 the LSRs that received a FOC. The sub-metrics that did not meet the
21 benchmark in May and/or June are as follows:

22

23 FOC Timeliness / xDSL / Electronic (B.1.9.5) (May/June)

1 FOC Timeliness / 2w Analog Loop Design / Electronic (B.1.9.8) (June)
2 FOC Timeliness / 2w Analog Loop w/LNP Design / Electronic (B.1.9.12)
3 (May/June)

4 FOC Timeliness / LNP (Standalone) / Electronic (B.1.9.17) (May)

5 BellSouth is conducting a detailed root cause analysis of the process for
6 electronic ordering. This analysis addresses the ordering systems (EDI, TAG,
7 and LENS) used by the CLECs and the back-end legacy applications, such
8 as SOCS, that are accessed by the ordering systems.

9

10 Thus far, the analysis has determined that many of the LSRs that did not
11 meet the three-hour benchmark were issued between 11:00 p.m. and 4:30
12 a.m. Between these hours the system is unable to process LSRs because
13 certain of the back-end legacy systems are out of service. Such hours should
14 be excluded from the measurement. BellSouth is currently reviewing the
15 scheduled down time for all systems and how that down time affects the
16 ordering capability of the CLECs.

17

18 FOC Timeliness / xDSL / Partially Electronic (B.1.11.5) (May/June)

19 There were only eight orders in this sub-metric for May and 6 orders for June
20 2001. Such a small universe does not produce a statistically conclusive
21 benchmark comparison.

22

1 FOC Timeliness / 2w Analog Loop w/LNP Design / Partially Electronic
2 (B.1.11.12) (June)

3 BellSouth met the benchmark for 79 of the 93 (84.95%) of the FOCs returned
4 for this sub-metric in June 2001. Using normal rounding convention,
5 BellSouth met the benchmark for the sub-metric for the month.

6

7 FOC Timeliness / 2w Analog Loop w/LNP Non-Design / Manual (B.1.13.13)
8 (June)

9 There was only one order in this sub-metric for June 2001. Such a small
10 universe does not produce a statistically conclusive benchmark comparison.

11

12 FOC & Reject Response Completeness

13 This measurement was introduced with the March 2001 data month. The
14 benchmark is 95%. BellSouth has determined that the coding for the FOC
15 and Reject Completeness measures is flawed and must be rewritten. In this
16 measure, BellSouth did not meet the benchmark in May and/or June 2001 for
17 the FOC and Reject Response Completeness metrics listed below:

18

19 FOC & Reject Response Completeness / Local Interoffice Transport /
20 Electronic (B.1.14.2) (May/June)

21 FOC & Reject Response Completeness / Combo (Loop & Port) / Electronic
22 (B.1.14.3) (June)

- 1 FOC & Reject Response Completeness / xDSL / Electronic (B.1.14.5)
- 2 (May/June)
- 3 FOC & Reject Response Completeness / ISDN Loop / Electronic (B.1.14.6)
- 4 (May/June)
- 5 FOC & Reject Response Completeness / 2w Analog Loop Design /
- 6 Electronic (B.1.14.8) (May/June)
- 7 FOC & Reject Response Completeness / Other Design / Electronic
- 8 (B.1.14.14) (May/June)
- 9 FOC & Reject Response Completeness / Other Non-Design / Electronic
- 10 (B.1.14.15) (June)
- 11 FOC & Reject Response Completeness / Combo (Loop & Port) / Manual
- 12 (B.1.16.3) (May/June)
- 13 FOC & Reject Response Completeness / xDSL / Manual (B.1.16.5)
- 14 (May/June)
- 15 FOC & Reject Response Completeness / 2w Analog Loop Non-Design /
- 16 Manual (B.1.16.9) (May/June)
- 17 FOC & Reject Response Completeness / Other Non-Design / Manual
- 18 (B.1.16.15) (May/June)
- 19 FOC & Reject Response Completeness (Multiple Responses) / Local
- 20 Interoffice Transport / Partial Electronic (B.1.18.2) (May)
- 21 FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
- 22 & Port) / Partial Electronic (B.1.18.3) (May/June)

- 1 FOC & Reject Response Completeness (Multiple Responses) / Other Non-
- 2 Design / Partial Electronic (B.1.18.15) (May/June)
- 3 FOC & Reject Response Completeness (Multiple Responses) / Local
- 4 Interoffice Transport / Manual (B.1.19.2) (May/June)
- 5 FOC & Reject Response Completeness (Multiple Responses) / Combo (Loop
- 6 & Port) / Manual (B.1.19.3) (June)
- 7 FOC & Reject Response Completeness (Multiple Responses) / xDSL /
- 8 Manual (B.1.19.5) (May/June)
- 9 FOC & Reject Response Completeness (Multiple Responses) / ISDN Loop /
- 10 Manual (B.1.19.6) (May/June)
- 11 FOC & Reject Response Completeness (Multiple Responses) / Line Sharing /
- 12 Manual (B.1.19.7) (May/June)
- 13 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 14 Loop Design / Manual (B.1.19.8) (May/June)
- 15 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 16 Loop Non Design / Manual (B.1.19.9) (May/June)
- 17 FOC & Reject Response Completeness (Multiple Responses) / 2w Analog
- 18 Loop w/INP Design / Manual (B.1.19.10) (June)
- 19 FOC & Reject Response Completeness (Multiple Responses) / Other Design
- 20 / Manual (B.1.19.14) (May/June)
- 21 FOC & Reject Response Completeness (Multiple Responses) / Other Non-
- 22 Design / Manual (B.1.19.15) (June)

1 BellSouth has determined that the coding for the FOC & Reject
2 Completeness measures failed to include rejections that were classified as
3 "auto clarifications." This coding change, which is in the process of being
4 rewritten, is projected for completion with August data in late September and
5 will impact all FOC & Reject Completeness measures.

6

7 Flow-Through

8 Attachment 1A, Items F.1.1 - F.1.3, shows Flow-Through data disaggregated
9 by customer type and for the Summary/Aggregate. The following table shows
10 the Regional Flow-Through results for the May through June 2001 period as
11 compared with the Interim SQM benchmarks.

12

13 % Flow-through Service Requests (F.1.1.1 – F.1.3.4)

<u>Customer Type</u>	<u>May 2001</u>	<u>June 2001</u>	<u>Benchmark</u>
Residence	90.25%	92.21%	95%
Business	61.15%	57.26%	90%
UNE	74.80%	78.33%	85%
LNP	90.65%	91.83%	85%

14

15 The table above excludes those LSRs designed to "fall out" for manual
16 handling. The business flow-through rate is well below the 90% objective.
17 Business LSRs are more complex than the typical LSRs and, as a result,
18 there is a greater probability for error. For example, an LSR requesting 10

1 lines with series completion hunting that are located over multiple floors and
2 have a variation of features on the lines presents many more opportunities for
3 system mismatches than one that adds just lines and features.

4

5 BellSouth has established a Flow-Through Improvement Program
6 Management process that includes seven different internal organizations.
7 Ongoing analysis is being done to determine trends and identify flow-through
8 problems. To date, fifteen system enhancements have been identified and
9 are targeted for Encore releases. These releases are being implemented in
10 July and August 2001.

11

12 **2. UNE Provisioning Measures**

13 BellSouth met 83% and 75% of the overall UNE Provisioning measurements
14 in the months of May and June 2001. The following sub-metrics did not meet
15 the applicable benchmarks / retail analogues in May and/or June 2001:

16

17 % Jeopardy Notice Interval >= 48 hours / Combo (Loop & Port) / < 10
18 Circuits (B.2.10.3) (May/June)

19 The calculations for this measure have been determined to be incorrect. The
20 coding change in the Service Order Control System (SOCS) is currently
21 scheduled for a September 13, 2001, system load date. Based on this
22 schedule, the October data month will be the first full month that the change
23 will be in effect.

1

2 % Missed Installation Appointments / Combo (Loop & Port) / >= 10 Circuits /
3 Dispatch (B.2.18.3.2.1) (May)

4 There were only a total of four appointments in this sub-metric for May 2001.
5 Such a small universe does not produce a statistically conclusive comparison
6 with the retail analogue. BellSouth met the retail analogue comparison for
7 this sub-metric in June 2001.

8

9 % Provisioning Troubles w/i 30 Days / Combo (Loop & Port) / < 10 Circuits /
10 Dispatch (B.2.19.3.1.1) (June)

11 BellSouth is currently analyzing the data for this sub-metric. The extremely
12 high number of troubles indicated does not match the overall report rates for
13 June.

14

15 % Provisioning Troubles w/i 30 Days / Combo (Loop & Port) / < 10 Circuits /
16 Non Dispatch (B.2.19.3.1.2) (May)

17 95% of all orders for both the CLECs and BellSouth retail received trouble
18 free service in this sub-metric for May 2001. There were 115 reports for the
19 2,524 orders that completed in the 30 days prior to May. The Customer
20 Wholesale Interconnection Network Service (CWINS) Center representatives
21 are being retrained on proper order setup, testing and cutover procedures.
22 BellSouth expects this training to have a positive impact on its performance

1 for CLECs. BellSouth met the retail analogue comparison for this sub-metric
2 in June 2001.

3

4 % Provisioning Troubles w/i 30 Days / Other Design / < 10 Circuits / Dispatch
5 (B.2.19.14.1.1) (June)

6 There were only eight orders associated with this sub-metric for June 2001.
7 Such a small universe does not produce a statistically conclusive benchmark
8 comparison.

9

10 Average Completion Notice Interval / Combo (Loop & Port) / < 10 Circuits /
11 Dispatch (B.2.21.3.1.1) (May/June)

12 Average Completion Notice Interval / Combo (Loop & Port) / < 10 Circuits /
13 Non-Dispatch (B.2.21.3.1.2) (May/June)

14 The root cause analysis of these measures indicated that the only differences
15 between the performance between BellSouth retail and CLECs are the
16 mismatches found when the orders are compared with the original LSRs.

17 The start of the completion interval is the point at which the technician
18 completes the order, and the interval ends when the completion notice is
19 sent. Any change to a name, number of items, etc., occurring during the
20 provisioning process will generate inconsistencies with the original LSRs that
21 must be resolved before a final completion notice can be sent. Any time to
22 resolve these inconsistencies with the original LSRs is included in the
23 average. Because of numerous CLEC changes and order updates,

1 mismatches on CLEC orders exceed those for BellSouth retail orders.
2 Combining this with the smaller base for the CLECs' measurement raises the
3 average, which sometimes results in a miss. Specific Service Representatives
4 within the Work Management Centers have been assigned to resolve any
5 completion issues that are required. Providing specific training and
6 dedicating personnel to this task should reduce the difference between the
7 CLEC and retail analogue results.

8

9 Service Order Accuracy / Design (Specials) / < 10 Circuits / Dispatch
10 (B.2.34.1.1.1) (May)

11 BellSouth met the standard for 34 of the 36 orders reviewed in this sub-metric
12 for May 2001. The 95% benchmark set a requirement of 35 based on the
13 quantity of orders for this sub-metric. BellSouth met the benchmark
14 comparison for this sub-metric in June 2001.

15

16 Service Order Accuracy / Design (Specials) / < 10 Circuits / Non-Dispatch
17 (B.2.34.1.1.2) (June)

18 BellSouth met the standard for 22 of the 25 orders reviewed in this sub-metric
19 for June 2001. The 95% benchmark set a requirement of 24 of 25 orders for
20 this sub-metric. BellSouth continues to focus on this measurement in order to
21 improve results to meet the benchmark.

22

1 Service Order Accuracy / Loops Non-Design / < 10 Circuits / Dispatch
2 (B.2.34.2.1.1) (May/June)

3 BellSouth met the standard for 26 of the 28 orders reviewed in this sub-metric
4 for May and 3 of the 5 orders reviewed in June 2001. The 95% benchmark
5 set a requirement of 27 of 28 in May and all 5 of 5 for June. BellSouth
6 continues to focus on this measurement in order to improve results to meet
7 the benchmark.

8

9 Service Order Accuracy / Loops Non-Design / < 10 Circuits / Non-Dispatch
10 (B.2.34.2.1.2) (May/June)

11 BellSouth met the standard for 114 of the 124 orders reviewed in this sub-
12 metric for May and for 53 of the 65 orders reviewed in June 2001. The 95%
13 benchmark set a requirement of 118 of 124 orders in May and 62 of 65 orders
14 in June for this sub-metric. BellSouth continues to focus on this measurement
15 in order to improve results to meet the benchmark.

16

17 Service Order Accuracy / Loops Non-Design / >= 10 Circuits / Dispatch
18 (B.2.34.2.2.1) (May)

19 There was only one observation in this sub-metric for May 2001. Such a
20 small universe does not produce a statistically conclusive benchmark
21 comparison. BellSouth met the retail analogue comparison for this sub-metric
22 in June 2001.

23

1 Service Order Accuracy / Loops Non-Design / >= 10 Circuits / Non-Dispatch
2 (B.2.34.2.2.2) (May/June)

3 There were only eight service orders reviewed for this sub-metric in May and
4 nine orders reviewed in June 2001. Such a small universe does not produce
5 a conclusive benchmark comparison.

6

7 **3. UNE Maintenance and Repair (M&R) Measures**

8 BellSouth met the applicable performance standard for 90% and 88% of the
9 overall UNE M&R measurements in May and June 2001, respectively. The
10 sub-metrics that did not meet the fixed critical value for this checklist item in
11 May and/or June 2001 are as follows:

12

13 % Missed Repair Appointments / Combo (Loop & Port) / Non-Dispatch
14 (B.3.1.3.2) (June)

15 BellSouth met 362 of the 380 repair appointments associated with this sub-
16 metric in June 2001. Twelve of the eighteen misses were associated with a
17 problem with one CLEC on the same day. The remaining six misses revealed
18 no distinct patterns or systemic maintenance issues.

19

20 % Missed Repair Appointments / Other Non-Design/ Dispatch (B.3.1.11.1)
21 (May)

22 BellSouth missed 2 of the 9 repair appointments scheduled for this sub-metric
23 in May 2001. Such a small universe does not produce a statistically

1 conclusive comparison with the retail analogue. BellSouth met the retail
2 analogue comparison for this sub-metric in June 2001.

3

4 Customer Trouble Report Rate / Other Design / Dispatch (B.3.2.10.1)
5 (May/June)

6 The difference between the retail analogue and the CLEC aggregate was less
7 than 2% for this sub-metric in both May and June 2001. Both the CLECs and
8 BellSouth retail had greater than 97% trouble free service for all in-service
9 lines in this sub-metric in May and June 2001.

10

11 Customer Trouble Report Rate / Other Design / Non Dispatch (B.3.2.10.2)
12 (May/June)

13 The difference between the retail analogue and the CLEC aggregate was less
14 than 1% for this sub-metric in both May and June 2001. Both the CLECs and
15 BellSouth retail had greater than 98% trouble free service for all in-service
16 lines in this sub-metric in May and June 2001.

17

18 4. **Other UNE Measures**

19

20 **Pre-Ordering**

21 Service Inquiry for xDSL loops (F.3.1.1), Loop Makeup Manual (F.2.1.1) and
22 Loop Makeup Electronic (F.2.2.1) are included in the Pre-Ordering

1 measurements. All measures met the established benchmarks for both May
2 and June 2001 as shown in Attachment 1A.

3

4 **Operations Support Systems**

5 The OSS/Preordering measures for which BellSouth did not meet the
6 benchmark/retail analogue in May and/or June 2001 were:

7

8 Average Response Interval – CLEC (LENS) / HAL / CRIS / Region / RNS
9 (D.1.3.5.1) (May/June)

10 Average Response Interval – CLEC (LENS) / HAL / CRIS / Region / ROS
11 (D.1.3.5.2) (May/June)

12 BellSouth averaged 12.61 seconds response interval in May and 13.09
13 seconds in June 2001 for the CLECs, which is approximately nine seconds
14 longer than the retail analogue. A detailed analysis has identified a problem
15 in the LENS software that deals with response times from HAL/CRIS. This
16 correction was implemented in an update released on July 28, 2001.

17

18 Average Response Interval / CRIS / Region (D.2.4.1.1) (May/June)

19 The average response interval for this sub-metric is measured in three
20 separate disaggregations. The percentage of queries that are responded to
21 in less than 4 seconds, less than 10 seconds and greater than 10 seconds.
22 The average response interval for the CLEC requests did not meet the retail
23 analogue intervals for the less than 4-second disaggregation but exceeded

1 both the less than 10 and greater than 10 seconds responses. The CLEC
2 response interval was 94.25% within 4 seconds in May as compared with
3 95.65% for the retail analogue and 94.76% for CLECs in June as compared
4 to 95.81% for the retail analogue. For the less than 10 second response
5 interval, the CLECs received over 99% of their responses and the retail
6 analogue received over 98% in both May and June. The one percent
7 difference for both of these intervals indicates equivalent service levels for the
8 CLECs and BellSouth retail.

9

10 Average Response Interval / DLETH / <= 4 sec / Region (D.2.4.2.1) (June)

11 The average response interval for this sub-metric is measured in three
12 separate intervals. The percentage of queries that are responded to in less
13 than 4 seconds, less than 10 seconds and greater than 10 seconds. In June
14 2001, the average response interval for the CLEC requests did not meet the
15 retail analogue intervals for the less than 4-second disaggregation, but
16 exceeded both the less than 10 and greater than 10 seconds responses.

17

18 Average Response Interval / LMOSupd / Region (D.2.4.5.1, D.2.4.5.2,
19 D.2.4.5.3) (May/June)

20 The average response interval for this sub-metric is measured in three
21 separate disaggregations. The percentage of queries that are responded to
22 in less than 4 seconds, less than 10 seconds and greater than 10 seconds.
23 The average response interval for the CLEC requests did not meet the retail

1 analogue intervals for all three of these sub-metrics in May or June 2001. For
2 each of the three sub-metrics, there was less than a 1% difference in the
3 responses received by the CLECs and BellSouth retail. The 1% difference
4 for all of these intervals indicates equivalent service levels for both the CLECs
5 and BellSouth retail.

6

7 **Average Response Interval / LNP/ Region (D.2.4.6.1) (May/June)**

8 The average response interval for this sub-metric is measured in three
9 separate disaggregations. The percentage of queries that are responded to
10 in less than 4 seconds, less than 10 seconds and greater than 10 seconds.
11 The average response interval for the CLEC requests did not meet the retail
12 analogue intervals for the less than 4-second disaggregation in May or June
13 2001, but exceeded both the less than 10 and greater than 10 seconds
14 responses in both months. The less than 1% difference for the response
15 intervals indicates equivalent service levels for the CLECs and BellSouth
16 retail.

17

18 **General - Change Management**

19 **% Software Release Notices sent on time (F.10.1) (May)**

20 There were only four releases in this sub-metric for May 2001 with BellSouth
21 meeting the benchmark for three of them. Such a small universe does not
22 produce a statistically conclusive benchmark comparison. BellSouth met the
23 benchmark intervals for this sub-metric in June 2001.

1

2 **General – Billing**

3 **Usage Data Delivery Accuracy (F.9.1) (May)**

4 This measure compares the rate at which usage data is sent accurately to
5 CLECs with the same measure for the BellSouth retail analogue. In May
6 2001, a software problem caused an error for one CLEC which dropped the
7 results to 99.99% compared to BellSouth's 100%. Out of approximately
8 14,000 packs (or groupings) of usage data sent to CLECs in May, only one of
9 the packs was impacted by the problem. Once the software was fixed, the
10 corrected pack data was resent successfully to the CLEC. BellSouth met the
11 retail analogue comparison for this sub-metric in June 2001.

12

13 **Mean Time to Deliver Usage (F.9.4) (May)**

14 This measure compares the average number of days to deliver usage to
15 CLECs with the BellSouth retail analogue. In May 2001, the CLEC result was
16 3.76 days compared to BellSouth's 3.73 days. While the CLEC measurement
17 is slightly greater than the BellSouth results, the CLECs are provided with
18 substantially the same opportunity to bill end users as is BellSouth. BellSouth
19 met the retail analogue comparison for this sub-metric in June 2001.

20

21 **Recurring Charge Completeness / UNE (F.9.5.2) (June)**

22 In June 2001, the result for this measure was 86.75% against a benchmark of
23 90%. During June 2001, 4 service orders were sent to the billing system for

1 inclusion on bills to CLECs beyond the bill period for which the order should
2 have been billed. Service representatives within the BellSouth Local
3 Interconnection Service Center (LISC) were completing error correction
4 activities which delayed the orders coming to the billing system. Even though
5 these orders caused the benchmark of 90% to be missed by a small amount,
6 the results are better than the results for BellSouth's own retail customers
7 (BellSouth's results were 75.14% for June, 2001). In any event, the CLECs
8 are provided with a meaningful opportunity to compete as these issues do not
9 impede the ability to serve end users.

10

11 **General – New Business Requests**

12 **% Quotes Provided within 10 Business Days (F.11.2.1) (June)**

13 In June 2001, there were only two requests in this sub-metric. Such a small
14 universe does not provide a conclusive benchmark comparison.

15

16 **General – Ordering**

17 **% Acknowledgement Message Timeliness / EDI (F.12.1.1) (May)**

18 A root cause analysis has identified 8,856 of 10,010 (88%) failed EDI
19 acknowledgements were submitted by the Florida Third Party Test (3PT)
20 CLEC and are not being filtered out of the acknowledgement calculations.

21 During the setup for the 3PT volume tests, a problem was encountered in the
22 EDI system. Since the setup had to be redone, all of the acknowledgements
23 that had been generated for the test were eliminated. With the removal of

1 these test messages the results would have been 98.8%, well above the 90%
2 benchmark for this sub-metric in May 2001. BellSouth met the retail analogue
3 comparison for this sub-metric in June 2001.

4

5 % Acknowledgement Message Completeness / EDI (F.12.2.1) (May/June)

6 BellSouth experienced EDI outages in May and June that caused less than
7 3% of the acknowledgement messages to not be returned. A Stability Plan to
8 improve EDI availability has been put into effect. This plan includes
9 implementing both a manual application monitoring schedule (24 / 7) and
10 increased mechanized application alarms to more adequately monitor and
11 react to application outages. The database parameters have also been
12 adjusted to allow for maximum processing in the EDI system.

13

14 % Acknowledgement Message Completeness / TAG (F.12.2.2) (May/June)

15 BellSouth failed to deliver 16 of the 183,966 messages in May and 50 of the
16 127,390 messages in June 2001 for this sub-metric. Analysis continues to
17 identify causes for message delivery failures. However, such a small number
18 of failed records have not revealed any systemic process problems.

19

20 General – Network Outage Notification

21 Mean Time to Notify CLEC of Network Outage (F.14.1) (June)

22 BellSouth did not meet the retail analogue for this sub-metric in June 2001.
23 Due to an undetected E-mail failure, one of the three CLEC notifications did

1 not get delivered. This interval was over 6,000 minutes that ran from June
2 26th when the outage occurred, until the end of the data month. BellSouth is
3 reviewing its procedures to eliminate this type of occurrence.

4

5 **D. CHECKLIST ITEM 4 – UNBUNDLED LOCAL LOOPS**

6 As discussed in Checklist Item 2, Sections B.2 and B.3 of Attachment 1A
7 provide data for provisioning and maintenance & repair measures for
8 unbundled local loops.

9

10 For purposes of discussion in this checklist item, the local loop sub-metrics
11 have been separated into two mode-of-entry groups, xDSL and
12 SL1/SL2/Digital. The xDSL group includes xDSL (ADSL, HDSL, UCL), ISDN
13 and Line Sharing sub-metrics. The SL1/SL2/Digital group includes the design
14 and non-design 2-wire analog loops, as well as the 2-wire and 4-wire digital
15 loop sub-metrics.

16

17 **xDSL Group**

18

19 **1. Provisioning Measures**

20 The xDSL group sub-metrics that did not meet the fixed critical value
21 comparison requirements for May and/or June 2001 are as follows:

22

23 **Held Order Interval / xDSL / < 10 Circuits / Other (B.2.3.5.1.3) (June)**

1 There was only one held order for this sub-metric in June 2001. Such a small
2 universe for this sub-metric does not provide a statistically conclusive
3 comparison to the retail analogue.

4

5 % Jeopardies / UNE ISDN / Electronic (B.2.5.6) (June)

6 There were 9 orders placed in jeopardy status of the 36 orders associated
7 with this sub-metric in June 2001. Of the 16 UNE ISDN orders placed in
8 jeopardy during the month, both electronic and manual, all but six of the
9 jeopardies were resolved prior to the due date and the orders worked on time.

10

11 Missed Installation Appointments / Line Sharing / < 10 Circuits / Non-Dispatch
12 (B.2.18.7.1.2) (June)

13 BellSouth met 40 of the 41 installation appointments associated with this sub-
14 metric in June 2001. The one missed appointment did not reveal any
15 systemic installation process problem.

16

17 % Provisioning Troubles w/i 30 Days / xDSL / < 10 Circuits / Dispatch
18 (B.2.19.5.1.1) (May)

19 There were a total of 16 reports for the 316 orders that completed in the 30
20 days prior to May 2001 for this sub-metric. 95% of all the orders completed
21 with trouble free service during this period. BellSouth met the retail analogue
22 comparison for this sub-metric in June 2001.

23

1 % Provisioning Troubles w/i 30 Days / UNE ISDN / < 10 Circuits / Dispatch
2 (B.2.19.6.1.1) (June)

3 There were 9 trouble reports for this sub-metric for the 87 orders that
4 completed during the 30 days prior to June 2001. No systemic installation
5 process problems were revealed from these trouble reports.

6

7 Average Completion Notice Interval / ISDN Loops / < 10 Circuits / Dispatch
8 (B.2.21.6.1.1) (May)

9 There were only a total of two completions in this sub-metric for May 2001.
10 Such a small universe does not produce a statistically conclusive comparison
11 with the retail analogue. BellSouth met the retail analogue comparison for
12 this sub-metric in June 2001.

13

14 **2. Maintenance & Repair Measures**

15 The xDSL group sub-metrics that did not meet the fixed critical value
16 comparison requirements for May and/or June 2001 are as follows:

17

18 % Missed Repair Appointments / ISDN Loops / Dispatch (B.3.1.6.1) (May)
19 BellSouth missed three of the thirteen scheduled appointments for this sub-
20 metric in May 2001. There was no systemic problem found for any of the
21 three missed appointments. BellSouth met the retail analogue comparison for
22 this sub-metric in June 2001.

23

1 % Missed Repair Appointments / Line Sharing / Non-Dispatch (B.3.1.7.2)

2 (June)

3 BellSouth missed one of only nine appointments associated with this sub-
4 metric in June 2001. Such a small universe for the sub-metric does not
5 provide a statistically conclusive comparison with the retail analogue.

6

7 Customer Trouble Report Rate / xDSL Loops / Non Dispatch (B.3.2.5.2)

8 (May/June)

9 There as a total of eighteen troubles reported for the 2,493 in-service lines for
10 this sub-metric in May and 12 troubles reported for the 2,589 lines for June
11 2001. Both the CLECs and BellSouth retail had greater than 99% trouble free
12 service for all in-service lines in this sub-metric in both May and June 2001.

13

14 Customer Trouble Report Rate / ISDN Loops / Dispatch (B.3.2.6.1) (May)

15 There was a total of 13 troubles reported for the 1,473 in service lines for this
16 sub-metric in May 2001. Both the CLECs and BellSouth retail had greater
17 than 99% trouble free service for all in service lines in this sub-metric in May.
18 BellSouth met the retail analogue comparison for this sub-metric in June
19 2001.

20

21 Customer Trouble Report Rate / ISDN Loops / Non-Dispatch (B.3.2.6.2)

22 (May)

1 The CLEC aggregate only reported one trouble for the 1,473 in service lines
2 for this sub-metric in May 2001. Both the CLECs and BellSouth retail had
3 greater than 99.9% trouble free service for all in service lines in this sub-
4 metric in May. BellSouth met the retail analogue comparison for this sub-
5 metric in June 2001.

6

7 Customer Trouble Report Rate / Line Sharing / Non-Dispatch (B.3.2.7.2)
8 (June)

9 There were only 9 troubles reported for the 292 lines in service for this sub-
10 metric in June 2001. Both the CLECs and BellSouth retail had greater than
11 96.9% trouble free service for all in service lines in this sub-metric in June.

12

13 Maintenance Average Duration / UNE ISDN / Non-Dispatch (B.3.3.6.2) (June)
14 There were only seven orders associated with this sub-metric in June 2001.
15 Such a small universe for the sub-metric does not provide a statistically
16 conclusive comparison to the retail analogue.

17

18 % Repeat Troubles w/I 30 Days / Line Sharing / Non-Dispatch (B.3.4.7.2)
19 (June)

20 There were only nine orders associated with this sub-metric in June 2001.
21 Such a small universe for the sub-metric does not provide a statistically
22 conclusive comparison to the retail analogue.

23

1 **SL1/SL2/Digital Loop Group**

2 **1. Provisioning Measures**

3 The SL1/SL2/Digital Loop group sub-metrics that did not meet the fixed
4 critical value comparison requirements for May and/or June 2001 are as
5 follows:

6

7 **Order Completion Interval (OCI)**

8 A root cause analysis for OCI for Non-Dispatch orders revealed that
9 BellSouth was offering a 0 to 2-day interval on retail non-dispatched POTS
10 orders, but the wholesale orders were incorrectly receiving the same longer
11 interval as "dispatched" orders. BellSouth is currently reviewing the
12 programming change to correct this issue.

13

14 In addition to the appointment interval issue, OCI is adversely affected by
15 LSRs for which CLECs request intervals beyond the offered interval and do
16 not enter an "L" code on the order. When a CLEC requests an interval
17 beyond the normal interval offered by BellSouth, an "L" code should be
18 entered on the service order. "L" coded orders are excluded from the OCI
19 metrics.

20

21 Order Completion Interval / 2w Analog Loop Design / < 10 Circuits / Dispatch
22 (B.2.1.8.1.1) (May)

1 The unadjusted order completion interval for this sub-metric was 7.08 days in
2 May 2001, compared to the retail analogue of 5.84 days. OCI is adversely
3 affected by LSRs for which CLECs request intervals beyond the offered
4 interval and do not enter an "L" code on the order. When a CLEC requests
5 an interval beyond the normal interval offered by BellSouth, an "L" code
6 should be entered on the service order. "L" coded orders are excluded from
7 the OCI metrics. Also, beginning with June data, all "C Order Disconnects"
8 received from End-User Customers will be excluded from this measure as are
9 "D Order" disconnects. BellSouth met the retail analogue comparison for this
10 sub-metric in June 2001.

11

12 % Jeopardies / 2w Analog Loop Design (B.2.5.8) (May/June)

13 There were a total of 12 jeopardy issued for the 17 electronic orders that
14 were scheduled for this sub-metric in May and 15 jeopardy issued for the 98
15 electronic orders scheduled for June 2001. While the data indicates that
16 BellSouth placed a higher percentage of CLEC orders in jeopardy status, all
17 but two of the orders which were placed in jeopardy were actually worked on
18 time as indicated by the fact that there were only two missed installation
19 appointments for this sub-metric in May 2001. Of the 31 total 2w Analog
20 Loop Design order jeopardy for June, both electronic and manual, all but 8
21 of the jeopardy were resolved prior to the due date and the orders worked
22 as scheduled.

23

1 % Jeopardies / Digital Loop < DS1 (B.2.5.18) (June)

2 There were 9 orders placed in jeopardy status of the 36 orders associated
3 with this sub-metric in June 2001. Of the 16 Digital Loop < DS1 orders placed
4 in jeopardy during the month, both electronic and manual, all but six of the
5 jeopardies were resolved prior to the due date and the orders worked on time.

6

7 % Jeopardy Notices issued >= 48 Hours / 2w Analog Loop w/LNP Design
8 (B.2.10.12) (May)

9 The calculations for this measure have been determined to be incorrect. The
10 coding change in the Service Order Control System (SOCS) is currently
11 scheduled for a September 13, 2001, system load date. Based on this
12 schedule, the October data month will be the first full month that the change
13 will be in effect.

14

15 Missed Installation Appointments / Digital Loop < DS1 / , 10 Circuits /
16 Dispatch (B.2.18.18.1.1) (June)

17 BellSouth met 69 of the 75 appointments scheduled for this sub-metric in
18 June 2001. The 6 missed appointments for June revealed no distinct patterns
19 or systemic installation process issues.

20

21 % Provisioning Troubles w/i 30 Days / 2w Analog Loop w/LNP Design / < 10
22 Circuits / Dispatch (B.2.19.12.1.1) (May/June)

1 There were a total of 30 troubles reported for the 247 orders that completed in
2 the 30 days prior to May and 38 troubles reported for the 171 orders
3 completed in the 30 days prior to June 2001 for this sub-metric. Analysis of
4 the trouble reports indicates that many of the reports (30% for June) are
5 closed as "NTF" (no trouble found). In June, 19 (50%) of the 38 total trouble
6 reports for the sub-metric were from one CLEC, with 9 of those reports closed
7 as "NTF."

8

9 % Provisioning Troubles w/i 30 Days / Digital Loop < DS1 / < 10 Circuits /
10 Dispatch (B.2.19.18.1.1) (June)

11 There were 9 trouble reports for this sub-metric for the 87 orders that
12 completed during the 30 days prior to June 2001. No systemic installation
13 process problems were revealed from these trouble reports.

14

15 % Provisioning Troubles w/i 30 Days / Digital Loop >= DS1 / < 10 Circuits /
16 Dispatch (B.2.19.19.1.1) (June)

17 There were 25 trouble reports for this sub-metric for the 289 orders that
18 completed during the 30 days prior to June 2001. Analysis of these reports
19 revealed no distinct patterns or systemic installation process problems.

20

21 Average Completion Notice Interval / 2w Analog Loop Design / < 10 Circuits /
22 Dispatch (B.2.21.8.1.1) (June)

1 Average Completion Notice Interval / 2w Analog Loop w/LNP Design / < 10

2 Circuits / Dispatch (B.2.21.12.1.1) (May/June)

3 The root cause analysis of these measures indicated that the only differences
4 between the performance comparing BellSouth retail and CLECs are the
5 mismatches found when the orders are compared with the original LSRs.
6 The start of the completion interval is the point at which the technician
7 completes the order, and the interval ends when the completion notice is
8 sent. Any change to a name, number of items, etc., occurring during the
9 provisioning process will generate inconsistencies with the original LSRs that
10 must be resolved before a final completion notice can be sent. Any time to
11 resolve these inconsistencies with the original LSRs is included in the
12 average. Because of numerous CLEC changes and order updates,
13 mismatches on CLEC orders exceed those for BellSouth retail orders.
14 Combining this with the smaller base for the CLECs' measurement raises the
15 average, which sometimes results in a miss. Specific Service
16 Representatives within the Work Management Centers have been assigned
17 to resolve any completion issues that are required. Providing specific training
18 and dedicating personnel to this task should reduce the difference between
19 the CLEC and retail analogue results.

20

21 **3. Maintenance & Repair Measures**

22 The SL1/SL2/Digital group sub-metrics that did not meet the fixed critical
23 value comparison requirements for May and/or June 2001 are as follows:

1

2 Missed Repair Appointments / 2w Analog Loop Design / Dispatch (B.3.1.8.1)
3 (June)

4 BellSouth met 333 of the 369 repair appointments scheduled for this sub-
5 metric in June 2001. Analysis of the missed appointments revealed that a
6 majority of the missed appointments were due to facilities unavailability
7 caused by five cut cables and one power company coordination issue that
8 occurred during the month.

9

10 **E. CHECKLIST ITEM 5 – UNBUNDLED LOCAL TRANSPORT**

11

12 The data in these measures indicate that BellSouth met the
13 benchmark/analogue requirements for all measurements in Checklist Item 5
14 for both May and June 2001.

15

16 **F. CHECKLIST ITEM 6 – UNBUNDLED LOCAL SWITCHING**

17

18 The data in these measures indicate that BellSouth met the
19 benchmark/analogue requirements for all measurements in Checklist Item 6
20 for both May and June 2001.

21

1 **G. CHECKLIST ITEM 7a – 911 AND E911 SERVICES**

2 **H. CHECKLIST ITEM 7b – DIRECTORY ASSISTANCE/OPERATOR**
3 SERVICES

4

5 As indicated in Attachment 1A, Sections F.6, F.7 and F.8, BellSouth met the
6 benchmark/analogue requirements of Checklist Items 7a and 7b in May and
7 June 2001. Even though BellSouth tracks and reports these measures, the
8 processes used in providing these services are designed to provide parity for
9 all users.

10

11 **I. CHECKLIST ITEM 10 – ACCESS TO DATABASES AND ASSOCIATED**
12 SIGNALING

13 BellSouth made three of the four sub-metrics associated with this checklist
14 item in May and met all of the benchmarks in June 2001. See items F.13.3.1
15 through F.13.3 in Attachment 1A for further details. The one item that did not
16 meet the appropriate benchmark in May 2001 is as follows:

17

18 % NXXs / LRNs Loaded by LERG Effective Date (Region) (F.13.3) (May)
19 The measure indicates that only 21 of the 33 NXXs were loaded by their
20 effective date for the entire BellSouth region. Tennessee met both of the NXX
21 load dates for this sub-metric in May 2001. BellSouth met the benchmark for
22 this sub-metric in June 2001.

23

1 I. CHECKLIST ITEM 11 – NUMBER PORTABILITY

2

3 All the measurements in this Checklist Item were met or exceeded for May
4 and/or June 2001 except for the following:

5

6 Order Completion Interval / LNP (Standalone) / < 10 Circuits / Dispatch
7 (B.2.1.17.1.1) (May)
8 The unadjusted order completion interval for May 2001 was 16.08 days
9 compared to the retail analogue of 5.83 days. OCI is adversely affected by
10 LSRs for which CLECs request intervals beyond the offered interval and do
11 not enter an "L" code on the order. When a CLEC requests an interval
12 beyond the normal interval offered by BellSouth, an "L" code should be
13 entered on the service order. "L" coded orders are excluded from the OCI
14 metrics. BellSouth met the retail analogue comparison for this sub-metric in
15 June 2001.

16

17 Order Completion Interval / LNP (Standalone) / < 10 Circuits / Non-Dispatch
18 (B.2.1.17.1.2) (May/June)
19 The unadjusted order completion interval for May 2001 was 8.21 days
20 compared to the retail analogue of 1.05 days. In June 2001, the unadjusted
21 order completion interval was 6.24 days compared to the retail analogue
22 interval of 0.88 days. A root cause analysis for OCI for Non-Dispatch orders
23 revealed that BellSouth was offering a 0 to 2-day interval on retail non-

1 dispatched POTS orders, but the wholesale non-dispatched orders were
2 receiving the same interval as "dispatched" orders. In addition, OCI is
3 adversely affected by LSRs for which CLECs request intervals beyond the
4 offered interval. When a CLEC requests an interval beyond the available
5 interval offered by BellSouth, an "L" code is entered on the Service Order
6 generated by BellSouth. "L" coded orders are excluded from the OCI metrics.

7

8 Order Completion Interval / LNP (Standalone) / >= 10 Circuits / Non-Dispatch
9 (B.2.1.17.2.2) (June)

10 There were only two orders for this sub-metric in June 2001. Such a small
11 universe for the sub-metric does not provide a statistically conclusive
12 comparison with the retail analogue.

13

14 Average Completion Notice Interval / LNP (Standalone) / < 10 Circuits /
15 Dispatch (B.2.21.17.1.1) (June)

16 Average Completion Notice Interval / LNP (Standalone) / < 10 Circuits / Non-
17 Dispatch (B.2.21.17.1.2) (May/June)

18 The root cause analysis of these measures indicated that the only differences
19 between the performance comparing BellSouth retail and CLECs are the
20 mismatches found when the orders are compared with the original LSRs.
21 The start of the completion interval is the point at which the technician
22 completes the order, and the interval ends when the completion notice is
23 sent. Any change to a name, number of items, etc., occurring during the

1 provisioning process will generate inconsistencies with the original LSRs that
2 must be resolved before a final completion notice can be sent. Any time to
3 resolve these inconsistencies with the original LSRs is included in the
4 average. Because of numerous CLEC changes and order updates,
5 mismatches on CLEC orders exceed those for BellSouth retail orders.
6 Combining this with the smaller base for the CLECs' measurement raises the
7 average, which sometimes results in a miss. Specific Service
8 Representatives within the Work Management Centers have been assigned
9 to resolve any completion issues that are required. Providing specific training
10 and dedicating personnel to this task should reduce the difference between
11 the CLEC and retail analogue results.

12

13 Disconnect Timeliness / LNP / < 10 Circuits (B.2.31.1) (May/June)

14 The Disconnect Timeliness measure is supposed to track the time it takes to
15 disconnect a number in the central office switch after the message has been
16 received from the Local Number Portability (LNP) Gateway that it is ready.
17 However, this measurement does not track the relevant time to perform this
18 function.

19

20 On a great majority of LNP orders, BellSouth creates what is referred to as a
21 "trigger" in conjunction with the order. This trigger gives the end user
22 customer the ability to make and receive calls from other customers who are
23 served by the customer's host switch at the time of the LNP activation. This

1 ability is not dependent upon BellSouth working a disconnect order in the
2 central office switch. In other words, when a trigger is involved, an end user
3 customer can receive calls from other customers served by the same host
4 switch before the disconnect order is ever worked.

5

6 As it currently exists, Performance Measure P-11 does not recognize the
7 importance of triggers and their effect on the LNP process. Rather, the
8 current measure calculates the end time of the LNP activity as the processing
9 of the actual disconnect order in the host switch, even though, from a
10 customer's perspective, this activity is totally meaningless on most LNP
11 orders. It is the activation of the LNP and the routing function accomplished
12 by the LSMS that ultimately determines whether the end user is back in full
13 service and is able to make and receive calls when a trigger is used in porting
14 a telephone number. So, while BellSouth may be missing this measure, the
15 actual impact on CLECs and their end users, for a great majority of the orders
16 is minimal, or nonexistent.

17

18 BellSouth is pursuing a change in this measure that more accurately reflects
19 the LNP process and its impacts on end users.

20

1

K. CHECKLIST ITEM 14 – RESALE

2 BellSouth has met or exceeded the benchmarks/analogues for 83% and 87%
3 of the total resale metrics for the months of May and June 2001, respectively.
4 The details are delineated in Attachment 1A, Items A.1.1.1.1 through A.4.2.

5

1. Resale Ordering Measures

FOC Timeliness

8 For the month of May 2001, BellSouth processed approximately 16,504
9 Resale LSRs in Tennessee and met the relevant benchmark on 98% of all
10 FOCs. Of the 16,504 LSRs, 14,680 were fully mechanized with 99% meeting
11 the 3-hour benchmark, clearly exceeding the 95% target. In June 2001,
12 BellSouth returned 98% of the 15,351 FOCs processed within the relevant
13 benchmark period. Of the 15,351 LSRs receiving FOCs, 13,308 were fully
14 mechanized with 98% meeting the 3-hour benchmark interval. See
15 Attachment 1A, Sections A.1.9 through A.1.13 for further details of the June
16 data.

17

Reject Interval

19 During the month of May 2001, there were 3,466 rejected LSRs, either
20 mechanically or manually processed, with 94% meeting the benchmark. The
21 benchmark for electronic rejects is 97% within 1 hour. 54% of all orders were
22 processed electronically, and 91% met the 1-hour benchmark. In June 2001,
23 there were 2,424 rejected Resale LSRs with 95% meeting the relevant

1 benchmark interval. Of the 2,424 rejected LSRs, 1,464 were processed
2 electronically with 94% meeting the 1-hour benchmark interval. See
3 Attachment 1A, Items A.1.4 through A.1.8 for further details of the June data.

4

5 The Resale Ordering sub-metrics for which BellSouth did not meet the
6 benchmarks/analogues for May and/or June 2001 were:

7

8 Reject Interval / Residence / Electronic (A.1.4.1) (May/June)

9 Reject Interval / Business / Electronic (A.1.4.2) (May)

10 The current benchmark for these sub-metrics is \geq 97% within one hour.
11 BellSouth is conducting a detailed root cause analysis of the process for
12 electronic rejects. This analysis addresses the ordering systems (EDI, TAG,
13 and LENS) used by the CLECs and the back-end legacy applications, such
14 as SOCS, that are accessed by the ordering systems.

15

16 Thus far, the analysis has determined that many of the LSRs that did not
17 meet the one-hour benchmark were issued between 11:00 p.m. and 4:30 a.m.
18 Between these hours, the system is unable to process LSRs because certain
19 of the back-end legacy systems are out of service. LSRs submitted during
20 these periods should be excluded from the measurement. BellSouth is
21 currently reviewing the scheduled down time for all systems and how that
22 down time affects the ordering capability of the CLECs.

23

1 With the implementation of May data, BellSouth was directed to change the
2 time stamp identification for the start and complete times of the interval for
3 this measurement from the Local Exchange Ordering (LEO) System to the
4 CLEC ordering interface system (TAG or EDI). However, with this change,
5 BellSouth is currently unable to identify multiple issues of the same version of
6 LSRs that have been rejected (fatal rejects). These rejected LSRs should be
7 excluded from the measurement. If there are multiple issues of the same
8 version, the measure currently calculates the interval from the initial issue to
9 the final issue of the LSR returned to the CLEC, Reject or FOC.
10 Consequently, BellSouth's performance level is inappropriately understated.
11 BellSouth is currently working to determine a fix for this issue.

12

13 FOC Timeliness / Design (Specials) / Partially Electronic (A.1.11.3) (May)
14 There were only three orders in this sub-metric for May 2001 with BellSouth
15 meeting the benchmark for two of them. Such a small universe does not
16 produce a statistically conclusive benchmark comparison. There was no
17 CLEC activity for this sub-metric in June 2001.

18

19 FOC & Reject Response Completeness

20 This measurement was introduced with the March 2001 data month. The
21 benchmark is 95%. BellSouth has determined that the coding for the FOC
22 and Reject Completeness measures is flawed and must be rewritten. In this

1 measure, BellSouth did not meet the benchmark in May and/or June 2001 for
2 the FOC and Reject Response Completeness metrics listed below:

3

4 FOC Reject & Response Completeness / Business / Electronic (A.1.14.2)
5 (May/June)

6 FOC Reject & Response Completeness / Design (Specials) / Electronic
7 (A.1.14.3) (May)

8 FOC Reject & Response Completeness / Business / Manual (A.1.16.2)
9 (May/June)

10 FOC Reject & Response Completeness / Design (Specials) / Manual
11 (A.1.16.3) (May/June)

12 FOC Reject & Response Completeness / PBX / Manual (A.1.16.4) (June)

13 FOC Reject & Response Completeness (Multiple Responses) / Residence /
14 Partially Electronic (A.1.18.1) (May/June)

15 FOC Reject & Response Completeness (Multiple Responses) / Business /
16 Partially Electronic (A.1.18.2) (May/June)

17 FOC Reject & Response Completeness (Multiple Responses) / Design
18 (Specials) / Partially Electronic (A.1.18.3) (May)

19 FOC Reject & Response Completeness (Multiple Responses) / Residence /
20 Manual (A.1.19.1) (May/June)

21 FOC Reject & Response Completeness (Multiple Responses) / Business /
22 Manual (A.1.19.2) (May/June)

1 FOC Reject & Response Completeness (Multiple Responses) / Design
2 (Specials) / Manual (A.1.19.3) (May/June)

3 FOC Reject & Response Completeness (Multiple Responses) / PBX / Manual
4 (A.1.19.4) (May)

5 BellSouth has determined that the coding for the FOC & Reject
6 Completeness measures failed to include rejections that were classified as
7 “auto clarifications.” This coding change, which is in the process of being
8 rewritten, is projected for completion with August data in late September and
9 will impact all FOC & Reject Completeness measures.

10

11 **2. Resale Provisioning Measures**

12

13 For the months of May and June 2001, BellSouth met or exceeded the
14 benchmarks or retail analogues for 75% and 82%, respectively, of all resale
15 provisioning measures. The details supporting the June 2001 percentage are
16 delineated in Items A.2.1.1.1 through A.2.25.3.2.2 of Attachment 1A.

17

18 **Order Completion Interval**

19 As discussed in Checklist Item 4, the failure to properly “L” code appropriate
20 orders and the missed appointments for customer reasons negatively impacts
21 the OCI measurements.

22

1 The testimony of Gustavo E. Bamberger (Bamberger), filed in this
2 proceeding, addresses the effect of LSRs submitted with extended
3 completion intervals and installation appointments missed due to end user
4 reasons. All LSRs seeking extended intervals should receive an "L" code
5 status. This would exclude these LSRs from the OCI measurement.
6 Bamberger examined the order completion data to determine the effect on
7 these measures from both not properly "L" coding these orders and end user
8 appointment misses.

9

10 The following are the measures for which BellSouth did not meet the retail
11 analogue in May and/or June 2001:

12

13 Order Completion Interval / Residence / < 10 Circuits / Non-Dispatch
14 (A.2.1.1.1.2) (May/June)

15 A root cause analysis for OCI for Non-Dispatch orders revealed that
16 BellSouth was offering a 0 to 2-day interval on retail non-dispatched POTS
17 orders, but the wholesale non-dispatched orders were receiving the same
18 interval as "dispatched" orders. BellSouth is currently changing the
19 programming for this function to provide the same intervals as the retail
20 analogue receives.

21

22 The unadjusted order completion interval for May 2001 was 1.09 days
23 compared to the retail analogue of 1.04 days. Table 3A in the Bamberger

1 testimony indicates that with the exclusion of all "L" coded orders and those
2 with end user caused misses would reduce this interval to 0.57 days
3 compared to the retail analogue of 1.03 days. With this adjustment, the sub-
4 metric would exceed the retail analogue for May. The unadjusted order
5 completion interval for June 2001 was 1.07 days compared to the retail
6 analogue of 0.86 days. The exclusion of all "L" coded orders and those with
7 end user caused misses would reduce this interval.

8

9 Order Completion Interval / PBX / < 10 Circuits / Non-Dispatch (A.2.1.4.1.2)
10 (May)

11 There were only seven orders in this sub-metric for May 2001. The small
12 universe for this measurement does not provide a statistically conclusive
13 comparison to the retail analogue. BellSouth met the retail analogue
14 comparison for this sub-metric in June 2001.

15

16 Order Completion Interval / Centrex / < 10 Circuits / Non-Dispatch
17 (A.2.1.5.1.2) (May)

18 There were only three orders in this sub-metric for May 2001. The small
19 universe for this measurement does not provide a statistically conclusive
20 comparison to the retail analogue. BellSouth met the retail analogue
21 comparison for this sub-metric in June 2001.

22

1 Order Completion Interval / ISDN / < 10 Circuits / Non-Dispatch (A.2.1.6.1.2)
2 (May)
3 The unadjusted order completion interval for May 2001 was 6.87 days
4 compared to the retail analogue of 2.62 days. A root cause analysis for OCI
5 for Non-Dispatch orders revealed that BellSouth was offering a 0 to 2-day
6 interval on retail non-dispatched POTS orders, but the wholesale non-
7 dispatched orders were receiving the same interval as "dispatched" orders.
8 BellSouth is currently changing the programming for this function to provide
9 the same intervals as the retail analogue receives. BellSouth met the retail
10 analogue comparison for this sub-metric in June 2001.
11
12 Other resale provisioning sub-metrics for which BellSouth did not meet the
13 benchmark/retail analogue in May and/or June 2001 were:
14
15 % Jeopardy Notice >= 48 hours / Residence / Mechanized (A.2.9.1)
16 (May/June)
17 The calculations for this measure have been determined to be incorrect. The
18 coding change in the Service Order Control System (SOCS) is currently
19 scheduled for a September 13, 2001, system load date. Based on this
20 schedule, the October data month will be the first full month that the change
21 will be in effect.
22

1 % Missed Installation Appointments / Business / < 10 Circuits / Non Dispatch

2 (A.2.11.2.1.2) (May)

3 There were a total of three missed appointments out of the 364 scheduled for
4 this sub-metric in May 2001. Both BellSouth retail and the CLECs had 99%
5 of all scheduled appointments completed on time in May. BellSouth met the
6 retail analogue comparison for this sub-metric in June 2001.

7

8 % Missed Installation Appointments / Business / >= 10 Circuits / Dispatch

9 (A.2.11.2.2.1) May)

10 There were only five orders in this sub-metric for May 2001. The small
11 universe for this measurement does not provide a statistically conclusive
12 comparison with the retail analogue. BellSouth met the retail analogue
13 comparison for this sub-metric in June 2001.

14

15 % Provisioning Troubles w/i 30 days / Residence / < 10 Circuits / Non-
16 Dispatch (A.2.12.1.1.2) (May)

17 Of the 357 reports received for the 10,010 orders that completed in the 30
18 days prior to May 2001 for this sub-metric, 89 were multiple reports for the
19 same orders and should not have been included in the calculation. With the
20 removal of these reports, the sub-metric would have met or exceeded the
21 retail analogue. An update to the measurement was implemented with June
22 data to eliminate this issue. BellSouth met the retail analogue comparison for
23 this sub-metric in June 2001.

1

2 % Provisioning Troubles w/i 30 days / Business / < 10 Circuits / Dispatch
3 (A.2.12.2.1.1) (June)

4 There were 9 trouble reports in this sub-metric for the 122 orders that
5 completed in the 30 days prior to June 2001. These reports did not reveal
6 any distinct patterns or systemic installation issues.

7

8 % Provisioning Troubles w/i 30 days / PBX / < 10 Circuits / Non Dispatch
9 (A.2.12.4.1.2) (May)

10 There was only one trouble report for the ten orders that completed in the 30
11 days prior to May 2001 for this sub-metric. The small universe for this
12 measurement does not provide a statistically conclusive comparison with the
13 retail analogue. BellSouth met the retail analogue comparison for this sub-
14 metric in June 2001.

15

16 Average Completion Notice Interval / Residence / < 10 Circuits / Non
17 Dispatch / Electronic (A.2.14.1.1.2) (May)

18 Average Completion Notice Interval / Business / < 10 Circuits / Non-Dispatch /
19 Electronic (A.2.14.2.1.2) (May/June)

20 Average Completion Notice Interval / Business / >= 10 Circuits / Dispatch /
21 Electronic (A.2.14.2.2.1) (May)

22 The root cause analysis of these measures indicated that the only differences
23 between the performance comparing BellSouth retail and CLECs are the

1 mismatches found when the orders are compared with the original LSRs.
2 The start of the completion interval is the point at which the technician
3 completes the order, and the interval ends when the completion notice is
4 sent. Any change to a name, number of items, etc., occurring during the
5 provisioning process will generate inconsistencies with the original LSRs that
6 must be resolved before a final completion notice can be sent. Any time to
7 resolve these inconsistencies with the original LSRs is included in the
8 average. Because of numerous CLEC changes and order updates,
9 mismatches on CLEC orders exceed those for BellSouth retail orders.
10 Combining this with the smaller base for the CLECs' measurement raises the
11 average, which sometimes results in a miss. Specific Service
12 Representatives within the Work Management Centers have been assigned
13 to resolve any completion issues that are required. Providing specific training
14 and dedicating personnel to this task should reduce the difference between
15 the CLEC and retail analogue results.

16

17 Service Order Accuracy / Residence / < 10 Circuits / Non-Dispatch
18 (A.2.25.1.1.2) (May/June)

19 BellSouth met the standard for 108 of the 115 orders reviewed in this sub-
20 metric for May and 126 of the 136 orders reviewed in June 2001. The 95%
21 benchmark set a requirement of 109 orders in May and 130 orders in June,
22 based on the quantity of orders for this sub-metric. BellSouth continues to

1 focus on this measurement in order to improve results to meet the
2 benchmark.

3

4 Service Order Accuracy / Business / < 10 Circuits / Dispatch (A.2.25.2.1.1)
5 (May/June)

6 BellSouth met the standard for 18 of the 20 orders reviewed in this sub-metric
7 for May and 7 of the 8 orders reviewed in June 2001. The 95% benchmark
8 set a requirement of 19 orders for May and all 8 orders for June, based on the
9 quantity of orders for this sub-metric. BellSouth continues to focus on this
10 measurement in order to improve results to meet the benchmark.

11

12 Service Order Accuracy / Business / < 10 Circuits / Non-Dispatch
13 (A.2.25.2.1.2) (May/June)

14 BellSouth met the standard for 97 of the 103 orders reviewed in this sub-
15 metric for May and for 81 of the 92 orders reviewed for June 2001. The 95%
16 benchmark set a requirement of 98 orders for May and 88 orders for June,
17 based on the quantity of orders for this sub-metric. BellSouth continues to
18 focus on this measurement in order to improve results to meet the
19 benchmark.

20

21 Service Order Accuracy / Business / >= 10 Circuits / Dispatch (A.2.25.2.2.1)
22 (June)

1 Service Order Accuracy / Business / >= 10 Circuits / Non-Dispatch

2 (A.2.25.2.2.2) (June)

3 Service Order Accuracy / Design (Specials) / < 10 Circuits / Non-Dispatch

4 (A.2.25.3.1.2) (June)

5 Service Order Accuracy / Design (Specials) / >= 10 Circuits / Non-Dispatch

6 (A.2.25.3.2.2) (June)

7 Each of these sub-metrics had 3 or fewer service orders reviewed for June
8 2001. Such small universe sizes do not provide conclusive benchmark
9 comparisons. BellSouth continues to focus on this measurement in order to
10 improve results to meet the benchmark.

11

12 Service Order Accuracy / Design (Specials) / < 10 Circuits / Dispatch

13 (A.2.25.3.1.1) (May/June)

14 BellSouth met the standard for 6 of the 8 orders reviewed in this sub-metric
15 for May and 2 of the 3 orders reviewed for June 2001. The 95% benchmark
16 set a requirement of all 8 for May and all 3 for June, based on the quantity of
17 orders for this sub-metric. Such small universe sizes do not provide
18 conclusive benchmark comparisons. BellSouth continues to focus on this
19 measurement in order to improve results to meet the benchmark.

20

21 **3. Resale Maintenance and Repair (M&R) Measures**

22

1 BellSouth met the relevant retail analogues for 90% of all the Resale
2 Maintenance & Repair measurements in both May and June 2001. The sub-
3 metrics for which BellSouth did not meet the retail analogues for May and/or
4 June were:

5

6 % Missed Repair Appointments / Business / Non Dispatch (A.3.1.2.2) (May)

7 BellSouth missed 4 of the 69 appointments scheduled for this sub-metric in
8 May 2001. There were no systemic problems identified for the four missed
9 appointments in May. BellSouth met the retail analogue comparison for this
10 sub-metric in June 2001.

11

12 Customer Trouble Report Rate / Residence / Dispatch (A.3.2.1.1) (May/June)

13 Both BellSouth retail and the CLECs received over 97% trouble free service
14 for all lines in this sub-metric for both May and June 2001. There was less
15 than 0.4% difference each month between the retail analogue and the CLECs
16 report rates. Twenty-one percent of the June reports were closed as "FOK"
17 (no trouble found), which means that the end-user customer experienced
18 minimal trouble levels for these reports. BellSouth will continue to focus on
19 these areas to reduce the reports.

20

21 Customer Trouble Report Rate / Business / Non-Dispatch (A.3.2.2.2) (June)

22 BellSouth provided over 99% trouble free service for both retail and the
23 CLECs for this sub-metric for the month of June 2001. When BellSouth

1 provisions high quality service coupled with very large universe sizes, it can
2 cause an apparent out of equity condition from a quantitative viewpoint. In
3 these cases, there is very little variation and the universe size is so large that
4 the Z-test becomes overly sensitive to any difference. In other words, the
5 statistical test shows that the measurement does not meet the fixed critical
6 value when compared with the retail analogue, but BellSouth's actual
7 performance for both CLECs and its own retail operations is at a very high
8 level – in this case, over 99%. From a practical point of view, the CLECs'
9 ability to compete has not been hindered even though the statistical results
10 may technically show that BellSouth failed to meet the benchmark/analogue.

11

12 Customer Trouble Report Rate / PBX / Dispatch (A.3.2.4.1) (May/June)

13 There were only 4 trouble reports for the 687 in service lines for this sub-
14 metric in May and 2 trouble reports for the 692 in service lines in June 2001.
15 BellSouth provided over 99.4% trouble free service for both retail and the
16 CLECs for this sub-metric for the months of May and June. When BellSouth
17 provisions high quality service coupled with very large universe sizes, it can
18 cause an apparent out of equity condition from a quantitative viewpoint. In
19 these cases, there is very little variation and the universe size is so large that
20 the Z-test becomes overly sensitive to any difference. In other words, the
21 statistical test shows that the measurement does not meet the fixed critical
22 value when compared with the retail analogue, but BellSouth's actual
23 performance for both CLECs and its own retail operations is at a very high

1 level – often 98% or 99%. From a practical point of view, the CLECs' ability
2 to compete has not been hindered even though the statistical results may
3 technically show that BellSouth failed to meet the benchmark/analogue.

4

5 Maintenance Average Duration / PBX / Dispatch (A.3.3.4.1) (June)

6 There were only two orders for this sub-metric in June 2001. Such a small
7 universe for the sub-metric does not provide a statistically conclusive
8 comparison to the retail analogue.

9

10 Maintenance Average Duration / Centrex / Non Dispatch (A.3.3.5.2)

11 There was only one trouble report for this sub-metric in May 2001. The small
12 universe for this measurement does not provide a statistically conclusive
13 comparison with the retail analogue. BellSouth met the retail analogue
14 comparison for this sub-metric in June 2001.

15

16 Out of Service > 24 Hours / Residence / Dispatch (A.3.5.1.1) (June)

17 BellSouth missed this sub-metric in June 2001 due to heavy load activity and
18 repair appointment due date scheduling problems. BellSouth is currently
19 investigating this sub-metric to identify the causes for the high incidence of
20 service outages over 24 hours.

21

22 Out of Service > 24 Hours / Business / Non-Dispatch (A.3.5.2.2) (June)

1 BellSouth cleared 27 of the 29 out-of-service trouble reports in less than 24
2 hours for this sub-metric in June 2001. For the two reports that exceeded 24
3 hours, no distinct pattern or systemic maintenance problems were revealed.

4

5 **V. Summary**

6

7 As stated in the Introduction to the Analysis of Performance Measurements
8 section, BellSouth met or exceeded the criteria for 430 of the 513 sub-metrics
9 (84%) for which there was CLEC activity in June 2001, and which were
10 compared to benchmarks/retail analogues. In May 2001, BellSouth met or
11 exceeded 409 of the 489 sub-metrics (84%) which had CLEC activity.

12

BellSouth Monthly State Summary
Tennessee, June 2001

Resale - Ordering	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z Score	Equity
% Rejected Service Requests - Mechanized									
A.1.1.1 0-7 Residence/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	9.43%	14.820		Diagnostic
A.1.1.2 0-7 Business/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	22.26%	301		Diagnostic
A.1.1.3 0-7 Design Specials/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]				Diagnostic
A.1.1.4 0-7 PEX/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]				Diagnostic
A.1.1.5 0-7 Centrex/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]				Diagnostic
A.1.1.6 0-7 ISDN/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]				Diagnostic
% Rejected Service Requests - Partially Mechanized									
A.1.2.1 0-7 Residence/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	31.33%	2,113		Diagnostic
A.1.2.2 0-7 Business/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	54.39%	285		Diagnostic
A.1.2.3 0-7 Design (Specials)/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	100.00%	1		Diagnostic
A.1.2.4 0-7 PBX/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]				Diagnostic
A.1.2.5 0-7 Centrex/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]				Diagnostic
A.1.2.6 0-7 ISDN/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]				Diagnostic
% Rejected Service Requests - Non-Mechanized									
A.1.3.1 0-7 Residence/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	48.15%	108		Diagnostic
A.1.3.2 0-7 Business/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	38.54%	192		Diagnostic
A.1.3.3 0-7 Design (Specials)/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	22.86%	35		Diagnostic
A.1.3.4 0-7 PBX/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	25.00%	4		Diagnostic
A.1.3.5 0-7 Centrex/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	0.00%	2		Diagnostic
A.1.3.6 0-7 ISDN/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	38.88%	18		Diagnostic
Reject Interval - Mechanized									
A.1.4.1 0-8 Residence/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 97% W in 1 hr			
A.1.4.2 0-8 Business/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 97% W in 1 hr			
A.1.4.3 0-8 Design (Specials)/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 97% W in 1 hr			
A.1.4.4 0-8 PBX/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 97% W in 1 hr			
A.1.4.5 0-8 Centrex/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 97% W in 1 hr			
A.1.4.6 0-8 ISDN/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 97% W in 1 hr			
Reject Interval - Partially Mechanized - 24 hours									
A.1.5.1 0-8 Residence/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
A.1.5.2 0-8 Business/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
A.1.5.3 0-8 Design (Specials)/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
A.1.5.4 0-8 PBX/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
A.1.5.5 0-8 Centrex/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
A.1.5.6 0-8 ISDN/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
Reject Interval - Partially Mechanized - 18 hours									
A.1.6.1 0-8 Residence/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 18 hrs			
A.1.6.2 0-8 Business/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 18 hrs			
A.1.6.3 0-8 Design (Specials)/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 18 hrs			
A.1.6.4 0-8 PBX/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 18 hrs			
A.1.6.5 0-8 Centrex/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 18 hrs			
A.1.6.6 0-8 ISDN/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 18 hrs			
Reject Interval - Non-Mechanized									
A.1.8.1 0-8 Residence/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
A.1.8.2 0-8 Business/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
A.1.8.3 0-8 Design (Specials)/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
A.1.8.4 0-8 PBX/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
A.1.8.5 0-8 Centrex/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
A.1.8.6 0-8 ISDN/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 85% W in 24 hrs			
FOC Timeliness - Mechanized									
O.9 Residence/T(N%)	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	[REDACTED]	>= 95% W in 3 hrs			YES

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		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z Score	Equity
A.1.9.2	O-9	Business/T(N%)								
A.1.9.3	O-9	Design (Specials)/T(N%)								
A.1.9.4	O-9	PBX/T(N%)								
A.1.9.5	O-9	Centrex/T(N%)								
A.1.9.6	O-9	ISDN/T(N%)								
FOC Timeliness - Partially Mechanized										
A.1.10.1	O-9	Residence/T(N%)								
A.1.10.2	O-9	Business/T(N%)								
A.1.10.3	O-9	Design (Specials)/T(N%)								
A.1.10.4	O-9	PBX/T(N%)								
A.1.10.5	O-9	Centrex/T(N%)								
A.1.10.6	O-9	ISDN/T(N%)								
FOC Timeliness - Partially Mechanized - 18 hours										
A.1.11.1	O-9	Residence/T(N%)								
A.1.11.2	O-9	Business/T(N%)								
A.1.11.3	O-9	Design (Specials)/T(N%)								
A.1.11.4	O-9	PBX/T(N%)								
A.1.11.5	O-9	Centrex/T(N%)								
A.1.11.6	O-9	ISDN/T(N%)								
FOC & Reject Response Completeness - Non-Mechanized										
A.1.13.1	O-9	Residence/T(N%)								
A.1.13.2	O-9	Business/T(N%)								
A.1.13.3	O-9	Design (Specials)/T(N%)								
A.1.13.4	O-9	PBX/T(N%)								
A.1.13.5	O-9	Centrex/T(N%)								
A.1.13.6	O-9	ISDN/T(N%)								
FOC & Reject Response Completeness - Mechanized										
A.1.14.1	O-11	Residence/T(N%)								
A.1.14.2	O-11	Business/T(N%)								
A.1.14.3	O-11	Design (Specials)/T(N%)								
A.1.14.4	O-11	PBX/T(N%)								
A.1.14.5	O-11	Centrex/T(N%)								
A.1.14.6	O-11	ISDN/T(N%)								
FOC & Reject Response Completeness - Partially Mechanized										
A.1.15.1	O-11	Residence/T(N%)								
A.1.15.2	O-11	Business/T(N%)								
A.1.15.3	O-11	Design (Specials)/T(N%)								
A.1.15.4	O-11	PBX/T(N%)								
A.1.15.5	O-11	Centrex/T(N%)								
A.1.15.6	O-11	ISDN/T(N%)								
FOC & Reject Response Completeness - Non-Mechanized										
A.1.16.1	O-11	Residence/T(N%)								
A.1.16.2	O-11	Business/T(N%)								
A.1.16.3	O-11	Design (Specials)/T(N%)								
A.1.16.4	O-11	PBX/T(N%)								
A.1.16.5	O-11	Centrex/T(N%)								
A.1.16.6	O-11	ISDN/T(N%)								
FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized										
A.1.17.1	O-11	Residence/T(N%)								
A.1.17.2	O-11	Business/T(N%)								
A.1.17.3	O-11	Design (Specials)/T(N%)								
A.1.17.4	O-11	PBX/T(N%)								
A.1.17.5	O-11	Centrex/T(N%)								
A.1.17.6	O-11	ISDN/T(N%)								

FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized

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		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
O-11	Residence/TN(%)				93.61%	2.113				NO
O-11	Business/TN(%)				89.47%	285				NO
C-11	Design (Specials)/TN(%)				100.00%	1				YES
O-11	PBX/TN(%)									
O-11	Centrex/TN(%)									
O-11	ISDN/TN(%)									
FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized										
A-1.19.1	O-11 Residence/TN(%)				91.03%	145				NO
A-1.19.2	O-11 Business/TN(%)				88.94%	217				NO
A-1.19.3	O-11 Design (Specials)/TN(%)				88.47%	19				NO
A-1.19.4	O-11 PBX/TN(%)				100.00%	2				YES
A-1.19.5	O-11 Centrex/TN(%)				100.00%	1				YES
A-1.19.6	O-11 ISDN/TN(%)				95.65%	23				YES
Resale - Provisioning										
Order Completion Interval										
P-4	P-4 Residence <10 circuits/Dispatch/TN(days)	Res	6.09	13.226	4.98	475	9.722	0.45402	24.527	YES
P-4	P-4 Residence <10 circuits/Non-Dispatch/TN(days)	Res	0.96	252.989	1.07	10.253	1.001	0.01008	-20.4000	NO
P-4	P-4 Residence >=10 circuits/Dispatch/TN(days)	Res	5.29	7			0.469			
P-4	P-4 Residence >=10 circuits/Non-Dispatch/TN(days)	Res	0.78	3			0.346			
P-4	P-4 Business <10 circuits/Dispatch/TN(days)	Bus	3.38	8.012	3.68	76	7.602	0.87617	-0.3382	YES
P-4	P-4 Business <10 circuits/Non-Dispatch/TN(days)	Bus	1.14	14.162	0.88	227	2.015	0.13480	1.9733	YES
P-4	P-4 Business >=10 circuits/Dispatch/TN(days)	Bus	9.85	62	8.33	3	14.609	8.33622	0.1762	YES
P-4	P-4 Business >=10 circuits/Non-Dispatch/TN(days)	Bus	0.82	13	0.33	1	1.183	1.22807	0.3971	YES
P-4	P-4 Design (Specials) <10 circuits/Dispatch/TN(days)	Design	19.46	1.983	4.25	4	28.576	14.30245	0.10632	YES
P-4	P-4 Design (Specials) <10 circuits/Non-Dispatch/TN(days)	Design	7.85	748	0.50	46	13.863	2.10582	3.4920	YES
P-4	P-4 Design (Specials) >=10 circuits/Non-Dispatch/TN(days)	Design	9.60	5			5.910			
P-4	P-4 PBX <10 circuits/Dispatch/TN(days)	PBX	4.00	14	4.00	1	3.103	3.21212	0.0000	YES
P-4	P-4 PBX <10 circuits/Non-Dispatch/TN(days)	PBX	1.37	47			11.121			
P-4	P-4 PBX >=10 circuits/Dispatch/TN(days)	PBX	4.17	142	2.38	7	7.819	3.02723	0.5988	YES
P-4	P-4 PBX >=10 circuits/Non-Dispatch/TN(days)	PBX	1.90	24						
P-4	P-4 Centrex <10 circuits/Dispatch/TN(days)	Centrex	7.05	206			2.921			
P-4	P-4 Centrex <10 circuits/Non-Dispatch/TN(days)	Centrex	3.63	584	4.17	2	10.938			
P-4	P-4 Centrex >=10 circuits/Dispatch/TN(days)	Centrex	17.94	16			6.321	4.47748	-0.1203	YES
P-4	P-4 Centrex >=10 circuits/Non-Dispatch/TN(days)	Centrex	3.70	36			18.598			
P-4	P-4 ISDN <10 circuits/Dispatch/TN(days)	ISDN	22.70	718	10.67	3	4.098	13.77725	0.8736	YES
P-4	P-4 ISDN <10 circuits/Non-Dispatch/TN(days)	ISDN	8.99	482	2.97	13	26.484	7.43806	0.8093	YES
P-4	P-4 ISDN >=10 circuits/Dispatch/TN(days)	ISDN	0.33	1			0.000			
Held Orders										
P-1	P-1 Residence <10 circuits/Facility/TN(days)	Res	9.05	166			6.978			
P-1	P-1 Residence <10 circuits/Equipment/TN(days)	Res	7.63	32			12.333			
P-1	P-1 Residence >=10 circuits/Other/TN(days)	Res								
P-1	P-1 Residence >=10 circuits/Equipment/TN(days)	Res								
P-1	P-1 Residence >=10 circuits/Other/Facility/TN(days)	Res								
P-1	P-1 Business <10 circuits/Equipment/TN(days)	Bus	10.00	35	10.00	1	11.259	11.41871	0.0000	YES
P-1	P-1 Business <10 circuits/Other/Facility/TN(days)	Bus	32.00	2	4.00	1	41.012	50.22948	0.5574	YES
P-1	P-1 Business >=10 circuits/Equipment/TN(days)	Bus								
P-1	P-1 Business >=10 circuits/Other/Facility/TN(days)	Bus								
P-1	P-1 Design (Specials) <10 circuits/Facility/TN(days)	Design	10.89	9			8.448			
P-1	P-1 Design (Specials) <10 circuits/Equipment/TN(days)	Design	114.93	15			133.002			
P-1	P-1 Design (Specials) >=10 circuits/Facility/TN(days)	Design								

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A.2.23.22	P-1	Design (Specialist) />= 10 circuits/Equipment/TN(days)
A.2.23.23	P-1	Design (Specialist) />= 10 circuits/Other/TN(days)
A.2.24.1.1	P-1	PBX /< 10 circuits/Facility/TN(days)
A.2.24.1.2	P-1	PBX /< 10 circuits/Equipment/TN(days)
A.2.24.1.3	P-1	PBX /< 10 circuits/Other/TN(days)
A.2.24.2.1	P-1	PBX />= 10 circuits/Facility/TN(days)
A.2.24.2.2	P-1	PBX />= 10 circuits/Equipment/TN(days)
A.2.24.2.3	P-1	PBX />= 10 circuits/Other/TN(days)
A.2.25.1.1	P-1	Centrex /< 10 circuits/Facility/TN(days)
A.2.25.1.2	P-1	Centrex /< 10 circuits/Equipment/TN(days)
A.2.25.1.3	P-1	Centrex /< 10 circuits/Other/TN(days)
A.2.25.2.1	P-1	Centrex />= 10 circuits/Facility/TN(days)
A.2.25.2.2	P-1	Centrex />= 10 circuits/Equipment/TN(days)
A.2.25.2.3	P-1	Centrex />= 10 circuits/Other/TN(days)
A.2.26.1.1	P-1	ISDN /< 10 circuits/Facility/TN(days)
A.2.26.1.2	P-1	ISDN /< 10 circuits/Equipment/TN(days)
A.2.26.1.3	P-1	ISDN /< 10 circuits/Other/TN(days)
A.2.26.2.1	P-1	ISDN />= 10 circuits/Facility/TN(days)
A.2.26.2.2	P-1	ISDN />= 10 circuits/Equipment/TN(days)
A.2.26.2.3	P-1	ISDN />= 10 circuits/Other/TN(days)

% Jeopardies - Mechanized

P-2	Residence/TN(%)
P-2	Business/TN(%)
P-2	Design (Specialist)/TN(%)
P-2	PBX/TN(%)
P-2	Centrex/TN(%)
P-2	ISDN/TN(%)

Average Jeopardy Notice Interval - Mechanized

A.2.5.1	P-2	Residence/TN(%)
A.2.5.2	P-2	Business/TN(%)
A.2.5.3	P-2	Design (Specialist)/TN(%)
A.2.5.4	P-2	PBX/TN(%)
A.2.5.5	P-2	Centrex/TN(%)
A.2.5.6	P-2	ISDN/TN(%)

Average Jeopardy Notice Interval - Non-Mechanized

A.2.7.1	P-2	Residence/TN(%)
A.2.7.2	P-2	Business/TN(hours)
A.2.7.3	P-2	Design (Specialist)/TN(hours)
A.2.7.4	P-2	PBX/TN(hours)
A.2.7.5	P-2	Centrex/TN(hours)
A.2.7.6	P-2	ISDN/TN(hours)

% Jeopardy Notice > 48 hours - Mechanized

A.2.8.1	P-2	Residence/TN(%)
A.2.8.2	P-2	Business/TN(%)
A.2.8.3	P-2	Design (Specialist)/TN(%)
A.2.8.4	P-2	PBX/TN(hours)
A.2.8.5	P-2	Centrex/TN(hours)
A.2.8.6	P-2	ISDN/TN(hours)

% Jeopardy Notice > 48 hours - Non-Mechanized

A.2.9.1	P-2	Residence/TN(%)
A.2.9.2	P-2	Business/TN(%)
A.2.9.3	P-2	Design (Specialist)/TN(%)
A.2.9.4	P-2	PBX/TN(%)
A.2.9.5	P-2	Centrex/TN(%)
A.2.9.6	P-2	ISDN/TN(%)

% Jeopardy Notice > 48 hours - Non-Mechanized

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				BST	Measure	BST	Volume	CLEC	CLEC	Standard	Error	Standard	Deviation	Z-Score	Equity	
P-2	Residence/(N%)							92.31%	26							
P-2	Business/(N%)							100.00%	3							
A.2.10.1																
A.2.10.2																
A.2.10.3																
A.2.10.4																
A.2.10.5																
A.2.10.6																
<i>% Missed Installation Appointments</i>																
A.2.11.1.1	Residence<10 circuits/Dispatch/(N%)	P-3	Residence<10 circuits/Non-Dispatch/(N%)					15.854	6.61%	510	0.01300	1.9459	YES			
A.2.11.1.2	Business<10 circuits/Dispatch/(N%)	P-3	Business<10 circuits/Non-Dispatch/(N%)					264.563	0.08%	10,982	0.00921	-1.6444	YES			
A.2.11.1.2.1	Residence>=10 circuits/Dispatch/(N%)	P-3	Residence>=10 circuits/Non-Dispatch/(N%)					9.09%	11							
A.2.11.1.2.2	Business<10 circuits/Dispatch/(N%)	P-3	Business<10 circuits/Non-Dispatch/(N%)					0.00%	3							
A.2.11.1.2.1.1	Business<10 circuits/Dispatch/(N%)	P-3	Business<10 circuits/Non-Dispatch/(N%)					2.96%	8,271	3.57%	112	0.01610	-0.3860	YES		
A.2.11.1.2.1.2	Business>=10 circuits/Dispatch/(N%)	P-3	Business>=10 circuits/Non-Dispatch/(N%)					0.16%	14,242	0.00%	307	0.00241	0.7269	YES		
A.2.11.2.1	Residence<10 circuits/Dispatch/(N%)	P-3	Residence<10 circuits/Non-Dispatch/(N%)					12.50%	80	33.33%	3					
A.2.11.2.2	Business>=10 circuits/Non-Dispatch/(N%)	P-3	Business>=10 circuits/Non-Dispatch/(N%)					0.00%	13	0.00%	1	0.19449	-1.0712	YES		
A.2.11.3.1.1	Design (Specials)<10 circuits/Dispatch/(N%)	P-3	Design (Specials)<10 circuits/Dispatch/(N%)					4.31%	1,990	0.00%	7	0.00900	0.5647	YES		
A.2.11.3.1.2	Design (Specials)<10 circuits/Non-Dispatch/(N%)	P-3	Design (Specials)<10 circuits/Non-Dispatch/(N%)					0.27%	753	0.00%	46	0.00782	0.3398	YES		
A.2.11.3.2.1	Design (Specials)>=10 circuits/Non-Dispatch/(N%)	P-3	Design (Specials)>=10 circuits/Non-Dispatch/(N%)					16.67%	6							
A.2.11.3.2.2	Design (Specials)>>10 circuits/Non-Dispatch/(N%)	P-3	Design (Specials)>>10 circuits/Non-Dispatch/(N%)					0.00%	14	0.00%	1	0.00000	YES			
A.2.11.4.1.1	PBX<10 circuits/Dispatch/(N%)	P-3	PBX<10 circuits/Non-Dispatch/(N%)					10.20%	49							
A.2.11.4.1.2	PBX<10 circuits/Non-Dispatch/(N%)	P-3	PBX<10 circuits/Non-Dispatch/(N%)					0.68%	147	0.00%	7	0.03180	0.2139	YES		
A.2.11.4.2.1	PBX>=10 circuits/Dispatch/(N%)	P-3	PBX>=10 circuits/Dispatch/(N%)					0.00%	29							
A.2.11.4.2.2	PBX>=10 circuits/Non-Dispatch/(N%)	P-3	PBX>=10 circuits/Non-Dispatch/(N%)					3.74%	214							
A.2.11.5.1.1	Centrex<10 circuits/Dispatch/(N%)	P-3	Centrex<10 circuits/Dispatch/(N%)					0.17%	588	0.00%	2	0.02919	0.0583	YES		
A.2.11.5.1.2	Centrex>=10 circuits/Non-Dispatch/(N%)	P-3	Centrex>=10 circuits/Non-Dispatch/(N%)					0.00%	16							
A.2.11.5.2.1	PBX<10 circuits/Dispatch/(N%)	P-3	PBX<10 circuits/Dispatch/(N%)					0.00%	39							
A.2.11.5.2.2	PBX>=10 circuits/Non-Dispatch/(N%)	P-3	PBX>=10 circuits/Non-Dispatch/(N%)					7.87%	730	0.00%	4	0.13343	0.5749	YES		
A.2.11.6.1.1	ISDN<10 circuits/Dispatch/(N%)	P-3	ISDN<10 circuits/Dispatch/(N%)					0.61%	494	0.00%	16	0.01973	0.3077	YES		
A.2.11.6.1.2	ISDN>=10 circuits/Dispatch/(N%)	P-3	ISDN>=10 circuits/Dispatch/(N%)					0.00%	1							
A.2.11.6.2.1	ISDN>=10 circuits/Non-Dispatch/(N%)	P-3	ISDN>=10 circuits/Non-Dispatch/(N%)													
<i>% Provisioning Troubles within 30 Days</i>																
A.2.12.1.1.1	Residence<10 circuits/Dispatch/(N%)	P-9	Residence<10 circuits/Non-Dispatch/(N%)					8.66%	15,932	10.71%	448	0.01347	-1.5284	YES		
A.2.12.1.2.1	Residence>=10 circuits/Dispatch/(N%)	P-9	Residence>=10 circuits/Non-Dispatch/(N%)					3.52%	256,502	3.67%	11,771	0.00174	-0.8918	YES		
A.2.12.1.2.2	Residence>>10 circuits/Non-Dispatch/(N%)	P-9	Residence>>10 circuits/Non-Dispatch/(N%)					15.38%	13							
A.2.12.2.1.1	Business<10 circuits/Dispatch/(N%)	P-9	Business<10 circuits/Non-Dispatch/(N%)					4.17%	9,039	7.38%	122	0.01822	-1.7596	NO		
A.2.12.2.1.2	Business>=10 circuits/Dispatch/(N%)	P-9	Business>=10 circuits/Non-Dispatch/(N%)					3.39%	14,861	3.85%	384	0.01031	0.0811	YES		
A.2.12.2.2.1	Business>=10 circuits/Non-Dispatch/(N%)	P-9	Business>=10 circuits/Non-Dispatch/(N%)					6.98%	86	0.00%	5	0.11719	0.5953	YES		
A.2.12.3.1.1	Design (Specials)<10 circuits/Dispatch/(N%)	P-9	Design (Specials)>=10 circuits/Dispatch/(N%)					0.00%	43							
A.2.12.3.1.2	Design (Specials)>>10 circuits/Non-Dispatch/(N%)	P-9	Design (Specials)>>10 circuits/Non-Dispatch/(N%)					0.00%	44	0.00%	3	0.00000	YES			
A.2.12.3.2.1	Design (Specials)>=10 circuits/Dispatch/(N%)	P-9	Design (Specials)>=10 circuits/Non-Dispatch/(N%)					0.00%	1							
A.2.12.4.1.1	PBX<10 circuits/Dispatch/(N%)	P-9	PBX<10 circuits/Non-Dispatch/(N%)					1.61%	62							
A.2.12.4.1.2	PBX>=10 circuits/Non-Dispatch/(N%)	P-9	PBX>=10 circuits/Dispatch/(N%)					0.00%	250	0.00%	7	0.00000	YES			
A.2.12.4.2.1	PBX>>10 circuits/Dispatch/(N%)	P-9	PBX>>10 circuits/Non-Dispatch/(N%)					0.00%	36							
A.2.12.5.1.1	Centrex<10 circuits/Dispatch/(N%)	P-9	Centrex<10 circuits/Non-Dispatch/(N%)					1.70%	235							
A.2.12.5.1.2	Centrex>=10 circuits/Dispatch/(N%)	P-9	Centrex>=10 circuits/Non-Dispatch/(N%)					0.99%	783	0.00%	4	0.04718	0.1895	YES		
A.2.12.5.2.1	Centrex>>10 circuits/Dispatch/(N%)	P-9	Centrex>>10 circuits/Non-Dispatch/(N%)					5.99%	17							
A.2.12.5.2.2	Centrex>>>10 circuits/Non-Dispatch/(N%)	P-9	Centrex>>>10 circuits/Non-Dispatch/(N%)					0.00%	52							
A.2.12.6.1.1	ISDN<10 circuits/Dispatch/(N%)	P-9	ISDN<10 circuits/Non-Dispatch/(N%)					0.14%	694	0.00%	3	0.02195	0.0657	YES		
A.2.12.6.1.2	ISDN>=10 circuits/Dispatch/(N%)	P-9	ISDN>=10 circuits/Non-Dispatch/(N%)					0.21%	480	0.00%	17	0.01125	0.1851	YES		
A.2.12.6.2.1	ISDN>=10 circuits/Non-Dispatch/(N%)	P-9	ISDN>=10 circuits/Non-Dispatch/(N%)													
<i>Average Completion Notice Interval - Mechanized</i>																
A.2.14.1.1	Residence<10 circuits/Dispatch/(N hours)	P-5	Residence<10 circuits/Dispatch/(N hours)					1.29	11,450	2.03	500	10.340	0.47239	-1.5598	YES	

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		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
P-5	Residence<10 circuits/Non-Dispatch/TN(hours)	Res	69	214,358	1,43	9,533	8,548	0,08948	2,9590	YES
A 2.14.1.1.2	Residence>=10 circuits/Non-Dispatch/TN(hours)	Res	09	9			0,236			
A 2.14.1.2.1	P-5 Residence>=10 circuits/Non-Dispatch/TN(hours)	Res	45	2			0,071			
A 2.14.1.2.2	P-5 Business<10 circuits/Dispatch/TN(hours)	Bus	5,23	3,332	10,01	93	115,514	12,14426	-0,3941	YES
P-5	P-5 Business<10 circuits/Non-Dispatch/TN(hours)	Bus	1,59	10,553	7,75	164	8,566	0,70555	-8,7184	NO
A 2.14.2.1.2	P-5 Business>=10 circuits/Dispatch/TN(hours)	Bus	69	43	0,05	4	7,190	3,75844	0,4376	YES
A 2.14.2.2.1	P-5 Business<10 circuits/Non-Dispatch/TN(hours)	Bus	0,74	13			0,218			
P-5	P-5 Design (Specials)<10 circuits/Dispatch/TN(hours)	Design	151,58	1,48			6,75,417			
A 2.14.3.1.1	P-5 Design (Specials)<10 circuits/Non-Dispatch/TN(hours)	Design	10,17	286	8,89	27	83,014	16,71310	0,0766	YES
A 2.14.3.1.2	P-5 Design (Specials)>=10 circuits/Dispatch/TN(hours)	Design	82,86	3			143,495			
P-5	P-5 Design (Specials)>=10 circuits/Non-Dispatch/TN(hours)	Design	0,74	10			0,252			
A 2.14.3.2.1	PBX<10 circuits/Dispatch/TN(hours)	PBX	29,06	28			47,683			
A 2.14.3.2.2	PBX<10 circuits/Non-Dispatch/TN(hours)	PBX	32,59	94	0,43	2	182,626	130,50271	0,2472	YES
P-5	PBX>>10 circuits/Non-Dispatch/TN(hours)	PBX	0,68	28			0,242			
A 2.14.4.2.2	PBX>>10 circuits/Non-Dispatch/TN(hours)	PBX	13,26	143			49,097			
P-5	Centrex<10 circuits/Dispatch/TN(hours)	Centrex	6,75	311			34,548			
A 2.14.5.1.2	P-5 Centrex<10 circuits/Non-Dispatch/TN(hours)	Centrex	2,43	8			6,587			
A 2.14.5.2.1	P-5 Centrex>=10 circuits/Dispatch/TN(hours)	Centrex	7,23	33			29,933			
A 2.14.5.2.2	P-5 Centrex>=10 circuits/Non-Dispatch/TN(hours)	ISDN	10,69	529			500,609			
A 2.14.6.1.1	P-5 ISDN<10 circuits/Non-Dispatch/TN(hours)	ISDN	19,19	407	0,73	1	567,784	568,48130	0,3333	YES
A 2.14.6.1.2	P-5 ISDN>>10 circuits/Non-Dispatch/TN(hours)	ISDN	0,73	1			0,000			
P-5	ISDN>=10 circuits/Non-Dispatch/TN(hours)	ISDN								
A 2.14.6.2.2	ISDN>=10 circuits/Non-Dispatch/TN(hours)									
A 2.14.6.2.2	ISDN>>10 circuits/Non-Dispatch/TN(hours)									
Average Completion Notice Interval - Non-Mechanized										
P-5	P-5 Residence<10 circuits/Non-Dispatch/TN(hours)						18,85			
A 2.15.1.1.2	P-5 Residence<10 circuits/Non-Dispatch/TN(hours)						14,16			
P-5	P-5 Residence>=10 circuits/Non-Dispatch/TN(hours)						43			
A 2.15.1.2.2	P-5 Business<10 circuits/Non-Dispatch/TN(hours)									
P-5	P-5 Business>=10 circuits/Non-Dispatch/TN(hours)									
A 2.15.2.1.1	P-5 Centrex<10 circuits/Dispatch/TN(hours)									
P-5	P-5 Business<10 circuits/Non-Dispatch/TN(hours)									
A 2.15.2.1.2	P-5 Business>=10 circuits/Non-Dispatch/TN(hours)									
P-5	P-5 Business>>10 circuits/Non-Dispatch/TN(hours)									
A 2.15.2.2.1	P-5 Business>>10 circuits/Non-Dispatch/TN(hours)									
P-5	P-5 Business>>10 circuits/Non-Dispatch/TN(hours)									
A 2.15.3.1.1	P-5 Design (Specials)<10 circuits/Dispatch/TN(hours)									
P-5	P-5 Design (Specials)>=10 circuits/Non-Dispatch/TN(hours)									
A 2.15.3.2.1	P-5 Design (Specials)>=10 circuits/Dispatch/TN(hours)									
P-5	P-5 Design (Specials)>>10 circuits/Non-Dispatch/TN(hours)									
A 2.15.3.2.2	P-5 Centrex<10 circuits/Dispatch/TN(hours)									
P-5	P-5 Centrex>=10 circuits/Non-Dispatch/TN(hours)									
A 2.15.4.1.1	P-5 Centrex>>10 circuits/Non-Dispatch/TN(hours)									
P-5	P-5 PBX<10 circuits/Non-Dispatch/TN(hours)									
A 2.15.4.2.1	P-5 PBX>=10 circuits/Dispatch/TN(hours)									
P-5	P-5 PBX>>10 circuits/Non-Dispatch/TN(hours)									
A 2.15.5.1.1	P-5 Centrex<10 circuits/Non-Dispatch/TN(hours)									
P-5	P-5 Centrex>=10 circuits/Non-Dispatch/TN(hours)									
A 2.15.5.1.2	P-5 Centrex>>10 circuits/Non-Dispatch/TN(hours)									
P-5	P-5 Centrex>=10 circuits/Non-Dispatch/TN(days)									
A 2.15.5.2.1	P-5 Centrex>=10 circuits/Non-Dispatch/TN(days)									
P-5	P-5 ISDN<10 circuits/Non-Dispatch/TN(days)									
A 2.15.6.1.1	P-5 ISDN>>10 circuits/Non-Dispatch/TN(days)									
P-5	P-5 ISDN>=10 circuits/Non-Dispatch/TN(days)									
A 2.15.6.2.1	P-5 ISDN>>10 circuits/Non-Dispatch/TN(days)									
P-5	P-5 ISDN>=10 circuits/Non-Dispatch/TN(days)									
A 2.15.6.2.2	P-5 ISDN>>10 circuits/Non-Dispatch/TN(days)									
Total Service Order Cycle Time - Mechanized										
A 2.17.1.1.1	P-10 Residence<10 circuits/Dispatch/TN(days)						5,34			
P-10	P-10 Residence<10 circuits/Non-Dispatch/TN(days)						1,09			
A 2.17.1.2.1	P-10 Residence>=10 circuits/Dispatch/TN(days)						8,760			
A 2.17.12.2	P-10 Residence>=10 circuits/Non-Dispatch/TN(days)									
P-10	P-10 Business<10 circuits/Dispatch/TN(days)									
A 2.17.2.1.1	P-10 Business>=10 circuits/Non-Dispatch/TN(days)									
A 2.17.2.2.2	P-10 Business>>10 circuits/Dispatch/TN(days)									
P-10	P-10 Business>=10 circuits/Non-Dispatch/TN(days)									
A 2.17.2.2.2	P-10 Business>>10 circuits/Non-Dispatch/TN(days)									
A 2.17.3.1.1	P-10 Design (Specials)<10 circuits/Dispatch/TN(days)									

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	P-10	P-10	P-10	P-10	P-10	P-10	P-10	P-10
A.2.17.3.1.2	Design (Specials)<10 circuits/Non-Dispatch/N(days)							Diagnostic
A.2.17.3.2.1	Design (Specials)>=10 circuits/Non-Dispatch/N(days)							Diagnostic
A.2.17.3.2.2	Design (Specials)>=10 circuits/Non-Dispatch/N(days)							Diagnostic
A.2.17.4.1.1	PBX<10 circuits/Non-Dispatch/N(days)							Diagnostic
A.2.17.4.1.2	PBX<10 circuits/Non-Dispatch/N(days)							Diagnostic
A.2.17.4.2.1	PBX>=10 circuits/Dispatch/N(days)							Diagnostic
A.2.17.4.2.2	PBX>=10 circuits/Dispatch/N(days)							Diagnostic
A.2.17.5.1.1	Centrex<10 circuits/Dispatch/N(days)							Diagnostic
A.2.17.5.1.2	Centrex<10 circuits/Non-Dispatch/N(days)							Diagnostic
A.2.17.5.2.1	P-10	Centrex>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.17.5.2.2	P-10	Centrex>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.17.6.1.1	P-10	ISDN<10 circuits/Dispatch/N(days)						Diagnostic
A.2.17.6.1.2	P-10	ISDN<10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.17.6.2.1	P-10	ISDN>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.17.6.2.2	P-10	ISDN>=10 circuits/Non-Dispatch/N(days)						Diagnostic
Total Service Order Cycle Time - Partially Mechanized								
A.2.18.1.1.1	P-10	Residence<10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.1.1.2	P-10	Residence<10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.18.1.2.1	P-10	Residence>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.1.2.2	P-10	Residence>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.18.2.1.1	P-10	Business<10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.2.1.2	P-10	Business<10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.18.2.2.1	P-10	Business>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.2.2.2	P-10	Business>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.18.3.1.1	P-10	Design (Specials)<10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.3.1.2	P-10	Design (Specials)>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.18.3.2.1	P-10	Design (Specials)>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.3.2.2	P-10	Design (Specials)>>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.18.4.1.1	P-10	PBX<10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.4.1.2	P-10	PBX><10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.18.4.2.1	P-10	PBX>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.4.2.2	P-10	PBX>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.18.5.1.1	P-10	Centrex<10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.5.1.2	P-10	Centrex<10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.18.5.2.1	P-10	Centrex>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.5.2.2	P-10	Centrex>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.18.6.1.1	P-10	ISDN<10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.6.1.2	P-10	ISDN<10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.18.6.2.1	P-10	ISDN>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.18.6.2.2	P-10	ISDN>=10 circuits/Non-Dispatch/N(days)						Diagnostic
Total Service Order Cycle Time - Non-Mechanized								
A.2.19.1.1.1	P-10	Residence<10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.1.1.2	P-10	Residence<10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.1.2.1	P-10	Residence>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.19.1.2.2	P-10	Residence>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.2.1.1	P-10	Business<10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.2.1.2	P-10	Business<10 circuits/Dispatch/N(days)						Diagnostic
A.2.19.2.2.1	P-10	Business>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.2.2.2	P-10	Business>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.19.2.2.3	P-10	Business>>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.2.2.4	P-10	Business>>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.19.3.1.1	P-10	Design (Specials)<10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.3.1.2	P-10	Design (Specials)>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.19.3.2.1	P-10	Design (Specials)>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.3.2.2	P-10	Design (Specials)>>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.4.1.1	P-10	PBX<10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.4.1.2	P-10	PBX>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.4.2.1	P-10	PBX>=10 circuits/Dispatch/N(days)						Diagnostic
A.2.19.4.2.2	P-10	PBX>>=10 circuits/Non-Dispatch/N(days)						Diagnostic
A.2.19.5.1.1	P-10	Centrex<10 circuits/Non-Dispatch/N(days)						Diagnostic

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	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
A.2.19.5.1.2	P-10	Centrex/<10 circuits/Non-Dispatch/N(days)				1.00	1		
A.2.19.5.2.1	P-10	Centrex/>=10 circuits/Dispatch/N(days)							
A.2.19.5.2.2	P-10	Centrex/>10 circuits/Non-Dispatch/N(days)							
A.2.19.6.1.1	P-10	ISDN/<10 circuits/Non-Dispatch/N(days)				30.00	1		
A.2.19.6.1.2	P-10	ISDN/<10 circuits/Non-Dispatch/N(days)				7.80	10		
A.2.19.6.2.1	P-10	ISDN/>=10 circuits/Dispatch/N(days)							
A.2.19.6.2.2	P-10	ISDN/>=10 circuits/Non-Dispatch/N(days)							
Total Service Order Cycle Time (offered) - Mechanized									
A.2.21.1.1.1	P-10	Residence/<10 circuits/Dispatch/N(days)							
A.2.21.1.1.2	P-10	Residence/>=10 circuits/Non-Dispatch/N(days)							
A.2.21.1.2.1	P-10	Residence/>=10 circuits/Non-Dispatch/N(days)							
A.2.21.1.2.2	P-10	Business/<10 circuits/Dispatch/N(days)							
A.2.21.1.2.1.2	P-10	Business/<10 circuits/Non-Dispatch/N(days)							
A.2.21.2.2.1	P-10	Business/>=10 circuits/Dispatch/N(days)							
A.2.21.2.2.2	P-10	Business/>=10 circuits/Non-Dispatch/N(days)							
A.2.21.3.1.1	P-10	Design (Specials)/<10 circuits/Dispatch/N(days)							
A.2.21.3.1.2	P-10	Design (Specials)/>10 circuits/Non-Dispatch/N(days)							
A.2.21.3.2.1	P-10	Design (Specials)/>10 circuits/Dispatch/N(days)							
A.2.21.3.2.2	P-10	Design (Specials)/>10 circuits/Non-Dispatch/N(days)							
A.2.21.4.1.1	P-10	PBX/<10 circuits/Dispatch/N(days)							
A.2.21.4.1.2	P-10	PBX/<10 circuits/Non-Dispatch/N(days)							
A.2.21.4.2.1	P-10	PBX/>=10 circuits/Dispatch/N(days)							
A.2.21.4.2.2	P-10	PBX/>=10 circuits/Non-Dispatch/N(days)							
A.2.21.5.1.1	P-10	Centrex/<10 circuits/Dispatch/N(days)							
A.2.21.5.1.2	P-10	Centrex/>=10 circuits/Non-Dispatch/N(days)							
A.2.21.5.2.1	P-10	Centrex/>=10 circuits/Dispatch/N(days)							
A.2.21.5.2.2	P-10	Centrex/>=10 circuits/Non-Dispatch/N(days)							
A.2.21.6.1.1	P-10	ISDN/<10 circuits/Dispatch/N(days)							
A.2.21.6.1.2	P-10	ISDN/<10 circuits/Non-Dispatch/N(days)							
A.2.21.6.2.1	P-10	ISDN/>=10 circuits/Dispatch/N(days)							
A.2.21.6.2.2	P-10	ISDN/>=10 circuits/Non-Dispatch/N(days)							
Total Service Order Cycle Time (offered) - Partially Mechanized									
A.2.22.1.1.1	P-10	Residence/<10 circuits/Dispatch/N(days)							
A.2.22.1.1.2	P-10	Residence/>=10 circuits/Non-Dispatch/N(days)							
A.2.22.1.2.1	P-10	Residence/>=10 circuits/Non-Dispatch/N(days)							
A.2.22.1.2.2	P-10	Business/<10 circuits/Dispatch/N(days)							
A.2.22.1.2.1.1	P-10	Business/<10 circuits/Non-Dispatch/N(days)							
A.2.22.2.2.1	P-10	Business/>=10 circuits/Dispatch/N(days)							
A.2.22.2.2.2	P-10	Business/>=10 circuits/Non-Dispatch/N(days)							
A.2.22.3.1.1	P-10	Design (Specials)/<10 circuits/Non-Dispatch/N(days)							
A.2.22.3.1.2	P-10	Design (Specials)/>10 circuits/Non-Dispatch/N(days)							
A.2.22.3.2.1	P-10	Design (Specials)/>10 circuits/Dispatch/N(days)							
A.2.22.5.2.1	P-10	Centrex/<10 circuits/Dispatch/N(days)							
A.2.22.5.2.2	P-10	Centrex/>=10 circuits/Dispatch/N(days)							
A.2.22.6.1.1	P-10	ISDN/<10 circuits/Dispatch/N(days)							
A.2.22.6.1.2	P-10	ISDN/<10 circuits/Non-Dispatch/N(days)							
A.2.22.6.2.1	P-10	ISDN/>=10 circuits/Dispatch/N(days)							
A.2.22.6.2.2	P-10	ISDN/>=10 circuits/Non-Dispatch/N(days)							
Total Service Order Cycle Time (offered) - Non-Mechanized									

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	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z Score	Equity
A.2.23.1.1	P-10 Residence<10 circuits/Dispatch/TN(days)			5.00	20				Diagnostic
A.2.23.1.2	P-10 Residence<10 circuits/Non-Dispatch/TN(days)			3.36	33				Diagnostic
A.2.23.1.2.1	P-10 Residence<=10 circuits/Dispatch/TN(days)								Diagnostic
A.2.23.1.2.2	P-10 Residence<=10 circuits/Non-Dispatch/TN(days)								Diagnostic
A.2.23.2.1.1	P-10 Business<10 circuits/Dispatch/TN(days)			4.50	2				Diagnostic
A.2.23.2.1.2	P-10 Business<10 circuits/Non-Dispatch/TN(days)			2.79	57				Diagnostic
A.2.23.2.2.1	P-10 Business<=10 circuits/Dispatch/TN(days)								Diagnostic
A.2.23.2.2.2	P-10 Business<=10 circuits/Non-Dispatch/TN(days)								Diagnostic
A.2.23.3.1.1	P-10 Design (Specials)<10 circuits/Dispatch/TN(days)			6.00	1				Diagnostic
A.2.23.3.1.2	P-10 Design (Specials)<10 circuits/Non-Dispatch/TN(days)			3.00	5				Diagnostic
A.2.23.3.2.1	P-10 Design (Specials)>=10 circuits/Dispatch/TN(days)								Diagnostic
A.2.23.3.2.2	P-10 Design (Specials)>=10 circuits/Non-Dispatch/TN(days)								Diagnostic
A.2.23.4.1.1	P-10 PBX<10 circuits/Dispatch/TN(days)			7.67	3				Diagnostic
A.2.23.4.1.2	P-10 PBX<=10 circuits/Non-Dispatch/TN(days)								Diagnostic
A.2.23.4.2.1	P-10 PBX>=10 circuits/Dispatch/TN(days)								Diagnostic
A.2.23.4.2.2	P-10 PBX>=10 circuits/Non-Dispatch/TN(days)								Diagnostic
A.2.23.5.1.1	P-10 Centrex<10 circuits/Dispatch/TN(days)								Diagnostic
A.2.23.5.1.2	P-10 Centrex<10 circuits/Non-Dispatch/TN(days)			1.00	1				Diagnostic
A.2.23.5.2.1	P-10 Centrex>=10 circuits/Dispatch/TN(days)								Diagnostic
A.2.23.5.2.2	P-10 Centrex>=10 circuits/Non-Dispatch/TN(days)								Diagnostic
A.2.23.6.1.1	P-10 ISDN<10 circuits/Dispatch/TN(days)			30.00	1				Diagnostic
A.2.23.6.1.2	P-10 ISDN<10 circuits/Non-Dispatch/TN(days)			7.80	10				Diagnostic
A.2.23.6.2.1	P-10 ISDN>=10 circuits/Dispatch/TN(days)								Diagnostic
A.2.23.6.2.2	P-10 ISDN>=10 circuits/Non-Dispatch/TN(days)			0.00%	1				Diagnostic
Service Order Accuracy									
A.2.25.1.1.1	P-11 Residence<10 circuits/Dispatch/TN(%)								= 95%
A.2.25.1.1.2	P-11 Residence<10 circuits/Non-Dispatch/TN(%)								= 95%
A.2.25.1.2.1	P-11 Residence>=10 circuits/Dispatch/TN(%)								= 95%
A.2.25.1.2.2	P-11 Residence>=10 circuits/Non-Dispatch/TN(%)								= 95%
A.2.25.2.1.1	P-11 Business<10 circuits/Dispatch/TN(%)								= 95%
A.2.25.2.1.2	P-11 Business<10 circuits/Non-Dispatch/TN(%)								= 95%
A.2.25.2.2.1	P-11 Business>=10 circuits/Dispatch/TN(%)								= 95%
A.2.25.2.2.2	P-11 Business>=10 circuits/Non-Dispatch/TN(%)								= 95%
A.2.25.3.1.1	P-11 Design (Specials)<10 circuits/Dispatch/TN(%)								= 95%
A.2.25.3.1.2	P-11 Design (Specials)<10 circuits/Non-Dispatch/TN(%)								= 95%
A.2.25.3.2.1	P-11 Design (Specials)>=10 circuits/Dispatch/TN(%)								= 95%
A.2.25.3.2.2	P-11 Design (Specials)>=10 circuits/Non-Dispatch/TN(%)								= 95%
Results - Maintenance and Repair									
Missed Repair Appointments									
M&R-1	Residence/Dispatch/TN(%)			6.88%	45.325	5.53%	994	0.00812	1.6611
M&R-1	Residence/Non-Dispatch/TN(%)			1.51%	25.447	0.67%	299	0.00709	1.1847
M&R-1	Business/Dispatch/TN(%)			7.59%	6.933	6.34%	142	0.02245	0.5564
M&R-1	Business/Non-Dispatch/TN(%)			3.65%	2.713	4.71%	85	0.02065	0.5117
M&R-1	Design (Specials)/Dispatch/TN(%)			1.19%	1.174	0.00%	12	0.03150	0.3186
M&R-1	Design (Specials)/Non-Dispatch/TN(%)			0.09%	1.069	0.00%	2	0.02164	0.04322
M&R-1	PBX/Dispatch/TN(%)			8.89%	45	0.00%	2	0.20565	0.4322
M&R-1	PBX/Non-Dispatch/TN(%)			5.88%	34				
M&R-1	Centrex/Dispatch/TN(%)			9.02%	399	0.00%	3	0.16603	0.5434
M&R-1	Centrex/Non-Dispatch/TN(%)			6.01%	233	0.00%	1	0.23916	0.2523
M&R-1	ISDN/Dispatch/TN(%)			10.00%	550	0.00%	1	0.30027	0.3330
M&R-1	ISDN/Non-Dispatch/TN(%)			3.04%	550	0.00%	2	0.12153	0.2498
Customer Trouble Report Rate									
M&R-2	Residence/Dispatch/TN(%)			2.30%	1.968365	2.65%	37.458	0.00079	-4.4375
M&R-2	Residence/Non-Dispatch/TN(%)			1.29%	1.968365	0.80%	37.458	0.00059	8.3378
M&R-2	Business/Dispatch/TN(%)			1.43%	486.501	1.23%	11.499	0.00113	1.5885
M&R-2	Business/Non-Dispatch/TN(%)			0.56%	486.501	0.74%	11.499	0.00070	-2.5766

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M&R-2	Design (Special) Dispatch/TN(%)	0.32%	368.991	0.49%	2,464			0.00114	-1.4810	YES	
A.3.2.3.1	M&R-2 Design (Special) Non-Dispatch/TN(%)	0.29%	368.991	0.08%	2,464			0.00109	1.9168	YES	
A.3.2.3.2	M&R-2 PBX Dispatch/TN(%)	0.07%	62.038	0.29%	692			0.00103	-2.1027	NO	
A.3.2.4.1	M&R-2 PBX Non-Dispatch/TN(%)	0.05%	62.038	0.00%	692			0.00089	0.6124	YES	
A.3.2.4.2	M&R-2 Centrex Dispatch/TN(%)	0.51%	77.531	1.15%	261			0.00445	-1.4272	YES	
A.3.2.5.1	M&R-2 Centrex Non-Dispatch/TN(%)	0.30%	77.531	0.38%	261			0.00340	-0.2431	YES	
A.3.2.5.2	M&R-2 ISDN Dispatch/TN(%)	1.84%	29.966	0.30%	338			0.00741	2.0775	YES	
A.3.2.6.1	M&R-2 ISDN Non-Dispatch/TN(%)	1.87%	29.966	0.59%	338			0.00748	1.7079	YES	
Maintenance Average Duration											
A.3.3.1.1	M&R-3 Residence Dispatch/TN(hours)	28.05	45.35	27.02	994			22.359	0.71692	1.4441	YES
A.3.3.1.2	M&R-3 Residence Non-Dispatch/TN(hours)	12.39	25.447	6.84	299			16.186	0.94156	6.1057	YES
A.3.3.1.3	M&R-3 Business Dispatch/TN(hours)	11.15	9.73	142				13.606	1.15343	1.2335	YES
A.3.3.2.2	M&R-3 Business Non-Dispatch/TN(hours)	4.69	2.713	3.22	85			8.295	0.91375	1.6059	YES
A.3.3.3.1	M&R-3 Design (Special) Dispatch/TN(hours)	4.90	1.174	6.16	12			25.012	7.25721	-0.1747	YES
A.3.3.3.2	M&R-3 Design (Special) Non-Dispatch/TN(hours)	2.17	1.089	3.24	2			14.773	0.45550	-0.1026	YES
A.3.3.4.1	M&R-3 PBX Dispatch/TN(hours)	8.14	45	18.37	2			7.918	5.72194	-1.7872	NO
A.3.3.4.2	M&R-3 PBX Non-Dispatch/TN(hours)	5.20	34					12.048			
A.3.3.5.1	M&R-3 Centrex Dispatch/TN(hours)	11.82	399	6.81	3						
A.3.3.5.2	M&R-3 Centrex Non-Dispatch/TN(hours)	5.24	233	2.30	1			13.408	13.27754	0.5881	YES
A.3.3.6.1	M&R-3 ISDN Dispatch/TN(hours)	10.46	650	4.42	1			13.384	13.37594	0.4519	YES
A.3.3.6.2	M&R-3 ISDN Non-Dispatch/TN(hours)	3.86	560	0.33	2			10.600	7.50889	0.4705	YES
% Repeat Troubles within 30 Days											
A.3.4.1.1	M&R-4 Residence Dispatch/TN(%)	22.76%	45.325	17.91%	994			0.01344	3.6111	YES	
A.3.4.1.2	M&R-4 Residence Non-Dispatch/TN(%)	18.82%	25.447	15.98%	299			0.02274	1.5107	YES	
A.3.4.2.1	M&R-4 Business Dispatch/TN(%)	16.53%	6.933	16.90%	142			0.03149	-0.1181	YES	
A.3.4.2.2	M&R-4 Business Non-Dispatch/TN(%)	14.34%	2.113	14.12%	85			0.03880	0.0572	YES	
A.3.4.3.1	M&R-4 Design (Special) Dispatch/TN(%)	41.99%	1.174	33.53%	12			0.14320	0.5047	YES	
A.3.4.3.2	M&R-4 Design (Special) Non-Dispatch/TN(%)	42.56%	1.069	50.00%	2			0.34995	-0.2125	YES	
A.3.4.4.1	M&R-4 PBX Dispatch/TN(%)	17.78%	45	0.00%	2			0.27629	0.6435	YES	
A.3.4.4.2	M&R-4 PBX Non-Dispatch/TN(%)	23.53%	34								
A.3.4.5.1	M&R-4 Centrex Dispatch/TN(%)	13.78%	399	33.33%	3			0.19978	-0.9785	YES	
A.3.4.5.2	M&R-4 Centrex Non-Dispatch/TN(%)	16.31%	233	0.00%	1			0.37024	0.4405	YES	
A.3.4.6.1	M&R-4 ISDN Dispatch/TN(%)	28.55%	560	0.00%	1			0.45204	0.6315	YES	
A.3.4.6.2	M&R-4 ISDN Non-Dispatch/TN(%)	24.32%	560	50.00%	2			0.39800	-0.8228	YES	
Out of Service > 24 hours											
A.3.5.1.1	M&R-5 Residence Dispatch/TN(%)	40.55%	29.987	46.12%	735			0.01833	-3.0395	NO	
A.3.5.1.2	M&R-5 Residence Non-Dispatch/TN(%)	18.33%	9.613	14.79%	142			0.03312	1.2509	YES	
A.3.5.2.1	M&R-5 Business Dispatch/TN(%)	5.77%	4.038	5.13%	78			0.02666	0.2408	YES	
A.3.5.2.2	M&R-5 Business Non-Dispatch/TN(%)	2.23%	1.121	6.90%	29			0.02777	-1.6802	NO	
A.3.5.3.1	M&R-5 Design (Special) Dispatch/TN(%)	1.19%	1.174	0.00%	12			0.37186			
A.3.5.3.2	M&R-5 Design (Special) Non-Dispatch/TN(%)	0.09%	1.069	0.00%	2			0.02164	0.0432	YES	
A.3.5.4.1	M&R-5 PBX Dispatch/TN(%)	0.00%	24	0.00%	2			0.00000			
A.3.5.4.2	M&R-5 PBX Non-Dispatch/TN(%)	20.00%	10								
A.3.5.5.1	M&R-5 Centrex Dispatch/TN(%)	5.18%	251	0.00%	1			0.22205	0.2332	YES	
A.3.5.5.2	M&R-5 Centrex Non-Dispatch/TN(%)	0.90%	111	0.00%	1			0.08491	0.0849	YES	
A.3.5.6.1	M&R-5 ISDN Dispatch/TN(%)	10.02%	549	0.00%	1			0.30052	0.3334	YES	
A.3.5.6.2	M&R-5 ISDN Non-Dispatch/TN(%)	3.04%	560	0.00%	2			0.12153	0.2498	YES	
Retail - Billing											
<i>Invoice Accuracy</i>											
E-1	1(N%)										
<i>Mean Time to Deliver Invoices - CRSS</i>											
E-2	Region(Business days)										
BST - State											
	3.72	1	3.18	1.791							
BST - Region											

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	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
Unbundled Network Elements - Ordering									
% Rejected Service Requests - Mechanized									
B.1.1.1	O-7	Switch Ports(TN%)							
B.1.1.2	O-7	Local Interface Transport(TN%)							
B.1.1.3	O-7	Loop + Port Combinations(TN%)							
B.1.1.4	O-7	Combo Other(TN%)							
B.1.1.5	O-7	xDSL (ADSL, HDSL and UCLY)(TN%)							
B.1.1.6	O-7	ISDN Loop (UDN, UDC)(TN%)							
B.1.1.7	O-7	Line Sharing(TN%)							
B.1.1.8	O-7	2W Analog Loop Design(TN%)							
B.1.1.9	O-7	2W Analog Loop Non-Design(TN%)							
B.1.1.10	O-7	2W Analog Loop w/INP Design(TN%)							
B.1.1.11	O-7	2W Analog Loop w/INP Non-Design(TN%)							
B.1.1.12	O-13	2W Analog Loop w/INP Design(TN%)							
B.1.1.13	O-13	2W Analog Loop w/INP Non-Design(TN%)							
B.1.1.14	O-7	Other Design(TN%)							
B.1.1.15	O-7	INP Standalone(TN%)							
B.1.1.16	O-7	INP (Standalone)(TN%)							
B.1.1.17	O-13	INP (Standalone)(TN%)							
% Rejected Service Requests - Partially Mechanized									
B.1.2.1	O-7	Switch Ports(TN%)							
B.1.2.2	O-7	Local Interface Transport(TN%)							
B.1.2.3	O-7	Loop + Port Combinations(TN%)							
B.1.2.4	O-7	Combo Other(TN%)							
B.1.2.5	O-7	xDSL (HDSL and UCLY)(TN%)							
B.1.2.6	O-7	ISDN Loop (UDN, UDC)(TN%)							
B.1.2.7	O-7	Line Sharing(TN%)							
B.1.2.8	O-7	2W Analog Loop Design(TN%)							
B.1.2.9	O-7	2W Analog Loop Non-Design(TN%)							
B.1.2.10	O-7	2W Analog Loop w/INP Design(TN%)							
B.1.2.11	O-7	2W Analog Loop w/INP Non-Design(TN%)							
B.1.2.12	O-13	2W Analog Loop w/INP Design(TN%)							
B.1.2.13	O-13	2W Analog Loop w/INP Non-Design(TN%)							
B.1.2.14	O-7	Other Design(TN%)							
B.1.2.15	O-7	INP Standalone(TN%)							
B.1.2.16	O-7	INP (Standalone)(TN%)							
B.1.2.17	O-13	INP (Standalone)(TN%)							
% Rejected Service Requests - Non-Mechanized									
B.1.3.1	O-7	Switch Ports(TN%)							
B.1.3.2	O-7	Local Interface Transport(TN%)							
B.1.3.3	O-7	Loop + Port Combinations(TN%)							
B.1.3.4	O-7	Combo Other(TN%)							
B.1.3.5	O-7	xDSL (HDSL and UCLY)(TN%)							
B.1.3.6	O-7	ISDN Loop (UDN, UDC)(TN%)							
B.1.3.7	O-7	Line Sharing(TN%)							
B.1.3.8	O-7	2W Analog Loop Design(TN%)							
B.1.3.9	O-7	2W Analog Loop Non-Design(TN%)							
B.1.3.10	O-7	2W Analog Loop w/INP Design(TN%)							
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design(TN%)							
B.1.3.12	O-13	2W Analog Loop w/INP Design(TN%)							
B.1.3.13	O-13	2W Analog Loop w/INP Non-Design(TN%)							
B.1.3.14	O-7	Other Design(TN%)							
B.1.3.15	O-7	INP Standalone(TN%)							
B.1.3.16	O-7	INP (Standalone)(TN%)							
B.1.3.17	O-13	INP (Standalone)(TN%)							
<i>Data included in B.1.3.20</i>									
B.1.3.1	O-7	Switch Ports(TN%)							
B.1.3.2	O-7	Local Interface Transport(TN%)							
B.1.3.3	O-7	Loop + Port Combinations(TN%)							
B.1.3.4	O-7	Combo Other(TN%)							
B.1.3.5	O-7	xDSL (HDSL and UCLY)(TN%)							
B.1.3.6	O-7	ISDN Loop (UDN, UDC)(TN%)							
B.1.3.7	O-7	Line Sharing(TN%)							
B.1.3.8	O-7	2W Analog Loop Design(TN%)							
B.1.3.9	O-7	2W Analog Loop Non-Design(TN%)							
B.1.3.10	O-7	2W Analog Loop w/INP Design(TN%)							
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design(TN%)							
B.1.3.12	O-13	2W Analog Loop w/INP Design(TN%)							
B.1.3.13	O-13	2W Analog Loop w/INP Non-Design(TN%)							
B.1.3.14	O-7	Other Design(TN%)							
B.1.3.15	O-7	INP Standalone(TN%)							
B.1.3.16	O-7	INP (Standalone)(TN%)							
B.1.3.17	O-13	INP (Standalone)(TN%)							
<i>Data included in B.1.3.19</i>									
B.1.3.1	O-7	Switch Ports(TN%)							
B.1.3.2	O-7	Local Interface Transport(TN%)							
B.1.3.3	O-7	Loop + Port Combinations(TN%)							
B.1.3.4	O-7	Combo Other(TN%)							
B.1.3.5	O-7	xDSL (HDSL and UCLY)(TN%)							
B.1.3.6	O-7	ISDN Loop (UDN, UDC)(TN%)							
B.1.3.7	O-7	Line Sharing(TN%)							
B.1.3.8	O-7	2W Analog Loop Design(TN%)							
B.1.3.9	O-7	2W Analog Loop Non-Design(TN%)							
B.1.3.10	O-7	2W Analog Loop w/INP Design(TN%)							
B.1.3.11	O-7	2W Analog Loop w/INP Non-Design(TN%)							
B.1.3.12	O-13	2W Analog Loop w/INP Design(TN%)							
B.1.3.13	O-13	2W Analog Loop w/INP Non-Design(TN%)							
B.1.3.14	O-7	Other Design(TN%)							
B.1.3.15	O-7	INP Standalone(TN%)							
B.1.3.16	O-7	INP (Standalone)(TN%)							
B.1.3.17	O-13	INP (Standalone)(TN%)							

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		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B.1.3.18	0.7	Loops Non-Design/T(N%)					57.20%	264		Diagnostic
B.1.3.19	0.7	Loops Non-Design w/NP/T(N%)					62.50%	40		Diagnostic
B.1.3.20	0.13	Loops Non-Design w/NP/T(N%)					75.81%	215		Diagnostic
Reject Interval - Mechanized										
B.1.4.1	0.8	Switch Ports/T(N%)					= 97% w in 1 hr			
B.1.4.2	0.8	Local Interoffice Transport/T(N%)					>= 97% w in 1 hr			
B.1.4.3	0.8	Loop + Port Combinations/T(N%)					>= 97% w in 1 hr			
B.1.4.4	0.8	Combo Other/T(N%)					>= 97% w in 1 hr			
B.1.4.5	0.8	xDSL (ADSL, HDSL and UCL)T(N%)					>= 97% w in 1 hr			
B.1.4.6	0.8	ISDN Loop (UDN, UDC)T(N%)					>= 97% w in 1 hr			
B.1.4.7	0.8	Line Sharing/T(N%)					>= 97% w in 1 hr			
B.1.4.8	0.8	2W Analog Loop Design/T(N%)					>= 97% w in 1 hr			
B.1.4.9	0.8	2W Analog Loop Non-Design/T(N%)					>= 97% w in 1 hr			
B.1.4.10	0.8	2W Analog Loop w/NP Design/T(N%)					>= 97% w in 1 hr			
B.1.4.11	0.8	2W Analog Loop w/NP Non-Design/T(N%)					>= 97% w in 1 hr			
B.1.4.12	0.14	2W Analog Loop w/NP Design/T(N%)					>= 97% w in 1 hr			
B.1.4.13	0.14	2W Analog Loop w/NP Non-Design/T(N%)					>= 97% w in 1 hr			
B.1.4.14	0.8	Other Design/T(N%)					>= 97% w in 1 hr			
B.1.4.15	0.8	Other Non-Design/T(N%)					>= 97% w in 1 hr			
B.1.4.16	0.8	INP Standalone/T(N%)					>= 97% w in 1 hr			
B.1.4.17	0.14	LNP (Standalone)/T(N%)					>= 97% w in 1 hr			
Reject Interval - Partially Mechanized - 24 hours										
B.1.5.1	0.8	Switch Ports/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.2	0.8	Local Interoffice Transport/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.3	0.8	Loop + Port Combinations/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.4	0.8	Combo Other/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.5	0.8	xDSL (ADSL, HDSL and UCL)T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.6	0.8	ISDN Loop (UDN, UDC)T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.7	0.8	Line Sharing/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.8	0.8	2W Analog Loop Design/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.9	0.8	2W Analog Loop Non-Design/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.10	0.8	2W Analog Loop w/NP Design/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.11	0.8	2W Analog Loop w/NP Non-Design/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.12	0.14	2W Analog Loop w/NP Design/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.13	0.14	2W Analog Loop w/NP Non-Design/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.14	0.8	Other Design/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.15	0.8	Other Non-Design/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.16	0.8	INP (Standalone)/T(N%)					This data not applicable after 5-1-2001 see below			
B.1.5.17	0.14	LNP (Standalone)/T(N%)					This data not applicable after 5-1-2001 see below			
Reject Interval - Partially Mechanized - 18 hours										
B.1.6.1	0.8	Switch Ports/T(N%)					= 85% w in 18 hrs			
B.1.6.2	0.8	Local Interoffice Transport/T(N%)					>= 85% w in 18 hrs			
B.1.6.3	0.8	Loop + Port Combinations/T(N%)					>= 85% w in 18 hrs			
B.1.6.4	0.8	Combo Other/T(N%)					>= 85% w in 18 hrs			
B.1.6.5	0.8	xDSL (ADSL, HDSL and UCL)T(N%)					>= 85% w in 18 hrs			
B.1.6.6	0.8	ISDN Loop (UDN, UDC)T(N%)					>= 85% w in 18 hrs			
B.1.6.7	0.8	Line Sharing/T(N%)					>= 85% w in 18 hrs			
B.1.6.8	0.8	2W Analog Loop Design/T(N%)					>= 85% w in 18 hrs			
B.1.6.9	0.8	2W Analog Loop Non-Design/T(N%)					>= 85% w in 18 hrs			
B.1.6.10	0.8	2W Analog Loop w/NP Design/T(N%)					>= 85% w in 18 hrs			
B.1.6.11	0.8	2W Analog Loop w/NP Non-Design/T(N%)					>= 85% w in 18 hrs			
B.1.6.12	0.14	2W Analog Loop w/NP Design/T(N%)					>= 85% w in 18 hrs			
B.1.6.13	0.14	2W Analog Loop w/NP Non-Design/T(N%)					>= 85% w in 18 hrs			
B.1.6.14	0.8	Other Design/T(N%)					>= 85% w in 18 hrs			
B.1.6.15	0.8	Other Non-Design/T(N%)					>= 85% w in 18 hrs			
B.1.6.16	0.8	INP Standalone/T(N%)					>= 85% w in 18 hrs			
B.1.6.17	0.14	LNP Standalone/T(N%)					>= 85% w in 18 hrs			

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Reject Interval - Non-Mechanized									
B.1.8.1	O.8 Switch Ports/T(N)%					100.00%	8		YES
B.1.8.2	O.8 Local Interoffice Transport/T(N)%					88.89%	27		YES
B.1.8.3	O.8 Loop + Port Combinations/T(N)%								
B.1.8.4	O.8 Combo Other/T(N)%								
B.1.8.5	O.8 xDSL (ADSL, HDSL and UCL)/T(N)%								
B.1.8.6	O.8 ISDN Loop (UDN, UDCL)/T(N)%								
B.1.8.7	O.8 Line Sharing/T(N)%								
B.1.8.8	O.8 2W Analog Loop Design/T(N)%								
B.1.8.9	O.8 2W Analog Loop Non-Design/T(N)%								
B.1.8.10	O.8 2W Analog Loop w/INP Design/T(N)%								
B.1.8.11	O.8 2W Analog Loop w/INP Non-Design/T(N)%								
B.1.8.12	O.8 2W Analog Loop w/LNP Design/T(N)%								
B.1.8.13	O.8 2W Analog Loop w/LNP Non-Design/T(N)%								
B.1.8.14	O.8 Other Design/T(N)%								
B.1.8.15	O.8 Other Non-Design/T(N)%								
B.1.8.16	O.8 INP Standalone/T(N)%								
B.1.8.17	O.8 LNP Standalone/T(N)%								
B.1.8.18	O.8 Loops Non-Design w/INP/T(N)%								
B.1.8.19	O.8 Loops Non-Design w/LNP/T(N)%								
B.1.8.20	O.8 Loops Non-Design w/INP/LNP/T(N)%								
FOC Timeliness - Mechanized									
B.1.9.1	O.9 Switch Ports/T(N)%								
B.1.9.2	O.9 Local Interoffice Transport/T(N)%								
B.1.9.3	O.9 Loop + Port Combinations/T(N)%								
B.1.9.4	O.9 Combo Other/T(N)%								
B.1.9.5	O.9 xDSL (ADSL, HDSL and UCL)/T(N)%								
B.1.9.6	O.9 ISDN Loop (UDN, UDCL)/T(N)%								
B.1.9.7	O.9 Line Sharing/T(N)%								
B.1.9.8	O.9 2W Analog Loop Design/T(N)%								
B.1.9.9	O.9 2W Analog Loop w/INP Design/T(N)%								
B.1.9.10	O.9 2W Analog Loop w/INP Non-Design/T(N)%								
B.1.9.11	O.9 2W Analog Loop w/LNP Design/T(N)%								
B.1.9.12	O.1.15 2W Analog Loop w/LNP Non-Design/T(N)%								
B.1.9.13	O.1.15 2W Analog Loop w/INP Non-Design/T(N)%								
B.1.9.14	O.9 Other Design/T(N)%								
B.1.9.15	O.9 Other Non-Design/T(N)%								
B.1.9.16	O.9 INP Standalone/T(N)%								
B.1.9.17	O.1.15 LNP Standalone/T(N)%								
FOC Timeliness - Partially Mechanized									
B.1.10.1	O.9 Switch Ports/T(N)%								
B.1.10.2	O.9 Local Interoffice Transport/T(N)%								
B.1.10.3	O.9 Loop + Port Combinations/T(N)%								
B.1.10.4	O.9 Combo Other/T(N)%								
B.1.10.5	O.9 xDSL (ADSL, HDSL and UCL)/T(N)%								
B.1.10.6	O.9 ISDN Loop (UDN, UDCL)/T(N)%								
B.1.10.7	O.9 Line Sharing/T(N)%								
B.1.10.8	O.9 2W Analog Loop Design/T(N)%								
B.1.10.9	O.9 2W Analog Loop Non-Design/T(N)%								
B.1.10.10	O.9 2W Analog Loop w/INP Design/T(N)%								
B.1.10.11	O.9 2W Analog Loop w/INP Non-Design/T(N)%								
B.1.10.12	O.1.15 2W Analog Loop w/INP Non-Design/T(N)%								
B.1.10.13	O.1.15 2W Analog Loop w/LNP Non-Design/T(N)%								
B.1.10.14	O.9 Other Design/T(N)%								
B.1.10.15	O.9 Other Non-Design/T(N)%								
B.1.10.16	O.9 INP Standalone/T(N)%								
B.1.10.17	O.1.15 LNP Standalone/T(N)%								
Time Gaps and Retention Rates after 6-1-2001, see below									
B.1.10.1	The data not applicable after 6-1-2001, see below								
B.1.10.2	The data not applicable after 6-1-2001, see below								
B.1.10.3	The data not applicable after 6-1-2001, see below								
B.1.10.4	The data not applicable after 6-1-2001, see below								
B.1.10.5	The data not applicable after 6-1-2001, see below								
B.1.10.6	The data not applicable after 6-1-2001, see below								
B.1.10.7	The data not applicable after 6-1-2001, see below								
B.1.10.8	The data not applicable after 6-1-2001, see below								
B.1.10.9	The data not applicable after 6-1-2001, see below								
B.1.10.10	The data not applicable after 6-1-2001, see below								
B.1.10.11	The data not applicable after 6-1-2001, see below								
B.1.10.12	The data not applicable after 6-1-2001, see below								
B.1.10.13	The data not applicable after 6-1-2001, see below								
B.1.10.14	The data not applicable after 6-1-2001, see below								
B.1.10.15	The data not applicable after 6-1-2001, see below								
B.1.10.16	The data not applicable after 6-1-2001, see below								
B.1.10.17	The data not applicable after 6-1-2001, see below								

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	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
FOC Timeliness - Partially Mechanized - 18 hours									
B.1.11.1	O-9	Switch Ports(N%)				100.00%	11		YES
B.1.11.2	O-9	Local Interoffice Transport(T/N%)				91.97%	1,383		YES
B.1.11.3	O-9	Loop + Port Combinations(T/N%)							NO
B.1.11.4	O-9	Combo Other(T/N%)							NO
B.1.11.5	O-9	xDSL (ADSL, HDSL and UCL/T/N%)				66.67%	6		
B.1.11.6	O-9	(SDN Loop (UDN, UDC))T/N%)				100.00%	27		YES
B.1.11.7	O-9	Line Sharing(N%)							YES
B.1.11.8	O-9	2W Analog Loop Non-Design(T/N%)							YES
B.1.11.9	O-9	2W Analog Loop Non-Design(T/N%)							YES
B.1.11.10	O-9	2W Analog Loop w/INP Design(T/N%)							YES
B.1.11.11	O-9	2W Analog Loop w/INP Non-Design(T/N%)							YES
B.1.11.12	O-15	2W Analog Loop w/LNP Design(T/N%)							NO
B.1.11.13	O-15	2W Analog Loop w/LNP Non-Design(T/N%)							NO
B.1.11.14	O-9	Other Non-Design(T/N%)							YES
B.1.11.15	O-9	Other Non-Design(T/N%)							YES
B.1.11.16	O-9	INP Standard(T/N%)							YES
B.1.11.17	O-15	LNP Standard(T/N%)							YES
FOC Timeliness - Non-Mechanized									
B.1.13.1	O-9	Switch Ports(T/N%)				100.00%	107		YES
B.1.13.2	O-9	Local Interoffice Transport(T/N%)				97.14%	35		YES
B.1.13.3	O-9	Loop + Port Combinations(T/N%)							YES
B.1.13.4	O-9	Combo Other(T/N%)							YES
B.1.13.5	O-9	xDSL (ADSL, HDSL and UCL/T/N%)							YES
B.1.13.6	O-9	(SDN Loop (UDN, UDC))T/N%)							YES
B.1.13.7	O-9	Line Sharing(T/N%)							YES
B.1.13.8	O-9	2W Analog Loop Design(T/N%)							YES
B.1.13.9	O-9	2W Analog Loop Non-Design(T/N%)							YES
B.1.13.10	O-9	2W Analog Loop w/INP Design(T/N%)							YES
B.1.13.11	O-9	2W Analog Loop w/INP Non-Design(T/N%)							YES
B.1.13.12	O-15	2W Analog Loop w/LNP Design(T/N%)							YES
B.1.13.13	O-15	2W Analog Loop w/LNP Non-Design(T/N%)							NO
B.1.13.14	O-9	Other Design(T/N%)							YES
B.1.13.15	O-9	Other Non-Design(T/N%)							YES
B.1.13.16	O-9	INP Standard(T/N%)							YES
B.1.13.17	O-15	LNP Standard(T/N%)							YES
FOC & Reject Response Completeness #Mechanized									
B.1.14.1	O-11	Switch Ports(T/N%)							NO
B.1.14.2	O-11	Local Interoffice Transport(T/N%)							NO
B.1.14.3	O-11	Loop + Port Combinations(T/N%)							NO
B.1.14.4	O-11	Combo Other(T/N%)							NO
B.1.14.5	O-11	xDSL (ADSL, HDSL and UCL/T/N%)							NO
B.1.14.6	O-11	(SDN Loop (UDN, UDC))T/N%)							NO
B.1.14.7	O-11	Line Sharing(T/N%)							NO
B.1.14.8	O-11	2W Analog Loop Design(T/N%)							NO
B.1.14.9	O-11	2W Analog Loop Non-Design(T/N%)							NO
B.1.14.10	O-11	2W Analog Loop w/INP Design(T/N%)							NO
B.1.14.11	O-11	2W Analog Loop w/INP Non-Design(T/N%)							NO
B.1.14.12	O-11	2W Analog Loop w/LNP Design(T/N%)							YES
B.1.14.13	O-11	2W Analog Loop w/LNP Non-Design(T/N%)							YES
B.1.14.14	O-11	Other Design(T/N%)							NO
B.1.14.15	O-11	Other Non-Design(T/N%)							NO
B.1.14.16	O-11	INP Standard(T/N%)							NO
B.1.14.17	O-11	LNP Standard(T/N%)							NO
FOC & Reject Response Completeness - Partially Mechanized									
B.1.15.1	O-11	Switch Ports(T/N%)							YES
B.1.15.2	O-11	Local Interoffice Transport(T/N%)							YES

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B.1.15.3	O-11 Loop + Port Combinations/T(N%)				100.00%	1.737			
B.1.15.4	O-11 Combo Other/T(N%)								YES
B.1.15.5	O-11 xDSL (ADSL, HDSL, and UCL)T(N%)								
B.1.15.6	O-11 ISDN Loop (UDN, UDC)T(N%)								
B.1.15.7	O-11 Line Sharing/T(N%)								YES
B.1.15.8	O-11 2W Analog Loop Design/T(N%)								
B.1.15.9	O-11 2W Analog Loop Non-Design/T(N%)								YES
B.1.15.10	O-11 2W Analog Loop w/NP Non-Design/T(N%)								
B.1.15.11	O-11 2W Analog Loop w/NP Non-Design/T(N%)								
B.1.15.12	O-11 2W Analog Loop w/LNP Design/T(N%)								
B.1.15.13	O-11 2W Analog Loop w/LNP Non-Design/T(N%)								
B.1.15.14	O-11 Other Design/T(N%)								
B.1.15.15	O-11 Other Non-Design/T(N%)								YES
B.1.15.16	O-11 INP Standalone/T(N%)								YES
B.1.15.17	O-11 LNP Standalone/T(N%)								YES
FOC & Reject Response Completeness - Non-Mechanized									
B.1.16.1	O-11 Switch Ports/T(N%)								
B.1.16.2	O-11 Local Interoffice Transport/T(N%)								YES
B.1.16.3	O-11 Loop + Port Combinations/T(N%)								NO
B.1.16.4	O-11 Combo Other/T(N%)								
B.1.16.5	O-11 xDSL (ADSL, HDSL and UCL)T(N%)								
B.1.16.6	O-11 ISDN Loop (UDN, UDC)T(N%)								NO
B.1.16.7	O-11 Line Sharing/T(N%)								YES
B.1.16.8	O-11 2W Analog Loop Design/T(N%)								YES
B.1.16.9	O-11 2W Analog Loop Non-Design/T(N%)								YES
B.1.16.10	O-11 2W Analog Loop w/NP Design/T(N%)								
B.1.16.11	O-11 2W Analog Loop w/NP Non-Design/T(N%)								
B.1.16.12	O-11 2W Analog Loop w/LNP Design/T(N%)								
B.1.16.13	O-11 2W Analog Loop w/LNP Non-Design/T(N%)								
B.1.16.14	O-11 Other Design/T(N%)								
B.1.16.15	O-11 Other Non-Design/T(N%)								
B.1.16.16	O-11 INP Standalone/T(N%)								
B.1.16.17	O-11 LNP Standalone/T(N%)								
FOC & Reject Response Completeness (Multiple Responses) - Mechanized									
B.1.17.1	O-11 Switch Ports/T(N%)								
B.1.17.2	O-11 Local Interoffice Transport/T(N%)								
B.1.17.3	O-11 Loop + Port Combinations/T(N%)								
B.1.17.4	O-11 Combo Other/T(N%)								
B.1.17.5	O-11 xDSL (ADSL, HDSL and UCL)T(N%)								
B.1.17.6	O-11 ISDN Loop (UDN, UDC)T(N%)								
B.1.17.7	O-11 Line Sharing/T(N%)								
B.1.17.8	O-11 2W Analog Loop Design/T(N%)								
B.1.17.9	O-11 2W Analog Loop Non-Design/T(N%)								
B.1.17.10	O-11 2W Analog Loop w/NP Design/T(N%)								
B.1.17.11	O-11 2W Analog Loop w/NP Non-Design/T(N%)								
B.1.17.12	O-11 2W Analog Loop w/LNP Design/T(N%)								
B.1.17.13	O-11 2W Analog Loop w/LNP Non-Design/T(N%)								
B.1.17.14	O-11 Other Design/T(N%)								
B.1.17.15	O-11 Other Non-Design/T(N%)								
B.1.17.16	O-11 INP Standalone/T(N%)								
B.1.17.17	O-11 LNP Standalone/T(N%)								
FOC & Reject Response Completeness (Multiple Responses) - Partially Mechanized									
B.1.18.1	O-11 Switch Ports/T(N%)								
B.1.18.2	O-11 Local Interoffice Transport/T(N%)								
B.1.18.3	O-11 Loop + Port Combinations/T(N%)								YES
B.1.18.4	O-11 Combo Other/T(N%)								NO
B.1.18.5	O-11 xDSL (ADSL, HDSL and UCL)T(N%)								
B.1.18.6	O-11 ISDN Loop (UDN, UDC)T(N%)								

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O-11	Line Sharing/T(N%)									
O-11	2W Analog Loop Non-Design/T(N%)	>= 95%								
O-11	2W Analog Loop Non-Design/T(N%)	>= 95%								
O-11	2W Analog Loop w/NP Non-Design/T(N%)	>= 95%								
O-11	2W Analog Loop w/NP Non-Design/T(N%)	>= 95%								
O-11	2W Analog Loop w/LNP Design/T(N%)	>= 95%								
O-11	2W Analog Loop w/LNP Non-Design/T(N%)	>= 95%								
O-11	Other Design/T(N%)	>= 95%								
O-11	Other Non-Design/T(N%)	>= 95%								
O-11	INP Standalone/T(N%)	>= 95%								
O-11	INP Standalone/T(N%)	>= 95%								
B.1.18.7	O-11									
B.1.18.8	O-11									
B.1.18.9	O-11									
B.1.18.10	O-11									
B.1.18.11	O-11									
B.1.18.12	O-11									
B.1.18.13	O-11									
B.1.18.14	O-11									
B.1.18.15	O-11									
B.1.18.16	O-11									
B.1.18.17	O-11									
B.1.19.1	O-11									
B.1.19.2	O-11									
B.1.19.3	O-11									
B.1.19.4	O-11									
B.1.19.5	O-11									
B.1.19.6	O-11									
B.1.19.7	O-11									
B.1.19.8	O-11									
B.1.19.9	O-11									
B.1.19.10	O-11									
B.1.19.11	O-11									
B.1.19.12	O-11									
B.1.19.13	O-11									
B.1.19.14	O-11									
B.1.19.15	O-11									
B.1.19.16	O-11									
B.1.19.17	O-11									
FOC & Reject Response Completeness (Multiple Responses) - Non-Mechanized										
O-11	Switch Ports/T(N%)	>= 95%								
O-11	Local Interoffice Transport/T(N%)	>= 95%								
O-11	Loop + Port Combinations/T(N%)	>= 95%								
O-11	Combo Other/T(N%)	>= 95%								
O-11	xDSL (ADSL, HDSL and UCL) w/NP Design/T(N%)	>= 95%								
O-11	xDSL (ADSL, HDSL and UCL) w/NP Non-Design/T(N%)	>= 95%								
O-11	ISDN Loop (UDN, UPC) w/NP Design/T(N%)	>= 95%								
O-11	ISDN Loop (UDN, UPC) w/NP Non-Design/T(N%)	>= 95%								
O-11	Line Sharing/T(N%)	>= 95%								
O-11	2W Analog Loop Design/T(N%)	>= 95%								
O-11	2W Analog Loop Non-Design/T(N%)	>= 95%								
O-11	2W Analog Loop w/NP Design/T(N%)	>= 95%								
O-11	2W Analog Loop w/NP Non-Design/T(N%)	>= 95%								
O-11	2W Analog Loop w/LNP Design/T(N%)	>= 95%								
O-11	2W Analog Loop w/LNP Non-Design/T(N%)	>= 95%								
O-11	Other Design/T(N%)	>= 95%								
O-11	Other Non-Design/T(N%)	>= 95%								
O-11	INP Standalone/T(N%)	>= 95%								
O-11	INP Standalone/T(N%)	>= 95%								
Unbundled Network Elements - Provisioning										
P-4	Order Completion Interval									
B.2.1.1.1	P-4	Switch Ports<10 circuits/Dispatch/T(N days)	R&B (POTS)	5.07	21.238					
B.2.1.1.2	P-4	Switch Ports<10 circuits/Non-Dispatch/T(N days)	R&B (POTS)	0.39	267.151					
B.2.1.1.2	P-4	Switch Ports>=10 circuits/Dispatch/T(N days)	R&B (POTS)	9.39	69					
B.2.1.1.2	P-4	Switch Ports>=10 circuits/Non-Dispatch/T(N days)	DS1/DS3	0.81	16					
B.2.1.1.2	P-4	Local Interoffice Transport<10 circuits/Dispatch/T(N days)	DS1/DS3			12.00	1			
B.2.1.1.2	P-4	Local Interoffice Transport>=10 circuits/Dispatch/T(N days)	DS1/DS3							
B.2.1.1.2	P-4	Local Interoffice Transport<10 circuits/Non-Dispatch/T(N days)	DS1/DS3							
B.2.1.1.2	P-4	Loop + Port Combinations<10 circuits/Dispatch/T(N days)	R&B	5.08	21.458					
B.2.1.1.2	P-4	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N days)	R&B	0.89	268.225					
B.2.1.1.3	P-4	Loop + Port Combinations<10 circuits/Dispatch/T(N days)	R&B			1.875	1.129			
B.2.1.1.3	P-4	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N days)	R&B							
B.2.1.1.4	P-4	Loop + Port Combinations<10 circuits/Dispatch/T(N days)	R&B							
B.2.1.1.4	P-4	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N days)	R&B							
B.2.1.1.4	P-4	Loop + Port Combinations>=10 circuits/Dispatch/T(N days)	R&B	11.00	85	6.00	1			
B.2.1.1.4	P-4	Loop + Port Combinations>=10 circuits/Non-Dispatch/T(N days)	R&B	2.50	77					
B.2.1.1.4	P-4	Loop + Port Combinations>=10 circuits/Switch Based Orders/T(N days)	R&B							
B.2.1.1.4	P-4	Loop + Port Combinations>=10 circuits/Dispatch/T(N days)	R&B							
B.2.1.1.4	P-4	Combo Other<10 circuits/Dispatch/T(N days)	R&B	6.79	24.112					
B.2.1.1.4	P-4	Combo Other<10 circuits/Non-Dispatch/T(N days)	R&B							
B.2.1.1.4	P-4	Combo Other>=10 circuits/Dispatch/T(N days)	R&B	10.92	90					
B.2.1.1.4	P-4	Combo Other>=10 circuits/Non-Dispatch/T(N days)	R&B							
B.2.1.1.4	P-4	xDSL (ADSL, HDSL and UCL)<6 circuits/Dispatch/T(N days)	R&B	5.51	225	4.81	89			
B.2.1.1.4	P-4	xDSL (ADSL, HDSL and UCL)<6 circuits/Non-Dispatch/T(N days)	R&B	3.29	115					
B.2.1.1.4	P-4	xDSL (ADSL, HDSL and UCL)>6-13 circuits/Dispatch/T(N days)	R&B	4.00	1					
B.2.1.1.4	P-4	xDSL (ADSL, HDSL and UCL)>14 circuits/Non-Dispatch/T(N days)	R&B							
B.2.1.1.4	P-4	xDSL (ADSL, HDSL and UCL)>14 circuits/Dispatch/T(N days)	R&B							
B.2.1.1.5.1	P-4									

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B2.1.5.52	P.4	xDSL (ADSL, HDSL and UCL)>=14 circuits/Non-Dispatch/N(days)	ADSL to Retail	17.06	397	10.26	61	21.881	3.00916	2.2598		YES
B2.1.6.31	P.4	UNE ISDN<6 circuits/Dispatch/N(days)	ISDN - BRI	1.49	424			2.870				
B2.1.6.32	P.4	UNE ISDN>6 circuits/Non-Dispatch/N(days)	ISDN - BRI									
B2.1.6.4	P.4	UNE ISDN<13 circuits/Non-Dispatch/N(days)	ISDN - BRI	0.33	1							
B2.1.6.4.2	P.4	UNE ISDN>13 circuits/Non-Dispatch/N(days)	ISDN - BRI									
B2.1.6.51	P.4	UNE ISDN>=14 circuits/Dispatch/N(days)	ISDN - BRI									
B2.1.6.52	P.4	UNE ISDN/=14 circuits/Non-Dispatch/N(days)	ISDN - BRI									
B2.1.7.3.1	P.4	Line Shaping<6 circuits/Dispatch/N(days)	ADSL to Retail	5.51	235							
B2.1.7.3.2	P.4	Line Shaping<6 circuits/Non-Dispatch/N(days)	ADSL to Retail	3.29	115	3.48		33	1.156	0.22827	-0.8324	YES
B2.1.7.4.1	P.4	Line Shaping<13 circuits/Dispatch/N(days)	ADSL to Retail	4.00	1				0.000			
B2.1.7.4.2	P.4	Line Shaping<13 circuits/Non-Dispatch/N(days)	ADSL to Retail									
B2.1.7.5.1	P.4	Line Shaping<=14 circuits/Dispatch/N(days)	ADSL to Retail									
B2.1.7.5.2	P.4	Line Shaping<=14 circuits/Non-Dispatch/N(days)	ADSL to Retail									
B2.1.8.1.1	P.4	2W Analog Loop Design<10 circuits/Non-Dispatch/N(days)	R&B - Disp	5.08	21486	5.12	195	9.095	0.65425	-0.0558		YES
B2.1.8.1.2	P.4	2W Analog Loop Design<10 circuits/Non-Dispatch/N(days)	R&B - Disp	5.08	21486			9.095				
B2.1.8.2.1	P.4	2W Analog Loop Design<>10 circuits/Non-Dispatch/N(days)	R&B - Disp	11.00	85			9.095				
B2.1.8.2.2	P.4	2W Analog Loop Non-Design<>10 circuits/Non-Dispatch/N(days)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	11.00	85			15.218				
B2.1.9.1.1	P.4	2W Analog Loop Non-Design<10 circuits/Dispatch/N(days)	R&B (POTS) excl SB Or	5.07	21238			9.077				
B2.1.9.1.4	P.4	2W Analog Loop Non-Design<10 circuits/Dispatch/N(days)	R&B (POTS) excl SB Or	1.29	152.244			1.284				
B2.1.9.2.1	P.4	2W Analog Loop Non-Design<10 circuits/Non-Dispatch/N(days)	R&B (POTS) excl SB Or	9.39	69			13.912				
B2.1.9.2.4	P.4	2W Analog Loop Non-Design<>10 circuits/Non-Dispatch/N(days)	R&B (POTS) excl SB Or	2.25	4			1.500				
B2.1.10.1.1	P.4	2W Analog Loop Non-Design<10 circuits/Dispatch/N(days)	R&B (POTS) excl SB Or	5.08	21486	5.00		1	9.095	0.09551	0.0082	YES
B2.1.10.1.2	P.4	2W Analog Loop Non-Design<>10 circuits/Dispatch/N(days)	R&B (POTS) excl SB Or	5.08	21486			9.095				
B2.1.10.2.1	P.4	2W Analog Loop Dispatch<>10 circuits/Dispatch/N(days)	R&B (POTS) excl SB Or	1.29	152.244			15.218				
B2.1.10.2.2	P.4	2W Analog Loop Dispatch<>10 circuits/Non-Dispatch/N(days)	R&B (POTS) excl SB Or	11.00	85			15.218				
B2.1.11.1	P.4	2W Analog Loop Non-Design<>10 circuits/Non-Dispatch/N(days)	R&B (POTS) excl SB Or	5.07	21238			9.077				
B2.1.11.1.4	P.4	2W Analog Loop w/LNP Design<<10 circuits/Dispatch/N(days)	R&B (POTS) excl SB Or	1.29	152.244			1.284				
B2.1.11.2.1	P.4	2W Analog Loop w/LNP Non-Design<<10 circuits/Dispatch/N(days)	R&B (POTS) excl SB Or	9.39	69			13.912				
B2.1.11.2.4	P.4	2W Analog Loop w/LNP Non-Design<>10 circuits/Dispatch/N(days)	R&B (POTS) excl SB Or	2.25	4			1.500				
B2.1.12.1.1	P.4	2W Analog Loop w/LNP Design<>10 circuits/Dispatch/N(days)	R&B (POTS) excl SB Or	5.08	21486	5.19		123	9.095	0.82241	-0.1258	YES
B2.1.12.1.2	P.4	2W Analog Loop w/LNP Design<<10 circuits/Non-Dispatch/N(days)	R&B (POTS) excl SB Or	5.08	21486			9.095				
B2.1.12.2.1	P.4	2W Analog Loop w/LNP Design<>10 circuits/Non-Dispatch/N(days)	R&B (POTS) excl SB Or	11.00	85			15.218				
B2.1.13.1.1	P.4	2W Analog Loop w/LNP Non-Design<<10 circuits/Non-Dispatch/N(days)	R&B (POTS) excl SB Or	5.07	21238			9.077				
B2.1.13.1.4	P.4	2W Analog Loop w/LNP Non-Design<>10 circuits/Non-Dispatch/N(days)	R&B (POTS) excl SB Or	1.29	152.244			1.284				
B2.1.13.2.1	P.4	2W Analog Loop w/LNP Design<<10 circuits/Dispatch/N(days)	R&B (POTS) excl SB Or	9.39	69			13.912				
B2.1.13.2.4	P.4	2W Analog Loop w/LNP Design<>10 circuits/Dispatch/N(days)	R&B (POTS) excl SB Or	2.25	4			1.500				
B2.1.14.1.1	P.4	Other Design<<10 circuits/Dispatch/N(days)	Design	20.34	2798	10.50	12		27.291	7.89577	1.2462	YES
B2.1.14.1.2	P.4	Other Design<>10 circuits/Non-Dispatch/N(days)	Design	11.62	882							
B2.1.14.2.1	P.4	Other Design<>10 circuits/Dispatch/N(days)	Design	1.60	5							
B2.1.14.2.2	P.4	Other Design<>10 circuits/Non-Dispatch/N(days)	Design	4.00	14							
B2.1.15.1.1	P.4	Other Non-Design<<10 circuits/Dispatch/N(days)	R&B (POTS)	5.08	21486	5.19		9.095				
B2.1.15.1.2	P.4	Other Non-Design<>10 circuits/Non-Dispatch/N(days)	R&B (POTS)	0.89	286325	0.33	1					
B2.1.15.2.1	P.4	Other Non-Design<>10 circuits/Non-Dispatch/N(days)	R&B (POTS)	11.00	85			15.218				
B2.1.15.2.2	P.4	Other Non-Design<>10 circuits/Non-Dispatch/N(days)	R&B (POTS)	2.50	77			3.478				
B2.1.16.1.1	P.4	INP (Standalone)<<10 circuits/Dispatch/N(days)	R&B (POTS)	5.07	21238			9.077				
B2.1.16.1.2	P.4	INP (Standalone)<<10 circuits/Non-Dispatch/N(days)	R&B (POTS)	0.88	267.151	0.33	1	1.080		1.08040	0.5082	YES
B2.1.17.2.1	P.4	INP (Standalone)>=10 circuits/Non-Dispatch/N(days)	R&B (POTS)	9.39	69			13.912				
B2.1.17.2.2	P.4	INP (Standalone)>=10 circuits/Dispatch/N(days)	R&B (POTS)	0.81	16			1.072				
B2.1.17.1.1	P.4	LNP (Standalone)<<10 circuits/Dispatch/N(days)	R&B (POTS)	5.07	21238	7.25		9.077		2.03059	-1.0735	YES
B2.1.18.2.1	P.4	LNP (Standalone)<<10 circuits/Non-Dispatch/N(days)	R&B (POTS)	0.88	267.151	6.24		98	1.080	0.10816	-4.7121	NO
B2.1.18.2.2	P.4	LNP (Standalone)>=10 circuits/Dispatch/N(days)	R&B (POTS)	9.39	69			13.912				
B2.1.18.1.1	P.4	Digital Loop < DS/1<<10 circuits/Non-Dispatch/N(days)	R&B (POTS)	0.81	16			1.072		0.80393	-6.4558	NO
B2.1.18.1.2	P.4	Digital Loop < DS/1<<10 circuits/Dispatch/N(days)	Digital Loop < DS1	20.50	4			61	9.574	1.35866	2.3458	YES
B2.1.18.2.1	P.4	Digital Loop < DS/1<=10 circuits/Non-Dispatch/N(days)	Digital Loop < DS1									
B2.1.18.2.2	P.4	Digital Loop < DS/1<=10 circuits/Non-Dispatch/N(days)	Digital Loop < DS1									
B2.1.19.1.1	P.4	Digital Loop >= DS/1<<10 circuits/Dispatch/N(days)	Digital Loop >= DS1	56.88	87	6.20		150				
B2.1.19.1.2	P.4	Digital Loop >= DS/1<=10 circuits/Non-Dispatch/N(days)	Digital Loop >= DS1	13.20	5				10.010			

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		Benchmark / Analog	
		BST Measure	BST Volume
		CLEC Measure	CLEC Volume
		Standard Deviation	Standard Error
		ZScore	Equity
P-4	Digital Loop >= DS1 (>=10 circuits/Dispatch)/N(days)	Digital Loop > DS1	
P-4	Digital Loop >= DS1 (>=10 circuits/Non-Dispatch)/N(days)	Digital Loop > DS1	
Order Completion Interval within X days			
P-4	xDSL (ADSL, HDSL and UCL) Loop with Conditioning (<6 circuits/Dispatch)/N(days)	14 days	
P-4	xDSL (ADSL, HDSL and UCL) Loop w/o Conditioning (<6 circuits/Dispatch)/N(days)	7 days	
Hold Orders			
P-1	Switch Ports<10 circuits/facility/TN(days)	R&B (POTS)	9.21
P-1	Switch Ports<10 circuits/Equipment/TN(days)	R&B (POTS)	9.06
P-1	Switch Ports<10 circuits/Other/TN(days)	R&B (POTS)	34
P-1	Switch Ports>=10 circuits/facility/TN(days)	R&B (POTS)	
P-1	Switch Ports>=10 circuits/Equipment/TN(days)	R&B (POTS)	
P-1	Switch Ports>=10 circuits/Other/TN(days)	R&B (POTS)	
B2.3.1.1	Local Interoffice Transport (<10 circuits/facility/TN(days))	DS1 // DS3 - Interoffice	
B2.3.1.1.2	Local Interoffice Transport (<10 circuits/Equipment/TN(days))	DS1 // DS3 - Interoffice	
B2.3.1.1.3	Local Interoffice Transport (<10 circuits/Other/TN(days))	DS1 // DS3 - Interoffice	
B2.3.1.2.1	Local Interoffice Transport (<10 circuits/facility/TN(days))	DS1 // DS3 - Interoffice	
B2.3.1.2.2	Local Interoffice Transport (<10 circuits/Equipment/TN(days))	DS1 // DS3 - Interoffice	
B2.3.1.2.3	Local Interoffice Transport (<10 circuits/Other/TN(days))	DS1 // DS3 - Interoffice	
B2.3.2.1.1	Local Interoffice Transport (<10 circuits/facility/TN(days))	DS1 // DS3 - Interoffice	
B2.3.2.1.2	Local Interoffice Transport (<10 circuits/Equipment/TN(days))	DS1 // DS3 - Interoffice	
B2.3.2.1.3	Local Interoffice Transport (<10 circuits/Other/TN(days))	DS1 // DS3 - Interoffice	
B2.3.2.2.1	Local Interoffice Transport (>=10 circuits/facility/TN(days))	DS1 // DS3 - Interoffice	
B2.3.2.2.2	Local Interoffice Transport (>=10 circuits/Equipment/TN(days))	DS1 // DS3 - Interoffice	
B2.3.2.2.3	Local Interoffice Transport (>=10 circuits/Other/TN(days))	DS1 // DS3 - Interoffice	
B2.3.3.1.1	Loop + Port Combinations (<10 circuits/facility/TN(days))	R&B	9.22
B2.3.3.1.2	Loop + Port Combinations (<10 circuits/Equipment/TN(days))	R&B	204
B2.3.3.1.3	Loop + Port Combinations (<10 circuits/Other/TN(days))	R&B	11.00
B2.3.3.2.1	Loop + Port Combinations (>=10 circuits/facility/TN(days))	R&B	1
B2.3.3.2.2	Loop + Port Combinations (>=10 circuits/Equipment/TN(days))	R&B	7.907
B2.3.3.2.3	Loop + Port Combinations (>=10 circuits/Other/TN(days))	R&B	7.92656
B2.3.4.1.1	Combo Other (<10 circuits/facility/TN(days))	R&B & Disp	0.2251
B2.3.4.1.2	Combo Other (<10 circuits/Equipment/TN(days))	R&B & Disp	YES
B2.3.4.1.3	Combo Other (<10 circuits/Other/TN(days))	R&B & Disp	15.092
B2.3.4.2.1	Combo Other (>=10 circuits/facility/TN(days))	R&B & Disp	
B2.3.4.2.2	Combo Other (>=10 circuits/Equipment/TN(days))	R&B & Disp	
B2.3.4.2.3	Combo Other (>=10 circuits/Other/TN(days))	R&B & Disp	
B2.3.5.1.1	xDSL (ADSL, HDSL and UCL) (<10 circuits/facility/TN(days))	ADSL to Retail	
P-1	xDSL (ADSL, HDSL and UCL) (<10 circuits/Equipment/TN(days))	ADSL to Retail	
P-1	xDSL (ADSL, HDSL and UCL) (<10 circuits/Other/TN(days))	ADSL to Retail	
B2.3.5.1.3	xDSL (ADSL, HDSL and UCL) (>=10 circuits/facility/TN(days))	ADSL to Retail	
P-1	xDSL (ADSL, HDSL and UCL) (>=10 circuits/Equipment/TN(days))	ADSL to Retail	
P-1	xDSL (ADSL, HDSL and UCL) (>=10 circuits/Other/TN(days))	ADSL to Retail	
B2.3.5.2.1	Line Sharing (<10 circuits/facility/TN(days))	ISDN - BRI	
P-1	Line Sharing (<10 circuits/Equipment/TN(days))	ISDN - BRI	
P-1	Line Sharing (<10 circuits/Other/TN(days))	ISDN - BRI	
B2.3.5.2.2	Line Sharing (>=10 circuits/facility/TN(days))	ISDN - BRI	
P-1	Line Sharing (>=10 circuits/Equipment/TN(days))	ISDN - BRI	
P-1	Line Sharing (>=10 circuits/Other/TN(days))	ISDN - BRI	
B2.3.6.1.1	UNE ISDN (<10 circuits/facility/TN(days))	ADSL to Retail	
P-1	UNE ISDN (<10 circuits/Equipment/TN(days))	ADSL to Retail	
P-1	UNE ISDN (<10 circuits/Other/TN(days))	ADSL to Retail	
B2.3.6.1.2	UNE ISDN (>=10 circuits/facility/TN(days))	ADSL to Retail	
P-1	UNE ISDN (>=10 circuits/Equipment/TN(days))	ADSL to Retail	
P-1	UNE ISDN (>=10 circuits/Other/TN(days))	ADSL to Retail	
B2.3.6.2.1	Line Sharing (<10 circuits/facility/TN(days))	ISDN - BRI	
P-1	Line Sharing (<10 circuits/Equipment/TN(days))	ISDN - BRI	
P-1	Line Sharing (<10 circuits/Other/TN(days))	ISDN - BRI	
B2.3.6.2.2	Line Sharing (>=10 circuits/facility/TN(days))	ISDN - BRI	
P-1	Line Sharing (>=10 circuits/Equipment/TN(days))	ISDN - BRI	
P-1	Line Sharing (>=10 circuits/Other/TN(days))	ISDN - BRI	
B2.3.7.1.1	Line Sharing (<10 circuits/facility/TN(days))	ADSL to Retail	
P-1	Line Sharing (<10 circuits/Equipment/TN(days))	ADSL to Retail	
P-1	Line Sharing (<10 circuits/Other/TN(days))	ADSL to Retail	
B2.3.7.1.3	Line Sharing (>=10 circuits/facility/TN(days))	ADSL to Retail	
P-1	Line Sharing (>=10 circuits/Equipment/TN(days))	ADSL to Retail	
P-1	Line Sharing (>=10 circuits/Other/TN(days))	ADSL to Retail	
B2.3.7.2.1	2W Analog Loop Design (<10 circuits/facility/TN(days))	R&B & Disp	
P-1	2W Analog Loop Design (<10 circuits/Equipment/TN(days))	R&B & Disp	
P-1	2W Analog Loop Design (<10 circuits/Other/TN(days))	R&B & Disp	
B2.3.8.1.3	2W Analog Loop Design (>=10 circuits/facility/TN(days))	R&B & Disp	
P-1	2W Analog Loop Design (>=10 circuits/Equipment/TN(days))	R&B & Disp	
P-1	2W Analog Loop Design (>=10 circuits/Other/TN(days))	R&B & Disp	
B2.3.8.2.2	2W Analog Loop Design (<10 circuits/facility/TN(days))	R&B & Disp	
P-1	2W Analog Loop Design (<10 circuits/Equipment/TN(days))	R&B & Disp	
P-1	2W Analog Loop Design (<10 circuits/Other/TN(days))	R&B & Disp	
B2.3.8.2.3	2W Analog Loop Design (>=10 circuits/facility/TN(days))	R&B & Disp	
P-1	2W Analog Loop Design (>=10 circuits/Equipment/TN(days))	R&B & Disp	
P-1	2W Analog Loop Design (>=10 circuits/Other/TN(days))	R&B & Disp	
B2.3.8.1.1	Loop Non-Design (<10 circuits/facility/TN(days))	R&B (POTS) excl SB Or	
B2.3.9.1.1	Loop Non-Design (<10 circuits/Equipment/TN(days))	R&B (POTS) excl SB Or	
B2.3.9.1.2	Loop Non-Design (<10 circuits/Other/TN(days))	R&B (POTS) excl SB Or	
B2.3.9.1.3	Loop Non-Design (>=10 circuits/facility/TN(days))	R&B (POTS) excl SB Or	
B2.3.9.2.1	Loop Non-Design (>=10 circuits/Equipment/TN(days))	R&B (POTS) excl SB Or	
P-1	Loop Non-Design (>=10 circuits/Other/TN(days))	R&B (POTS) excl SB Or	

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		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B.2.3.9.22	P-1	2W Analog Loop w/NP Design >= 10 circuits/Equipment/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.9.23	P-1	2W Analog Loop Non-Design >= 10 circuits/Other/Unit(N/day\$)	R&B (POTS) excl SB Or R&B - Disp	9.22	204					
B.2.3.10.1.1	P-1	2W Analog Loop w/NP Design < 10 circuits/Facility/Unit(N/day\$)	R&B - Disp							7.907
B.2.3.10.1.2	P-1	2W Analog Loop w/NP Design < 10 circuits/Equipment/Unit(N/day\$)	R&B - Disp							
B.2.3.10.1.3	P-1	2W Analog Loop w/NP Design < 10 circuits/Other/Unit(N/day\$)	R&B - Disp							15.092
B.2.3.10.2.1	P-1	2W Analog Loop w/NP Design >= 10 circuits/Facility/Unit(N/day\$)	R&B - Disp							
B.2.3.10.2.2	P-1	2W Analog Loop w/NP Design >= 10 circuits/Equipment/Unit(N/day\$)	R&B - Disp							
B.2.3.10.2.3	P-1	2W Analog Loop w/NP Design >= 10 circuits/Other/Unit(N/day\$)	R&B - Disp							
B.2.3.11.1.1	P-1	2W Analog Loop w/NP Non-Design < 10 circuits/Facility/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							0.000
B.2.3.11.1.2	P-1	2W Analog Loop w/NP Non-Design < 10 circuits/Equipment/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							7.864
B.2.3.11.1.3	P-1	2W Analog Loop w/NP Non-Design < 10 circuits/Other/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							15.092
B.2.3.11.2.1	P-1	2W Analog Loop w/NP Non-Design >= 10 circuits/Facility/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.11.2.2	P-1	2W Analog Loop w/NP Non-Design >= 10 circuits/Equipment/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.11.2.3	P-1	2W Analog Loop w/NP Non-Design >= 10 circuits/Other/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.12.1.1	P-1	2W Analog Loop w/NP Design < 10 circuits/Facility/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp	9.22	204					
B.2.3.12.1.2	P-1	2W Analog Loop w/NP Design < 10 circuits/Equipment/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.12.1.3	P-1	2W Analog Loop w/NP Design < 10 circuits/Other/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.12.2.1	P-1	2W Analog Loop w/NP Design >= 10 circuits/Facility/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.12.2.2	P-1	2W Analog Loop w/NP Design >= 10 circuits/Equipment/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.12.2.3	P-1	2W Analog Loop w/NP Design >= 10 circuits/Other/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.13.1.1	P-1	2W Analog Loop w/NP Non-Design < 10 circuits/Facility/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							0.000
B.2.3.13.1.2	P-1	2W Analog Loop w/NP Non-Design < 10 circuits/Equipment/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							7.864
B.2.3.13.1.3	P-1	2W Analog Loop w/NP Non-Design < 10 circuits/Other/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							15.092
B.2.3.13.2.1	P-1	2W Analog Loop w/NP Non-Design >= 10 circuits/Facility/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.13.2.2	P-1	2W Analog Loop w/NP Non-Design >= 10 circuits/Equipment/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.13.2.3	P-1	2W Analog Loop w/NP Non-Design >= 10 circuits/Other/Unit(N/day\$)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or R&B - Disp							
B.2.3.14.1.1	P-1	Other Design < 10 circuits/Facility/Unit(N/day\$)	Design	56.92	24					83.881
B.2.3.14.1.2	P-1	Other Design < 10 circuits/Equipment/Unit(N/day\$)	Design							133.002
B.2.3.14.1.3	P-1	Other Design < 10 circuits/Other/Unit(N/day\$)	Design							
B.2.3.14.2.1	P-1	Other Design >= 10 circuits/Facility/Unit(N/day\$)	Design							
B.2.3.14.2.2	P-1	Other Design >= 10 circuits/Equipment/Unit(N/day\$)	Design							
B.2.3.14.2.3	P-1	Other Design >= 10 circuits/Other/Unit(N/day\$)	Design							
B.2.3.15.1.1	P-1	Other Non-Design < 10 circuits/Facility/Unit(N/day\$)	R&B (POTS)							7.907
B.2.3.15.1.2	P-1	Other Non-Design < 10 circuits/Equipment/Unit(N/day\$)	R&B (POTS)							
B.2.3.15.1.3	P-1	Other Non-Design < 10 circuits/Other/Unit(N/day\$)	R&B (POTS)							15.092
B.2.3.15.2.1	P-1	Other Non-Design >= 10 circuits/Facility/Unit(N/day\$)	R&B (POTS)							
B.2.3.15.2.2	P-1	Other Non-Design >= 10 circuits/Equipment/Unit(N/day\$)	R&B (POTS)							
B.2.3.15.2.3	P-1	Other Non-Design >= 10 circuits/Other/Unit(N/day\$)	R&B (POTS)							
B.2.3.16.1.1	P-1	NP (Standalone) < 10 circuits/Facility/Unit(N/day\$)	R&B (POTS)							
B.2.3.16.1.2	P-1	NP (Standalone) < 10 circuits/Equipment/Unit(N/day\$)	R&B (POTS)							
B.2.3.16.1.3	P-1	NP (Standalone) < 10 circuits/Other/Unit(N/day\$)	R&B (POTS)							
B.2.3.16.2.1	P-1	NP (Standalone) >= 10 circuits/Facility/Unit(N/day\$)	R&B (POTS)							
B.2.3.16.2.2	P-1	NP (Standalone) >= 10 circuits/Equipment/Unit(N/day\$)	R&B (POTS)							
B.2.3.16.2.3	P-1	NP (Standalone) >= 10 circuits/Other/Unit(N/day\$)	R&B (POTS)							
B.2.3.17.1.1	P-1	Digital Loop < DS / < 10 circuits/Facility/Unit(N/day\$)	Digital Loop < DS1							
B.2.3.17.1.2	P-1	Digital Loop < DS / < 10 circuits/Equipment/Unit(N/day\$)	Digital Loop < DS1							
B.2.3.17.1.3	P-1	Digital Loop < DS / < 10 circuits/Other/Unit(N/day\$)	Digital Loop < DS1							
B.2.3.17.2.1	P-1	Digital Loop > DS / = 10 circuits/Facility/Unit(N/day\$)	Digital Loop < DS1							
B.2.3.17.2.2	P-1	Digital Loop > DS / = 10 circuits/Equipment/Unit(N/day\$)	Digital Loop < DS1							
B.2.3.17.2.3	P-1	Digital Loop > DS / = 10 circuits/Other/Unit(N/day\$)	Digital Loop < DS1							
B.2.3.18.1.1	P-1	Digital Loop > DS / > 10 circuits/Facility/Unit(N/day\$)	Digital Loop > DS1							
B.2.3.18.1.2	P-1	Digital Loop > DS / > 10 circuits/Equipment/Unit(N/day\$)	Digital Loop > DS1							
B.2.3.18.1.3	P-1	Digital Loop > DS / > 10 circuits/Other/Unit(N/day\$)	Digital Loop > DS1							
B.2.3.18.2.1	P-1	Digital Loop > DS / = 10 circuits/Facility/Unit(N/day\$)	Digital Loop > DS1							
B.2.3.18.2.2	P-1	Digital Loop > DS / = 10 circuits/Equipment/Unit(N/day\$)	Digital Loop > DS1							
B.2.3.18.2.3	P-1	Digital Loop > DS / = 10 circuits/Other/Unit(N/day\$)	Digital Loop > DS1							
B.2.3.19.1.1	P-1	Digital Loop > DS / > 10 circuits/Facility/Unit(N/day\$)	Digital Loop > DS1							
B.2.3.19.1.2	P-1	Digital Loop > DS / > 10 circuits/Equipment/Unit(N/day\$)	Digital Loop > DS1							
B.2.3.19.1.3	P-1	Digital Loop > DS / > 10 circuits/Other/Unit(N/day\$)	Digital Loop > DS1							

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	P-1	Digital Loop >= DS1 />=10 circuits/Facility/TN/(days)	
B2.3.19.2.1	P-1	Digital Loop >= DS1 />=10 circuits/Equipment/TN/(days)	
B2.3.19.2.2	P-1	Digital Loop >= DS1 />=10 circuits/Other/TN/(days)	
B2.3.19.2.3	P-1	Digital Loop >= DS1 />=10 circuits/Other/TN/(days)	
% Jeopardies - Mechanized			
B.2.5.1	P-2	Switch Ports/TN(%)	
B.2.5.2	P-2	Local Interoffice Transport/TN(%)	
B.2.5.3	P-2	Loop + Port Combinations/TN(%)	
B.2.5.4	P-2	Combo Other/TN(%)	
B.2.5.5	P-2	xDSL (ADSL, HDSL and UCL)/TN(%)	
B.2.5.6	P-2	UNE ISDN/TN(%)	
B.2.5.7	P-2	Line Shaping/TN(%)	
B.2.5.8	P-2	2W Analog Loop Design/TN(%)	
B.2.5.9	P-2	2W Analog Loop Non-Design/TN(%)	
B.2.5.10	P-2	2W Analog Loop w/INP Design/TN(%)	
B.2.5.11	P-2	2W Analog Loop w/INP Non-Design/TN(%)	
B.2.5.12	P-2	2W Analog Loop w/INP Design/TN(%)	
B.2.5.13	P-2	2W Analog Loop w/INP Non-Design/TN(%)	
B.2.5.14	P-2	Other Design/TN(%)	
B.2.5.15	P-2	Other Non-Design/TN(%)	
B.2.5.16	P-2	INP (Standalone)/TN(%)	
B.2.5.17	P-2	INP (Standalone)/TN(%)	
B.2.5.18	P-2	Digital Loop < DS1/TN(%)	
B.2.5.19	P-2	Digital Loop >= DS1/TN(%)	

% Jeopardies - Non-Mechanized

	P-2	Switch Ports/TN(%)	
B.2.6.1	P-2	Local Interoffice Transport/TN(%)	
B.2.6.2	P-2	Loop + Port Combinations/TN(%)	
B.2.6.3	P-2	Combo Other/TN(%)	
B.2.6.4	P-2	xDSL (ADSL, HDSL and UCL)/TN(%)	
B.2.6.5	P-2	UNE ISDN/TN(%)	
B.2.6.6	P-2	Line Sharing/TN(%)	
B.2.6.7	P-2	2W Analog Loop Design/TN(%)	
B.2.6.8	P-2	2W Analog Loop Non-Design/TN(%)	
B.2.6.9	P-2	2W Analog Loop w/INP Design/TN(%)	
B.2.6.10	P-2	2W Analog Loop w/INP Non-Design/TN(%)	
B.2.6.11	P-2	2W Analog Loop w/INP Design/TN(%)	
B.2.6.12	P-2	2W Analog Loop w/INP Non-Design/TN(%)	
B.2.6.13	P-2	2W Analog Loop w/INP Non-Design/TN(%)	
B.2.6.14	P-2	Other Design/TN(%)	
B.2.6.15	P-2	INP (Standalone)/TN(%)	
B.2.6.16	P-2	INP (Standalone)/TN(%)	
B.2.6.17	P-2	Digital Loop < DS1/TN(%)	
B.2.6.18	P-2	Digital Loop >= DS1/TN(%)	
B.2.6.19	P-2	Digital Loop >= DS1/TN(%)	

Average Jeopardy Notice Interval - Mechanized

B.2.8.1	P-2	Switch Ports/TN(hours)	>= 48 hrs
B.2.8.2	P-2	Local Interoffice Transport/TN(hours)	>= 48 hrs
B.2.8.3	P-2	Loop + Port Combinations/TN(hours)	>= 48 hrs
B.2.8.4	P-2	Combo Other/TN(hours)	>= 48 hrs
B.2.8.5	P-2	xDSL (ADSL, HDSL and UCL)/TN(hours)	>= 48 hrs
B.2.8.6	P-2	UNE ISDN/TN(hours)	>= 48 hrs
B.2.8.7	P-2	Line Sharing/TN(hours)	>= 48 hrs
B.2.8.8	P-2	2W Analog Loop Design/TN(hours)	>= 48 hrs
B.2.8.9	P-2	2W Analog Loop Non-Design/TN(hours)	>= 48 hrs
B.2.8.10	P-2	2W Analog Loop w/INP Design/TN(hours)	>= 48 hrs
B.2.8.11	P-2	2W Analog Loop w/INP Non-Design/TN(hours)	>= 48 hrs
B.2.8.12	P-2	2W Analog Loop w/INP Design/TN(hours)	>= 48 hrs
B.2.8.13	P-2	2W Analog Loop w/INP Non-Design/TN(hours)	>= 48 hrs
B.2.8.14	P-2	Other Design/TN(hours)	>= 48 hrs

B.2.5.1	P-2	R&B (POTS), DS1 /> DS3 - Interoffice R&B	0.64%	304/209	0.08%	2
B.2.5.2	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.65%	305/629	0.23%	3,031
B.2.5.3	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.92%	310/346	18.43%	YES
B.2.5.4	P-2	R&B - Disp ADSL to Retail ISDN - BRI	12.40%	931	4.08%	98
B.2.5.5	P-2	R&B - Disp ADSL to Retail ISDN - BRI	18.13%	3,435	25.00%	36
B.2.5.6	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.65%	305/629	15.31%	NO
B.2.5.7	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00812	-1,0497	NO	
B.2.5.8	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00147	2,8654	YES	
B.2.5.9	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00972	3,6119	2,2483	
B.2.5.10	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.05601	0.05601		
B.2.5.11	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00812	1,0497		
B.2.5.12	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556		
B.2.5.13	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556		
B.2.5.14	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556		
B.2.5.15	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556		
B.2.5.16	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556		
B.2.5.17	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556		
B.2.5.18	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556		
B.2.5.19	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556		

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B.2.5.1	P-2	R&B (POTS), DS1 /> DS3 - Interoffice R&B	0.64%	304/209	0.08%	2			
B.2.5.2	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.65%	305/629	0.23%	3,031			
B.2.5.3	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.92%	310/346	18.43%	YES			
B.2.5.4	P-2	R&B - Disp ADSL to Retail ISDN - BRI	12.40%	931	4.08%	98			
B.2.5.5	P-2	R&B - Disp ADSL to Retail ISDN - BRI	18.13%	3,435	25.00%	36			
B.2.5.6	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.65%	305/629	15.31%	98			
B.2.5.7	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00812	-1,0497	NO				
B.2.5.8	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00147	2,8654	YES				
B.2.5.9	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00972	3,6119	2,2483				
B.2.5.10	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.05601	0.05601					
B.2.5.11	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00812	-1,0497					
B.2.5.12	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556					
B.2.5.13	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556					
B.2.5.14	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556					
B.2.5.15	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556					
B.2.5.16	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556					
B.2.5.17	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556					
B.2.5.18	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556					
B.2.5.19	P-2	R&B - Disp ADSL to Retail ISDN - BRI	0.00518	1,2556					

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	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B2.8.15	P2 Other Non Design/T(N)hours)								
B2.8.16	P2 INP (Standalone/T(N)hours)								
B2.8.17	P2 LNP (Standalone/T(N)hours)								
B2.8.18	P2 Digital Loop < DS /T(N)hours)								
B2.8.19	P2 Digital Loop >= DS /T(N)hours)								
Average Jeopardy Notice Interval - Non-Mechanized									
B2.9.1	P2 Switch Ports/T(N)hours)								
B2.9.2	P2 Local Interoffice Transport/T(N)hours)								
B2.9.3	P2 Loop + Port Combinations/T(N)hours)								
B2.9.4	P2 Combo Other/T(N)hours)								
B2.9.5	P2 xDSL (ADSL, HDSL and UCL)/T(N)hours)								
B2.9.6	P2 UNE ISDN/T(N)hours)								
B2.9.7	P2 Line Sharing/T(N)hours)								
B2.9.8	P2 2W Analog Loop Design/T(N)hours)								
B2.9.9	P2 2W Analog Loop Non-Design/T(N)hours)								
B2.9.10	P2 2W Analog Loop w/INP Design/T(N)hours)								
B2.9.11	P2 2W Analog Loop w/INP Non-Design/T(N)hours)								
B2.9.12	P2 2W Analog Loop w/INP Design/T(N)hours)								
B2.9.13	P2 2W Analog Loop w/INP Non-Design/T(N)hours)								
B2.9.14	P2 Other Design/T(N)hours)								
B2.9.15	P2 Other Non Design/T(N)hours)								
B2.9.16	P2 INP (Standalone/T(N)hours)								
B2.9.17	P2 LNP (Standalone/T(N)hours)								
B2.9.18	P2 Digital Loop < DS /T(N)hours)								
B2.9.19	P2 Digital Loop >= DS /T(N)hours)								
% Jeopardy Notice >= 48 hours - Mechanized									
B2.10.1	P2 Switch Ports/T(N)%)								
B2.10.2	P2 Local Interoffice Transport/T(N)%)								
B2.10.3	P2 Loop + Port Combinations/T(N)%)								
B2.10.4	P2 Combo Other/T(N)%)								
B2.10.5	P2 xDSL (ADSL, HDSL and UCL)/T(N)%)								
B2.10.6	P2 UNE ISDN/T(N)%)								
B2.10.7	P2 Line Sharing/T(N)%)								
B2.10.8	P2 2W Analog Loop Design/T(N)%)								
B2.10.9	P2 2W Analog Loop Non-Design/T(N)%)								
B2.10.10	P2 2W Analog Loop w/INP Design/T(N)%)								
B2.10.11	P2 2W Analog Loop w/INP Non-Design/T(N)%)								
B2.10.12	P2 2W Analog Loop w/INP Design/T(N)%)								
B2.10.13	P2 2W Analog Loop w/INP Non-Design/T(N)%)								
B2.10.14	P2 Other Design/T(N)%)								
B2.10.15	P2 Other Non-Design/T(N)%)								
B2.10.16	P2 INP (Standalone/T(N)%)								
B2.10.17	P2 LNP (Standalone/T(N)%)								
B2.10.18	P2 Digital Loop < DS /T(N)%)								
B2.10.19	P2 Digital Loop >= DS /T(N)%)								
% Jeopardy Notice >= 48 hours - Non-Mechanized									
B2.11.1	P2 Switch Ports/T(N)%)								
B2.11.2	P2 Local Interoffice Transport/T(N)%)								
B2.11.3	P2 Loop + Port Combinations/T(N)%)								
B2.11.4	P2 Combo Other/T(N)%)								
B2.11.5	P2 xDSL (ADSL, HDSL and UCL)/T(N)%)								
B2.11.6	P2 UNE ISDN/T(N)%)								
B2.11.7	P2 Line Sharing/T(N)%)								
B2.11.8	P2 2W Analog Loop Design/T(N)%)								
B2.11.9	P2 2W Analog Loop Non-Design/T(N)%)								
B2.11.10	P2 2W Analog Loop w/INP Design/T(N)%)								
B2.11.11	P2 2W Analog Loop w/INP Non-Design/T(N)%)								
B2.11.12	P2 2W Analog Loop w/INP Design/T(N)%)								

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	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	zScore	Equity
P.2	2W Analog Loop w/LNP Non-Design TN(%)								Diagnostic
P.2	Other Design TN(%)								Diagnostic
B.2.11.14	Other Non-Design TN(%)								Diagnostic
B.2.11.15	INP (Standalone) TN(%)								Diagnostic
B.2.11.16	LNP (Standalone) TN(%)								Diagnostic
B.2.11.17	Digital Loop < DSI/TN(%)								Diagnostic
B.2.11.18	Digital Loop > DST/TN(%)								Diagnostic
B.2.11.19	Coordinated Customer Conversations								Diagnostic
B.2.12.1	P.7 Loop with INP/TN(%)	= 95% w in 15 min	99.45%	901					
B.2.12.2	P.7 Loops with LNP/TN(%)	>= 95% w in 15 min							
B.2.13.1	% Hot Cuts > 15 minutes Early								
B.2.13.2	P.7A Time-Specific SL1/TN(%)	<= 5%							
B.2.13.3	P.7A Time-Specific SL2/TN(%)	<= 5%							
B.2.13.4	P.7A Non-Time Specific SL1/TN(%)	<= 5%							
B.2.13.5	P.7A Non-Time Specific SL2/TN(%)	<= 5%							
B.2.14.1	% Hot Cut Timeliness								
B.2.14.2	P.7A Time-Specific SL1/TN(%)	>= 95% w in 15 min	98.40%	250					
B.2.14.3	P.7A Time-Specific SL2/TN(%)	>= 95% w in 15 min	1.60%	250					
B.2.14.4	P.7A Non-Time Specific SL1/TN(%)	>= 95% w in 15 min	0.00%	52					
B.2.15.1	% Hot Cuts > 15 minutes Late								
B.2.15.2	P.7A Time-Specific SL1/TN(%)	<= 5%							
B.2.15.3	P.7A Non-Time Specific SL1/TN(%)	<= 5%							
B.2.15.4	P.7A Non-Time Specific SL2/TN(%)	<= 5%							
B.2.16.1	Average Recovery Time - CCC								
B.2.16.2	P.7B Loops with INP/TN(time units)								
B.2.16.3	P.7B Loops with LNP/TN(time units)								
B.2.17.1.1	% Provisioning Troubles within 7 Days - Hot Cuts								
B.2.17.1.2	P.7C UNE Loop Design/Dispatch TN(%)	<= 5%							
B.2.17.2.1	P.7C UNE Loop Non-Design/Dispatch TN(%)	<= 5%							
B.2.17.2.2	P.7C UNE Loop Non-Design/Non-Dispatch TN(%)	<= 5%							
B.2.18.1.1	% Missed Installation Appointments								
B.2.18.1.2	P.7.3 Switch Ports < 10 circuits/Non Dispatch TN(%)	R&B (POTS)	7.05%	24.125					
B.2.18.1.2.1	P.7.3 Switch Ports >= 10 circuits/Dispatch TN(%)	R&B (POTS)	0.05%	279.195					
B.2.18.1.2.2	P.7.3 Switch Ports >= 10 circuits/Non-Dispatch TN(%)	R&B (POTS)	12.09%	91					
B.2.18.2.1	P.7.3 Local Interoffice Transport < 10 circuits/Dispatch TN(%)	DS1/DS3	0.00%	16					
B.2.18.2.1.1	P.7.3 Local Interoffice Transport >= 10 circuits/Non-Dispatch TN(%)	DS1/DS3	0.00%	3					
B.2.18.2.1.2	P.7.3 Local Interoffice Transport < 10 circuits/Dispatch TN(%)	DS1/DS3	0.00%						
B.2.18.2.2	P.7.3 Local Interoffice Transport >= 10 circuits/Non-Dispatch TN(%)	DS1/DS3	0.00%						
B.2.18.3.1.1	P.7.3 Loop + Port Combinations < 10 circuits/Dispatch TN(%)	R&B	7.04%	24.363					
B.2.18.3.1.2	P.7.3 Loop + Port Combinations >= 10 circuits/Non-Dispatch TN(%)	R&B	0.05%	280.986					
B.2.18.3.1.3	P.7.3 Loop + Port Combinations < 10 circuits/Switch Based Order ST/TN(%)	R&B	7.05%	3.291					
B.2.18.3.1.4	P.7.3 Loop + Port Combinations >= 10 circuits/Dispatch TN(%)	R&B	10.28%	107					
B.2.18.3.2.1	P.7.3 Loop + Port Combinations >= 10 circuits/Non Dispatch TN(%)	R&B	0.00%	85					
B.2.18.3.2.2	P.7.3 Loop + Port Combinations >= 10 circuits/Switch Based Order ST/TN(%)	R&B	10.28%	1					
B.2.18.3.2.3	P.7.3 Loop + Port Combinations >= 10 circuits/Dispatch TN(%)	R&B	10.28%	1					
B.2.18.3.2.4	P.7.3 Loop + Port Combinations >= 10 circuits/Dispatch TN(%)	R&B	10.28%	1					
B.2.18.4.1	Combo Other < 10 circuits/Dispatch TN(%)	R&BD - Disp	6.85%	27.108					
B.2.18.4.1.4	Combo Other < 10 circuits/Dispatch TN(%)	R&BD - Disp	10.62%	113					
B.2.18.4.2.1	Combo Other >= 10 circuits/Dispatch TN(%)	R&BD - Disp	10.62%						
B.2.18.4.2.4	Combo Other < 10 circuits/Dispatch TN(%)	R&BD - Disp	10.62%						
B.2.18.5.1.1	xDSL (ADSL, HDSL and UCLY) < 10 circuits/Dispatch TN(%)	ADSL to Retail	5.03%	3.278					
				3.14%				0.01775	1.0639
					159				YES

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		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
P.3	xDSL (ADSL, HDSL and UCL)<10 circuits/Non-Dispatch/TN(%)	ADSL to Retail	0.00%	178						
P.3	xDSL (ADSL, HDSL and UCL)>=10 circuits/Dispatch/TN(%)	ADSL to Retail	0.00%	6						
B.2.18.5.2.1		ADSL to Retail	10.81%	407	8.00%	75			0.03902	0.7204
B.2.18.5.2.2		ISDN - BRI	0.69%	434						YES
P.3	UNE ISDN<10 circuits/Non-Dispatch/TN(%)	ISDN - BRI								
B.2.18.6.1.1		ISDN - BRI								
B.2.18.6.1.2										
P.3	UNE ISDN>=10 circuits/Dispatch/TN(%)									
B.2.18.6.2.1										
B.2.18.6.2.2										
P.3	UNE ISDN<10 circuits/Non-Dispatch/TN(%)									
B.2.18.7.1.1										
B.2.18.7.1.2										
P.3	Line Shaving<10 circuits/Dispatch/TN(%)									
B.2.18.7.2.1										
P.3	Line Shaving<10 circuits/Non-Dispatch/TN(%)									
B.2.18.7.2.2										
P.3	Line Shaving<=10 circuits/Dispatch/TN(%)									
B.2.18.8.1.1										
P.3	2W Analog Loop Design<10 circuits/Dispatch/TN(%)									
B.2.18.8.1.2										
P.3	2W Analog Loop Design<10 circuits/Non-Dispatch/TN(%)									
B.2.18.8.2.1										
P.3	2W Analog Loop Design<10 circuits/Non-Dispatch/TN(%)									
B.2.18.9.1.1										
P.3	2W Analog Loop Non-Design<10 circuits/Dispatch/TN(%)									
B.2.18.9.1.4										
P.3	2W Analog Loop Non-Design<10 circuits/Non-Dispatch/TN(%)									
B.2.18.9.2.1										
P.3	2W Analog Loop Non-Design<=10 circuits/Dispatch/TN(%)									
B.2.18.9.2.4										
P.3	2W Analog Loop Design<10 circuits/Non-Dispatch/TN(%)									
B.2.18.10.1.1										
P.3	2W Analog Loop w/NP Design<10 circuits/Dispatch/TN(%)									
B.2.18.10.1.2										
P.3	2W Analog Loop w/NP Design<10 circuits/Non-Dispatch/TN(%)									
B.2.18.10.2.1										
P.3	2W Analog Loop w/NP Design<=10 circuits/Dispatch/TN(%)									
B.2.18.10.2.2										
P.3	2W Analog Loop w/NP Design<=10 circuits/Non-Dispatch/TN(%)									
B.2.18.11.1.1										
P.3	2W Analog Loop w/NP Non-Design<10 circuits/Dispatch/TN(%)									
B.2.18.11.1.4										
P.3	2W Analog Loop w/NP Non-Design<10 circuits/Non-Dispatch/TN(%)									
B.2.18.11.2.4										
P.3	2W Analog Loop w/NP Non-Design<>10 circuits/Dispatch/TN(%)									
B.2.18.12.1.1										
P.12	2W Analog Loop w/NP Design<10 circuits/Dispatch/TN(%)									
B.2.18.12.1.2										
P.12	2W Analog Loop w/NP Design<=10 circuits/Non-Dispatch/TN(%)									
B.2.18.12.2.1										
P.12	2W Analog Loop w/NP Non-Design<10 circuits/Dispatch/TN(%)									
B.2.18.12.2.2										
P.12	2W Analog Loop w/NP Non-Design<>10 circuits/Non-Dispatch/TN(%)									
B.2.18.12.2.4										
P.12	2W Analog Loop w/NP Other Design<10 circuits/Dispatch/TN(%)									
B.2.18.13.1.1										
P.12	2W Analog Loop w/NP Non-Design<>10 circuits/Dispatch/TN(%)									
B.2.18.13.1.4										
P.12	2W Analog Loop w/NP Non-Design<>10 circuits/Non-Dispatch/TN(%)									
B.2.18.13.2.1										
P.12	2W Analog Loop w/NP Non-Design<=10 circuits/Dispatch/TN(%)									
B.2.18.13.2.4										
P.12	2W Analog Loop w/NP Non-Design<=10 circuits/Non-Dispatch/TN(%)									
B.2.18.14.1.1										
P.3	Other Design<10 circuits/Dispatch/TN(%)									
B.2.18.14.1.2										
P.3	Other Design<10 circuits/Non-Dispatch/TN(%)									
B.2.18.14.2.1										
P.3	Other Design<=10 circuits/Non-Dispatch/TN(%)									
B.2.18.15.1.1										
P.3	Other Non-Design<10 circuits/Non-Dispatch/TN(%)									
B.2.18.15.1.2										
P.3	Other Non-Design<=10 circuits/Non-Dispatch/TN(%)									
B.2.18.15.2.1										
P.3	Other Non-Design<10 circuits/Dispatch/TN(%)									
B.2.18.15.2.2										
P.3	Other Design<10 circuits/Non-Dispatch/TN(%)									
B.2.18.16.1.1										
P.3	INP (Standalone)<10 circuits/Dispatch/TN(%)									
B.2.18.16.1.2										
P.3	INP (Standalone)>=10 circuits/Non-Dispatch/TN(%)									
B.2.18.16.2.1										
P.3	INP (Standalone)>=10 circuits/Non-Dispatch/TN(%)									
B.2.18.17.1.1										
P.12	LNP (Standalone)<=10 circuits/Non-Dispatch/TN(%)									
B.2.18.17.2.1										
P.12	LNP (Standalone)>=10 circuits/Non-Dispatch/TN(%)									
B.2.18.18.1.1										
P.3	Digital Loop - DS/1<10 circuits/Dispatch/TN(%)									
B.2.18.18.1.2										
P.3	Digital Loop - DS/1<10 circuits/Non-Dispatch/TN(%)									
B.2.18.19.1.1										
P.3	Digital Loop > DS/1<10 circuits/Dispatch/TN(%)									
B.2.18.19.1.2										
P.3	Digital Loop > DS/1<10 circuits/Non-Dispatch/TN(%)									
B.2.18.19.2.1										
P.3	Digital Loop > DS/1>=10 circuits/Dispatch/TN(%)									
B.2.18.19.2.2										

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% Provisioning Troubles within 30 Days		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B2.19.1.11	P-9	Switch Ports<10 circuits/Dispatch TN(%)	R&B (POTS)	7.03%	24.971					
B2.19.1.12	P-9	Switch Ports<10 circuits/Non-Dispatch TN(%)	R&B (POTS)	3.54%	273.363					
B2.19.1.21	P-9	Switch Ports>=10 circuits/Dispatch TN(%)	R&B (POTS)	8.08%	99					
B2.19.1.22	P-9	Switch Ports>=10 circuits/Non-Dispatch TN(%)	R&B (POTS)	0.00%	43					
B2.19.2.1.1	P-9	Local Interoffice Transport<10 circuits/Dispatch TN(%)	DS/DS3			0.00%				3
B2.19.2.1.2	P-9	Local Interoffice Transport<10 circuits/Non-Dispatch TN(%)	DS/DS3			0.00%				
B2.19.2.2.1	P-9	Local Interoffice Transport>=10 circuits/Dispatch TN(%)	DS/DS3			0.00%				
B2.19.2.2.2	P-9	Local Interoffice Transport>=10 circuits/Non-Dispatch TN(%)	DS/DS3			0.00%				
B2.19.3.1.1	P-9	Loop + Port Combinations<10 circuits/Dispatch TN(%)	R&B	6.98%	25.234	33.33%	168		0.01972	-1.33610 NO
B2.19.3.1.2	P-9	Loop + Port Combinations<10 circuits/Non-Dispatch TN(%)	R&B	3.52%	274.783	3.84%	3.592		0.00310	-1.02558 YES
B2.19.3.1.3	P-9	Loop + Port Combinations<10 circuits/Dispatch TN(%)	R&B							
B2.19.3.1.4	P-9	Loop + Port Combinations<10 circuits/Non-Dispatch TN(%)	R&B							
B2.19.3.2.1	P-9	Loop + Port Combinations>=10 circuits/Dispatch TN(%)	R&B	7.78%	116	0.00%	4		0.13605	0.5703 YES
B2.19.3.2.2	P-9	Loop + Port Combinations>=10 circuits/Non-Dispatch TN(%)	R&B	0.00%	131	0.00%	1		0.00000	0.00000 YES
B2.19.3.2.3	P-9	Loop + Port Combinations<10 circuits/Dispatch TN(%)	R&B							
B2.19.3.2.4	P-9	Loop + Port Combinations<10 circuits/Non-Dispatch TN(%)	R&B							
B2.19.4.1.1	P-9	Combo Other<10 circuits/Dispatch TN(%)	R&B&D - Disp	6.61%	21.546					
B2.19.4.1.4	P-9	Combo Other<10 circuits/Non-Dispatch TN(%)	R&B&D - Disp							
B2.19.4.2.1	P-9	Combo Other>=10 circuits/Dispatch TN(%)	R&B&D - Disp	7.69%	117					
B2.19.4.2.4	P-9	Combo Other>=10 circuits/Non-Dispatch TN(%)	ADSL to Retail							
B2.19.5.1.1	P-9	XDSL (ADSL, HDSL and UCL)<10 circuits/Dispatch TN(%)	ADSL to Retail	7.48%	3.156	5.36%	168		0.02083	1.0183 YES
B2.19.5.1.2	P-9	XDSL (ADSL, HDSL and UCL)>10 circuits/Non-Dispatch TN(%)	ADSL to Retail	6.45%	280					
B2.19.5.2.1	P-9	XDSL (ADSL, HDSL and UCL)<10 circuits/Dispatch TN(%)	ADSL to Retail	0.00%	7					
B2.19.5.2.2	P-9	XDSL (ADSL, HDSL and UCL)>10 circuits/Non-Dispatch TN(%)	ADSL to Retail							
B2.19.6.1.1	P-9	LINE ISDN<10 circuits/Dispatch TN(%)	ISDN - BRI	0.23%	432	10.34%	87		0.00585	-1.79084 NO
B2.19.6.1.2	P-9	UNE ISDN<10 circuits/Non-Dispatch TN(%)	ISDN - BRI	0.2%	466					
B2.19.6.2.1	P-9	UNE ISDN>=10 circuits/Dispatch TN(%)	ISDN - BRI							
B2.19.6.2.2	P-9	UNE ISDN>=10 circuits/Non-Dispatch TN(%)	ISDN - BRI							
B2.19.7.1.1	P-9	Line Sharing<10 circuits/Dispatch TN(%)	ADSL to Retail	7.48%	3.156					
B2.19.7.1.2	P-9	Line Sharing<10 circuits/Non-Dispatch TN(%)	ADSL to Retail	6.45%	280	0.00%	13		0.06958	0.9239 YES
B2.19.7.2.1	P-9	Line Sharing>=10 circuits/Non-Dispatch TN(%)	ADSL to Retail	0.00%	7					
B2.19.8.1.1	P-9	2W Analog Loop Design<10 circuits/Dispatch TN(%)	R&B - Disp	6.98%	25.234	7.09%	282		0.01526	-0.0730 YES
B2.19.8.1.2	P-9	2W Analog Loop Design<10 circuits/Non-Dispatch TN(%)	R&B - Disp	6.98%	25.234					
B2.19.8.2.1	P-9	2W Analog Loop Design<10 circuits/Dispatch TN(%)	R&B - Disp	7.76%	116	0.00%	2		0.19079	0.4067 YES
B2.19.8.2.2	P-9	2W Analog Loop Design<10 circuits/Non-Dispatch TN(%)	R&B - Disp	7.76%	116					
B2.19.9.1.1	P-9	2W Analog Loop Non-Design<10 circuits/Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	7.03%	24.971	0.00%	4		0.12785	0.5500 YES
B2.19.9.1.4	P-9	2W Analog Loop Non-Design<10 circuits/Non-Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	3.58%	158.327					
B2.19.9.2.1	P-9	2W Analog Loop WINP Non-Design<10 circuits/Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	8.08%	89					
B2.19.9.2.4	P-9	2W Analog Loop WINP Non-Design<10 circuits/Non-Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	0.00%	24					
B2.19.10.1.1	P-9	2W Analog Loop WINP Design<10 circuits/Dispatch TN(%)	R&B - Disp	6.98%	25.234					
B2.19.10.1.2	P-9	2W Analog Loop WINP Design<10 circuits/Non-Dispatch TN(%)	R&B - Disp	6.98%	25.234					
B2.19.10.2.1	P-9	2W Analog Loop WINP Design<10 circuits/Dispatch TN(%)	R&B - Disp	7.76%	116					
B2.19.10.2.2	P-9	2W Analog Loop WINP Design<10 circuits/Non-Dispatch TN(%)	R&B - Disp	7.76%	116					
B2.19.11.1.1	P-9	2W Analog Loop WINP Non-Design<10 circuits/Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	7.03%	24.971					
B2.19.11.1.4	P-9	2W Analog Loop WINP Non-Design<10 circuits/Non-Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	3.58%	158.627					
B2.19.11.2.1	P-9	2W Analog Loop WINP Non-Design>=10 circuits/Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	8.08%	99					
B2.19.11.2.2	P-9	2W Analog Loop WINP Non-Design>=10 circuits/Non-Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	0.00%	24					
B2.19.12.1.1	P-9	2W Analog Loop w/NP Non-Design<10 circuits/Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	6.98%	25.234	22.22%	171		0.01955	-7.7972 NO
B2.19.12.1.2	P-9	2W Analog Loop w/NP Non-Design<10 circuits/Non-Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	6.98%	25.234					
B2.19.12.2.1	P-9	2W Analog Loop w/NP Non-Design>=10 circuits/Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	7.76%	116	0.00%	1		0.26867	0.2888 YES
B2.19.12.2.2	P-9	2W Analog Loop w/NP Non-Design>=10 circuits/Non-Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	7.76%	116					
B2.19.13.1.1	P-9	2W Analog Loop w/NP Non-Design<10 circuits/Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	7.03%	24.971	0.00%	7		0.09865	0.7276 YES
B2.19.13.1.2	P-9	2W Analog Loop w/NP Non-Design<10 circuits/Non-Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	3.58%	158.627					
B2.19.13.1.4	P-9	2W Analog Loop w/NP Non-Design>=10 circuits/Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	8.08%	99					
B2.19.13.2.4	P-9	2W Analog Loop w/NP Non-Design>=10 circuits/Non-Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	0.00%	24					
B2.19.14.1.1	P-9	Other Design<10 circuits/Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	2.34%	2.312	62.56%	8		0.05676	-10.5458 NO
B2.19.14.1.2	P-9	Other Design<10 circuits/Non-Dispatch TN(%)	R&B (POTS) excl SB Or R&B (POTS) excl SB Or	0.00%	137					

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Average Completion Notice Interval - Mechanized	
P.5	Switch Ports<10 circuits/Non-Dispatch/T(N)hours)
P.5	Switch Ports>=10 circuits/Non-Dispatch/T(N)hours)
P.5	Switch Ports>=10 circuits/Dispatch/T(N)hours)
P.5	Local Interoffice Transport<10 circuits/Dispatch/T(N)hours)
P.5	Local Interoffice Transport<10 circuits/Non-Dispatch/T(N)hours)
P.5	Local Interoffice Transport>=10 circuits/Non-Dispatch/T(N)hours)
P.5	Loop + Port Combinations<10 circuits/Dispatch/T(N)hours)
P.5	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N)hours)
P.5	Loop + Port Combinations<10 circuits/Dispatch/T(N)hours)
P.5	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N)hours)
P.5	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N)hours)
P.5	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N)hours)
P.5	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N)hours)
P.5	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N)hours)
P.5	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N)hours)
P.5	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N)hours)
P.5	Combo Other<10 circuits/Dispatch/T(N)hours)
P.5	Combo Other>=10 circuits/Dispatch/T(N)hours)
P.5	xDSL (ADSL, HDSL and UCL)<10 circuits/Dispatch/T(N)hours)
P.5	xDSL (ADSL, HDSL and UCL)<=10 circuits/Non-Dispatch/T(N)hours)
P.5	xDSL (ADSL, HDSL and UCL)>=10 circuits/Non-Dispatch/T(N)hours)
P.5	UNE ISDN<10 circuits/Non-Dispatch/T(N)hours)
P.5	UNE ISDN<=10 circuits/Non-Dispatch/T(N)hours)
P.5	UNE ISDN>=10 circuits/Non-Dispatch/T(N)hours)
P.5	Line Sharing<10 circuits/Non-Dispatch/T(N)hours)
P.5	Line Sharing>=10 circuits/Non-Dispatch/T(N)hours)
P.5	Line Sharing<=10 circuits/Dispatch/T(N)hours)
P.5	2W Analog Loop Design<10 circuits/Non-Dispatch/T(N)hours)
P.5	2W Analog Loop Design<10 circuits/Dispatch/T(N)hours)
P.5	2W Analog Loop Design>=10 circuits/Non-Dispatch/T(N)hours)

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z Score	Equity	Design	
									Design	Design
B.2.19.14.2.1	P.9	Other Design	>=10 circuits/Dispatch/T(N)%	1						
B.2.19.14.2.2	P.9	Other Design	>=10 circuits/Non-Dispatch/T(N)%							
B.2.19.15.1.1	P.9	Other Non-Design	<10 circuits/Non-Dispatch/T(N)%							
B.2.19.15.1.2	P.9	Other Non-Design	<10 circuits/Non-Dispatch/T(N)%							
B.2.19.15.2.1	P.9	Other Non-Design	>=10 circuits/Dispatch/T(N)%							
B.2.19.15.2.2	P.9	Other Non-Design	<10 circuits/Non-Dispatch/T(N)%							
B.2.19.16.1.1	P.9	IMP (Standalone)<>10 circuits/Dispatch/T(N)%								
B.2.19.16.1.2	P.9	IMP (Standalone)<>10 circuits/Non-Dispatch/T(N)%								
B.2.19.16.2.1	P.9	IMP (Standalone)>=10 circuits/Dispatch/T(N)%								
B.2.19.16.2.2	P.9	IMP (Standalone)>=10 circuits/Non-Dispatch/T(N)%								
B.2.19.17.1.1	P.9	LNP (Standalone)<>10 circuits/Dispatch/T(N)%								
B.2.19.17.1.2	P.9	LNP (Standalone)>=10 circuits/Dispatch/T(N)%								
B.2.19.17.2.1	P.9	LNP (Standalone)>=10 circuits/Non-Dispatch/T(N)%								
B.2.19.17.2.2	P.9	LNP (Standalone)>=10 circuits/Non-Dispatch/T(N)%								
B.2.19.18.1.1	P.9	Digital Loop < DS1<10 circuits/Dispatch/T(N)%								
B.2.19.18.1.2	P.9	Digital Loop < DS1<10 circuits/Non-Dispatch/T(N)%								
B.2.19.18.2.1	P.9	Digital Loop < DS1>=10 circuits/Dispatch/T(N)%								
B.2.19.18.2.2	P.9	Digital Loop < DS1>=10 circuits/Non-Dispatch/T(N)%								
B.2.19.19.1.1	P.9	Digital Loop > DS1<10 circuits/Dispatch/T(N)%								
B.2.19.19.1.2	P.9	Digital Loop > DS1<10 circuits/Non-Dispatch/T(N)%								
B.2.19.19.2.1	P.9	Digital Loop > DS1>=10 circuits/Dispatch/T(N)%								
B.2.19.19.2.2	P.9	Digital Loop > DS1>=10 circuits/Non-Dispatch/T(N)%								
Average Completion Notice Interval - Mechanized										
B.2.21.1.1.1	P.5	Switch Ports<10 circuits/Non-Dispatch/T(N)hours)								
B.2.21.1.1.2	P.5	Switch Ports>=10 circuits/Non-Dispatch/T(N)hours)								
B.2.21.1.2.1	P.5	Switch Ports>=10 circuits/Dispatch/T(N)hours)								
B.2.22.1.1.1	P.5	Local Interoffice Transport<10 circuits/Dispatch/T(N)hours)								
B.2.22.1.2.1	P.5	Local Interoffice Transport<10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.2.2	P.5	Local Interoffice Transport>=10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.3.1.1	P.5	Loop + Port Combinations<10 circuits/Dispatch/T(N)hours)								
B.2.22.1.3.1.2	P.5	Loop + Port Combinations<10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.3.1.3	P.5	Loop + Port Combinations<10 circuits/Switch Based Orders/T(N)hours)								
B.2.22.1.3.1.4	P.5	Loop + Port Combinations<10 circuits/Dispatch In/T(N)hours)								
B.2.22.1.3.2.1	P.5	Loop + Port Combinations<10 circuits/Non-Dispatch In/T(N)hours)								
B.2.22.1.3.2.2	P.5	Loop + Port Combinations<=10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.3.2.3	P.5	Loop + Port Combinations>=10 circuits/Dispatch In/T(N)hours)								
B.2.22.1.3.2.4	P.5	Combo Other<10 circuits/Dispatch/T(N)hours)								
B.2.22.1.4.1	P.5	Combo Other<10 circuits/Dispatch In/T(N)hours)								
B.2.22.1.4.1.4	P.5	Combo Other>=10 circuits/Dispatch/T(N)hours)								
B.2.22.1.4.2.4	P.5	Combo Other>=10 circuits/Dispatch In/T(N)hours)								
B.2.22.1.5.1.1	P.5	xDSL (ADSL, HDSL and UCL)<10 circuits/Dispatch/T(N)hours)								
B.2.22.1.5.1.2	P.5	xDSL (ADSL, HDSL and UCL)<=10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.5.2.1	P.5	xDSL (ADSL, HDSL and UCL)>=10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.5.2.2	P.5	xDSL (ADSL, HDSL and UCL)>>10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.6.1.1	P.5	UNE ISDN<10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.6.2.1	P.5	UNE ISDN<=10 circuits/Dispatch/T(N)hours)								
B.2.22.1.6.2.2	P.5	UNE ISDN>=10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.7.1.1	P.5	Line Sharing<10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.7.1.2	P.5	Line Sharing<=10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.7.2.1	P.5	Line Sharing>=10 circuits/Dispatch/T(N)hours)								
B.2.22.1.7.2.2	P.5	Line Sharing>>10 circuits/Dispatch/T(N)hours)								
B.2.22.1.8.1.1	P.5	2W Analog Loop Design<10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.8.1.2	P.5	2W Analog Loop Design<10 circuits/Dispatch/T(N)hours)								
B.2.22.1.8.2.1	P.5	2W Analog Loop Design>=10 circuits/Non-Dispatch/T(N)hours)								
B.2.22.1.8.2.2	P.5	2W Analog Loop Design>>10 circuits/Non-Dispatch/T(N)hours)								

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		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
P.5	2W Analog Loop WinP Non-Design<10 circuits/Dispatch TN(hours)	R&B (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	2.18	14.792			55.593			
P.5	2W Analog Loop Non-Design<10 circuits/Dispatch TN(hours)	RBB (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	1.39	126.449			6.890			
P.5	2W Analog Loop Non-Design>=10 circuits/Dispatch TN(hours)	R&B Disp R&B Disp R&B Disp R&B Disp R&B Disp	1.41	52	4		6.554			
P.5	2W Analog Loop WinP Design<10 circuits/Dispatch TN(hours)	RBB (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	0.56		2.21	14.948			0.277	
P.5	2W Analog Loop WinP Design>=10 circuits/Non-Dispatch TN(hours)	R&B Disp R&B Disp R&B Disp R&B Disp R&B Disp	0.55	60	2.21	14.948			55.390	
P.5	2W Analog Loop WinP Design>=10 circuits/Non-Dispatch TN(hours)	R&B (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	1.55	60	2.21	14.948			6.511	
P.5	2W Analog Loop WinP Non-Design<10 circuits/Dispatch TN(hours)	RBB (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	2.18	14.792			55.593			
P.5	2W Analog Loop Non-Design<10 circuits/Dispatch TN(hours)	RBB (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	1.39	126.449			6.890			
P.5	2W Analog Loop WinP Non-Design<10 circuits/Dispatch TN(hours)	R&B Disp R&B Disp R&B Disp R&B Disp R&B Disp	1.41	52			6.554			
P.5	2W Analog Loop WinP Non-Design>=10 circuits/Dispatch TN(hours)	RBB (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	0.56	4			0.277			
P.5	2W Analog Loop WinP Non-Design>=10 circuits/Non-Dispatch TN(hours)	R&B (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	2.21	14.948	29.00	56	55.390	7.41568	-3.6130	NO
B22111.1	2W Analog Loop WinP Non-Design<10 circuits/Dispatch TN(hours)	R&B Disp R&B Disp R&B Disp R&B Disp R&B Disp	2.21	14.948			55.390			
B22111.1	2W Analog Loop Non-Design<10 circuits/Dispatch TN(hours)	R&B Disp R&B Disp R&B Disp R&B Disp R&B Disp	1.55	60			6.511			
B22111.4	2W Analog Loop WinP Non-Design<10 circuits/Dispatch TN(hours)	RBB (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	2.18	14.792			55.593			
B22112.1	2W Analog Loop WinP Non-Design<10 circuits/Dispatch TN(hours)	RBB (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	1.39	126.449			6.890			
B22112.1	2W Analog Loop WinP Non-Design>=10 circuits/Dispatch TN(hours)	R&B Disp R&B Disp R&B Disp R&B Disp R&B Disp	1.41	52			6.554			
B22112.4	2W Analog Loop WinP Non-Design<10 circuits/Dispatch TN(hours)	RBB (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	0.56	4			0.277			
B22112.11	2W Analog Loop WinP Non-Design>=10 circuits/Non-Dispatch TN(hours)	R&B (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	2.21	14.948	29.00	56	55.390	7.41568	-3.6130	NO
B22112.12	2W Analog Loop WinP Non-Design<10 circuits/Non-Dispatch TN(hours)	R&B Disp R&B Disp R&B Disp R&B Disp R&B Disp	2.21	14.948			55.390			
B22112.21	2W Analog Loop WinP Non-Design>=10 circuits/Non-Dispatch TN(hours)	R&B Disp R&B Disp R&B Disp R&B Disp R&B Disp	1.55	60			6.511			
B22112.22	2W Analog Loop WinP Non-Design<10 circuits/Dispatch TN(hours)	RBB (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	2.18	14.792			55.593			
B22113.11	2W Analog Loop WinP Non-Design<10 circuits/Dispatch TN(hours)	RBB (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	1.39	126.449			6.890			
B22113.14	2W Analog Loop WinP Non-Design<10 circuits/Dispatch TN(hours)	R&B Disp R&B Disp R&B Disp R&B Disp R&B Disp	1.41	52			6.554			
B22113.21	2W Analog Loop WinP Non-Design>=10 circuits/Dispatch TN(hours)	RBB (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	0.56	4			0.277			
B22113.22	2W Analog Loop WinP Non-Design<10 circuits/Non-Dispatch TN(hours)	R&B (POTS) excl SB Or RBB (POTS) excl SB Or RBB (POTS) excl SB Or	1.39	126.449			6.890			
B22114.1.1	Other Non-Design<10 circuits/Dispatch TN(hours)	Design Design Design Design	1.962 388 3 10		139.30 214.50 82.86 0.74	1.962 388 3 10	630.507 586.190 143.495 0.252			
B22114.1.1	Other Non-Design>=10 circuits/Dispatch TN(hours)	R&B R&B R&B R&B	1.69 60 77 14.948		224.911 1.41 52 2.21	1.69 60 77 14.948	8841 6.511 19.895 6.554			
B22114.12	Other Non-Design<10 circuits/Non-Dispatch TN(hours)	R&B R&B R&B R&B	1.55 60 77 14.948		224.911 1.41 52 2.21	1.55 60 77 14.948	8841 6.511 19.895 6.554			
B22114.21	Other Non-Design>=10 circuits/Non-Dispatch TN(hours)	R&B R&B R&B R&B	1.49 77 52 2.21		224.911 1.41 52 2.21	1.49 77 52 2.21	8841 6.511 19.895 6.554			
B22114.22	Other Non-Design<10 circuits/Non-Dispatch TN(hours)	R&B R&B R&B R&B	1.49 77 52 2.21		224.911 1.41 52 2.21	1.49 77 52 2.21	8841 6.511 19.895 6.554			
B22115.1.1	Other Non-Design<10 circuits/Dispatch TN(hours)	R&B R&B R&B R&B	1.69 60 77 14.948		224.911 1.41 52 2.21	1.69 60 77 14.948	8841 6.511 19.895 6.554			
B22115.2.1	Other Non-Design>=10 circuits/Dispatch TN(hours)	R&B R&B R&B R&B	1.55 60 77 14.948		224.911 1.41 52 2.21	1.55 60 77 14.948	8841 6.511 19.895 6.554			
B22115.22	Other Non-Design<10 circuits/Non-Dispatch TN(hours)	R&B R&B R&B R&B	1.49 77 52 2.21		224.911 1.41 52 2.21	1.49 77 52 2.21	8841 6.511 19.895 6.554			
B22116.1.1	NP (Standalone)<10 circuits/Non-Dispatch TN(hours)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS)	1.69 224.911 1.41 52		224.911 1.41 52 2.21	1.69 224.911 1.41 52	8841 6.511 19.895 6.554			
B22116.1.2	NP (Standalone)>=10 circuits/Non-Dispatch TN(hours)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS)	0.70 15 2.21 14.948		224.911 1.41 52 2.21	0.70 15 2.21 14.948	8841 6.511 19.895 6.554			
B22116.21	NP (Standalone)<=10 circuits/Dispatch TN(hours)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS)	0.70 15 2.21 14.948		224.911 1.41 52 2.21	0.70 15 2.21 14.948	8841 6.511 19.895 6.554			
B22116.22	NP (Standalone)>=10 circuits/Non-Dispatch TN(hours)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS)	0.70 15 2.21 14.948		224.911 1.41 52 2.21	0.70 15 2.21 14.948	8841 6.511 19.895 6.554			
B22117.1.1	NP (Standalone)<10 circuits/Dispatch TN(hours)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS)	0.70 15 2.21 14.948		224.911 1.41 52 2.21	0.70 15 2.21 14.948	8841 6.511 19.895 6.554			
B22117.1.2	NP (Standalone)<10 circuits/Non-Dispatch TN(hours)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS)	0.70 15 2.21 14.948		224.911 1.41 52 2.21	0.70 15 2.21 14.948	8841 6.511 19.895 6.554			
B22117.2.1	NP (Standalone)>=10 circuits/Dispatch TN(hours)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS)	0.70 15 2.21 14.948		224.911 1.41 52 2.21	0.70 15 2.21 14.948	8841 6.511 19.895 6.554			
B22117.22	NP (Standalone)>=10 circuits/Non-Dispatch TN(hours)	R&B (POTS) R&B (POTS) R&B (POTS) R&B (POTS)	0.70 15 2.21 14.948		224.911 1.41 52 2.21	0.70 15 2.21 14.948	8841 6.511 19.895 6.554			
B22118.1.1	Digital Loop <10 circuits/Dispatch TN(hours)	Digital Loop <DS1 Digital Loop <DS1 Digital Loop <DS1 Digital Loop <DS1	2.56 4 2.56 4		173.3174 1.41 41.00	173.3174 1.41 41.00	3998 5.093			
B22118.1.2	Digital Loop <10 circuits/Non-Dispatch TN(hours)	Digital Loop <DS1 Digital Loop <DS1 Digital Loop <DS1 Digital Loop <DS1	2.56 4 2.56 4		173.3174 1.41 41.00	173.3174 1.41 41.00	3998 5.093			
B22118.21	Digital Loop < DS1<10 circuits/Dispatch TN(hours)	Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1	2.56 4 2.56 4		173.3174 1.41 41.00	173.3174 1.41 41.00	3998 5.093			
B22118.22	Digital Loop < DS1>=10 circuits/Dispatch TN(hours)	Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1	2.56 4 2.56 4		173.3174 1.41 41.00	173.3174 1.41 41.00	3998 5.093			
B22119.1.1	Digital Loop < DS1<10 circuits/Non-Dispatch TN(hours)	Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1	2.56 4 2.56 4		173.3174 1.41 41.00	173.3174 1.41 41.00	3998 5.093			
B22119.1.2	Digital Loop < DS1>=10 circuits/Non-Dispatch TN(hours)	Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1	2.56 4 2.56 4		173.3174 1.41 41.00	173.3174 1.41 41.00	3998 5.093			
B22119.21	Digital Loop < DS1<10 circuits/Dispatch TN(hours)	Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1	2.56 4 2.56 4		173.3174 1.41 41.00	173.3174 1.41 41.00	3998 5.093			
B22119.22	Digital Loop < DS1>=10 circuits/Non-Dispatch TN(hours)	Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1 Digital Loop < DS1	2.56 4 2.56 4		173.3174 1.41 41.00	173.3174 1.41 41.00	3998 5.093			
B22119.31	Average Completion Notice Interval - Non-Mechanized	[REDACTED]								
P.5	Switch Ports<10 circuits/Non-Dispatch TN(hours)	[REDACTED]								
P.5	Switch Ports>10 circuits/Non-Dispatch TN(hours)	[REDACTED]								
B22212.1	Local Interoffice Transport<10 circuits/Non-Dispatch TN(hours)	[REDACTED]								
P.5	Local Interoffice Transport<10 circuits/Dispatch TN(hours)	[REDACTED]								
B22222.1	Local Interoffice Transport<10 circuits/Non-Dispatch TN(hours)	[REDACTED]								
P.5	Local Interoffice Transport<10 circuits/Dispatch TN(hours)	[REDACTED]								
B22231.1	Loop + Port Combinations<10 circuits/Non-Dispatch TN(hours)	[REDACTED]								
P.5	Loop + Port Combinations<10 circuits/Dispatch TN(hours)	[REDACTED]								
B22231.3	Loop + Port Combinations<10 circuits/Non-Dispatch TN(hours)	[REDACTED]								
P.5	Loop + Port Combinations<10 circuits/Dispatch TN(hours)	[REDACTED]								
B22232.1	Loop + Port Combinations<10 circuits/Non-Dispatch TN(hours)	[REDACTED]								
P.5	Loop + Port Combinations<10 circuits/Dispatch TN(hours)	[REDACTED]								
B22232.2	Loop + Port Combinations<10 circuits/Non-Dispatch TN(hours)	[REDACTED]								
P.5	Loop + Port Combinations<10 circuits/Dispatch TN(hours)	[REDACTED]								
Data included in B.2.23.1-1										
Data included in B.2.22.3.1-2										
Data included in B.2.22.3.1-3										
Data included in B.2.22.3.2-1										
Data included in B.2.22.3.2-2										

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	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Zscore	Equity
P.5	Digital Loop < DS1<10 circuits/Non-Dispatch/N(hours)								Diagnostic
B.222 18.12	Digital Loop < DS1>=10 circuits/Dispatch/N(hours)								Diagnostic
B.222 18.21	Digital Loop < DS1/2<=10 circuits/Non-Dispatch/N(hours)								Diagnostic
P.5	Digital Loop < DS1/2>=10 circuits/Non-Dispatch/N(hours)								Diagnostic
B.222 18.22	Digital Loop >= DS1/2<10 circuits/Dispatch/N(hours)								Diagnostic
B.222 19.11	Digital Loop >= DS1/2>=10 circuits/Non-Dispatch/N(hours)								Diagnostic
B.222 19.12	Digital Loop >> DS1/2<10 circuits/Dispatch/N(hours)								Diagnostic
P.5	Digital Loop >> DS1/2>=10 circuits/Non-Dispatch/N(hours)								Diagnostic
B.222 19.21	Digital Loop >> DS1/2>=10 circuits/Dispatch/N(hours)								Diagnostic
P.5	Digital Loop >> DS1/2>=10 circuits/Non-Dispatch/N(hours)								Diagnostic
Total Service Order Cycle Time - Mechanized									
B.224 1.11	P.10 Switch Ports<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 1.12	P.10 Switch Ports<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 1.12	P.10 Switch Ports<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 1.12	P.10 Switch Ports<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 2.11	P.10 Local Interoffice Transport<10 circuits/Dispatch/N(days)								Diagnostic
B.224 2.12	P.10 Local Interoffice Transport<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 2.22	P.10 Local Interoffice Transport=>10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 2.22	P.10 Local Interoffice Transport=>10 circuits/Dispatch/N(days)								Diagnostic
B.224 3.11	P.10 Loop + Port Combinations<10 circuits/Dispatch/N(days)								Diagnostic
B.224 3.12	P.10 Loop + Port Combinations<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 3.21	P.10 Loop + Port Combinations=>10 circuits/Dispatch/N(days)								Diagnostic
B.224 3.22	P.10 Loop + Port Combinations=>10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 4.11	P.10 Combo Other<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 4.12	P.10 Combo Other<10 circuits/Dispatch/N(days)								Diagnostic
B.224 4.21	P.10 Combo Other=>10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 4.22	P.10 Combo Other=>10 circuits/Dispatch/N(days)								Diagnostic
B.224 5.11	P.10 xDSL (ADSL, HDSL, and UCL)<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 5.12	P.10 xDSL (ADSL, HDSL, and UCL)>=10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 5.21	P.10 xDSL (ADSL, HDSL, and UCL)>=10 circuits/Dispatch/N(days)								Diagnostic
B.224 5.22	P.10 xDSL (ADSL, HDSL, and UCL)<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 6.11	P.10 UNE (SDN)<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 6.12	P.10 UNE (SDN)<10 circuits/Dispatch/N(days)								Diagnostic
B.224 6.21	P.10 UNE (SDN)=10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 6.22	P.10 UNE (SDN)=10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 7.11	P.10 Line Sharing<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 7.12	P.10 Line Sharing<10 circuits/Dispatch/N(days)								Diagnostic
B.224 7.21	P.10 Line Sharing=>10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 7.22	P.10 Line Sharing=>10 circuits/Dispatch/N(days)								Diagnostic
B.224 8.11	P.10 2W Analog Loop Design<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 8.12	P.10 2W Analog Loop Design<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 8.21	P.10 2W Analog Loop Design=>10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 8.22	P.10 2W Analog Loop Design=>10 circuits/Dispatch/N(days)								Diagnostic
B.224 9.11	P.10 2W Analog Loop Non-Design<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 9.12	P.10 2W Analog Loop Non-Design=>10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 9.21	P.10 2W Analog Loop w/NP Non-Design<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 9.22	P.10 2W Analog Loop w/NP Non-Design=>10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 10.11	P.10 2W Analog Loop w/NP Design<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 11.21	P.10 2W Analog Loop w/NP Design=>10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 10.21	P.10 2W Analog Loop w/NP Design>=10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 10.22	P.10 2W Analog Loop w/NP Design>=10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 11.11	P.10 2W Analog Loop w/NP Non-Design<10 circuits/Dispatch/N(days)								Diagnostic
B.224 11.12	P.10 2W Analog Loop w/NP Non-Design=>10 circuits/Dispatch/N(days)								Diagnostic
B.224 11.22	P.10 2W Analog Loop w/NP Non-Design>=10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 12.11	P.14 2W Analog Loop w/NP Design<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 12.12	P.14 2W Analog Loop w/NP Design=>10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 12.21	P.14 2W Analog Loop w/NP Non-Design<10 circuits/Dispatch/N(days)								Diagnostic
B.224 12.22	P.14 2W Analog Loop w/NP Non-Design=>10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 13.11	P.14 2W Analog Loop w/NP Non-Design<10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 13.12	P.14 2W Analog Loop w/NP Non-Design=>10 circuits/Non-Dispatch/N(days)								Diagnostic
B.224 13.21	P.14 2W Analog Loop w/NP Non-Design>=10 circuits/Dispatch/N(days)								Diagnostic

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		Total Service Order Cycle Time - Partially Mechanized					
B.224.13.22	P-14	2W Analog Loop w/1NP Non-Design>=10 circuits/Non-Dispatch/TN(days)					
B.224.14.11	P-10	Other Design<10 circuits/Dispatch/TN(days)					
B.224.14.12	P-10	Other Design<10 circuits/Non-Dispatch/TN(days)					
B.224.14.21	P-10	Other Design>=10 circuits/Non-Dispatch/TN(days)					
B.224.14.22	P-10	Other Design>=10 circuits/Non-Dispatch/TN(days)					
B.224.15.11	P-10	Other Non-Design<10 circuits/Dispatch/TN(days)					
B.224.15.12	P-10	Other Non-Design<10 circuits/Non-Dispatch/TN(days)					
B.224.15.21	P-10	Other Non-Design>=10 circuits/Dispatch/TN(days)					
B.224.15.22	P-10	Other Non-Design>=10 circuits/Non-Dispatch/TN(days)					
B.224.16.11	P-10	INP (Standalone)<10 circuits/Dispatch/TN(days)					
B.224.16.12	P-10	INP (Standalone)<10 circuits/Non-Dispatch/TN(days)					
B.224.16.21	P-10	INP (Standalone)>=10 circuits/Dispatch/TN(days)					
B.224.16.22	P-10	INP (Standalone)>=10 circuits/Non-Dispatch/TN(days)					
B.224.17.11	P-14	LNP (Standalone)<10 circuits/Dispatch/TN(days)					
B.224.17.12	P-14	LNP (Standalone)<10 circuits/Non-Dispatch/TN(days)					
B.224.17.21	P-14	LNP (Standalone)>=10 circuits/Non-Dispatch/TN(days)					
B.224.17.22	P-14	LNP (Standalone)>=10 circuits/Non-Dispatch/TN(days)					
B.224.18.11	P-10	Digital Loop < DS1<10 circuits/Dispatch/TN(days)					
B.224.18.12	P-10	Digital Loop < DS1<10 circuits/Non-Dispatch/TN(days)					
B.224.18.21	P-10	Digital Loop < DS1>=10 circuits/Non-Dispatch/TN(days)					
B.224.18.22	P-10	Digital Loop < DS1>=10 circuits/Dispatch/TN(days)					
B.224.19.11	P-10	Digital Loop >= DS1<10 circuits/Dispatch/TN(days)					
B.224.19.12	P-10	Digital Loop >= DS1<10 circuits/Non-Dispatch/TN(days)					
B.224.19.21	P-10	Digital Loop >= DS1>=10 circuits/Dispatch/TN(days)					
B.224.19.22	P-10	Digital Loop >= DS1>=10 circuits/Non-Dispatch/TN(days)					

		Total Service Order Cycle Time - Fully Mechanized					
B.225.11.1	P-10	Switch Ports<10 circuits/Dispatch/TN(days)					
B.226.1.12	P-10	Switch Ports<10 circuits/Non-Dispatch/TN(days)					
B.225.12.1	P-10	Switch Ports>=10 circuits/Dispatch/TN(days)					
B.225.12.2	P-10	Switch Ports>=10 circuits/Non-Dispatch/TN(days)					
B.225.2.11	P-10	Local Interoffice Transport<10 circuits/Dispatch/TN(days)					
B.225.2.12	P-10	Local Interoffice Transport<10 circuits/Non-Dispatch/TN(days)					
B.225.2.21	P-10	Local Interoffice Transport>=10 circuits/Dispatch/TN(days)					
B.225.2.22	P-10	Local Interoffice Transport>=10 circuits/Non-Dispatch/TN(days)					
B.225.3.11	P-10	Loop + Port Combinations<10 circuits/Dispatch/TN(days)					
B.225.3.12	P-10	Loop + Port Combinations<10 circuits/Non-Dispatch/TN(days)					
B.225.3.21	P-10	Loop + Port Combinations>=10 circuits/Dispatch/TN(days)					
B.225.3.22	P-10	Loop + Port Combinations>=10 circuits/Non-Dispatch/TN(days)					
B.225.4.1.1	P-10	Combo Other<10 circuits/Dispatch/TN(days)					
B.225.4.1.2	P-10	Combo Other<10 circuits/Non-Dispatch/TN(days)					
B.225.4.2.1	P-10	Combo Other>=10 circuits/Dispatch/TN(days)					
B.225.4.2.2	P-10	Combo Other>=10 circuits/Non-Dispatch/TN(days)					
B.225.6.1.1	P-10	UNE ISDN<10 circuits/Non-Dispatch/TN(days)					
B.225.6.1.2	P-10	UNE ISDN>=10 circuits/Dispatch/TN(days)					
B.225.6.2.1	P-10	xDSL (ADSL, HDSL and UCL)<10 circuits/Non-Dispatch/TN(days)					
B.225.6.2.2	P-10	xDSL (ADSL, HDSL and UCL)>=10 circuits/Non-Dispatch/TN(days)					
B.225.7.1.1	P-10	Line Shaving<10 circuits/Non-Dispatch/TN(days)					
B.225.7.1.2	P-10	Line Shaving>=10 circuits/Non-Dispatch/TN(days)					
B.225.7.2.1	P-10	Line Shaving<10 circuits/Non-Dispatch/TN(days)					
B.225.7.2.2	P-10	Line Shaving>=10 circuits/Non-Dispatch/TN(days)					
B.225.8.1.1	P-10	2W Analog Loop Design<10 circuits/Dispatch/TN(days)					
B.225.8.1.2	P-10	2W Analog Loop Design<10 circuits/Non-Dispatch/TN(days)					
B.225.8.2.1	P-10	2W Analog Loop Design>=10 circuits/Dispatch/TN(days)					
B.225.8.2.2	P-10	2W Analog Loop Design>=10 circuits/Non-Dispatch/TN(days)					
B.225.9.1.1	P-10	2W Analog Loop Non-Design<10 circuits/Dispatch/TN(days)					

		Benchmark / Analog					
		BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error
B.224.13.22	P-14						
B.224.14.11	P-10						
B.224.14.12	P-10						
B.224.14.21	P-10						
B.224.14.22	P-10						
B.224.15.11	P-10						
B.224.15.12	P-10						
B.224.15.21	P-10						
B.224.15.22	P-10						
B.224.16.11	P-10						
B.224.16.12	P-10						
B.224.16.21	P-10						
B.224.16.22	P-10						
B.224.17.11	P-14						
B.224.17.12	P-14						
B.224.17.21	P-14						
B.224.17.22	P-14						
B.224.18.11	P-10						
B.224.18.12	P-10						
B.224.18.21	P-10						
B.224.18.22	P-10						
B.224.19.11	P-10						
B.224.19.12	P-10						
B.224.19.21	P-10						
B.224.19.22	P-10						
B.225.11.1	P-10						
B.226.1.12	P-10						
B.225.12.1	P-10						
B.225.12.2	P-10						
B.225.2.11	P-10						
B.225.2.12	P-10						
B.225.2.21	P-10						
B.225.2.22	P-10						
B.225.3.11	P-10						
B.225.3.12	P-10						
B.225.3.21	P-10						
B.225.3.22	P-10						
B.225.4.1.1	P-10						
B.225.4.1.2	P-10						
B.225.4.2.1	P-10						
B.225.4.2.2	P-10						
B.225.5.1.1	P-10						
B.225.5.1.2	P-10						
B.225.5.2.1	P-10						
B.225.5.2.2	P-10						
B.225.6.1.1	P-10						
B.225.6.1.2	P-10						
B.225.6.2.1	P-10						
B.225.6.2.2	P-10						
B.225.7.1.1	P-10						
B.225.7.1.2	P-10						
B.225.7.2.1	P-10						
B.225.7.2.2	P-10						
B.225.8.1.1	P-10						
B.225.8.1.2	P-10						
B.225.8.2.1	P-10						
B.225.8.2.2	P-10						
B.225.9.1.1	P-10						

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		Total Service Order Cycle Time - Non-Mechanized							
P-10	Benchmark Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 226 9.1.2	2W Analog Loop w/NP Non-Design<=10 circuits/Non-Dispatch/TN(days)								
B 226 9.2.1	2W Analog Loop Non-Design>=10 circuits/Dispatch/TN(days)								
B 226 9.2.2	2W Analog Loop Non-Design>=2-10 circuits/Non-Dispatch/TN(days)								
P-10	2W Analog Loop Non-Design<=10 circuits/Non-Dispatch/TN(days)								
B 226 10.1.1	2W Analog Loop w/NP Design<=10 circuits/Non-Dispatch/TN(days)								
B 226 10.1.2	2W Analog Loop w/NP Design<=10 circuits/Non-Dispatch/TN(days)								
P-10	2W Analog Loop w/NP Design>=10 circuits/Non-Dispatch/TN(days)								
B 226 10.2.1	2W Analog Loop w/NP Design>=10 circuits/Dispatch/TN(days)								
P-10	2W Analog Loop w/NP Design>=10 circuits/Non-Dispatch/TN(days)								
B 226 10.2.2	2W Analog Loop w/NP Non-Design<=10 circuits/Dispatch/TN(days)								
P-10	2W Analog Loop w/NP Non-Design<=10 circuits/Non-Dispatch/TN(days)								
B 226 11.1.1	2W Analog Loop w/NP Non-Design<=10 circuits/Non-Dispatch/TN(days)								
P-10	2W Analog Loop w/NP Non-Design<>10 circuits/Non-Dispatch/TN(days)								
B 226 11.1.2	2W Analog Loop w/NP Non-Design>=10 circuits/Dispatch/TN(days)								
P-10	2W Analog Loop w/NP Non-Design>=10 circuits/Non-Dispatch/TN(days)								
B 226 11.2.2	2W Analog Loop w/NP Non-Design>=2-10 circuits/Non-Dispatch/TN(days)								
P-10	2W Analog Loop w/NP Non-Design>=2-10 circuits/Non-Dispatch/TN(days)								
B 226 12.1.1	2W Analog Loop w/NP Design<=10 circuits/Non-Dispatch/TN(days)								
P-14	2W Analog Loop w/NP Design<=10 circuits/Dispatch/TN(days)	8.79	24						
B 226 12.1.2	2W Analog Loop w/NP Non-Design<=10 circuits/Non-Dispatch/TN(days)								
P-14	2W Analog Loop w/NP Non-Design<=10 circuits/Non-Dispatch/TN(days)								
B 226 12.2.1	2W Analog Loop w/NP Non-Design>=10 circuits/Non-Dispatch/TN(days)								
P-14	2W Analog Loop w/NP Non-Design>=10 circuits/Non-Dispatch/TN(days)								
B 226 12.2.2	2W Analog Loop w/NP Non-Design>=2-10 circuits/Non-Dispatch/TN(days)								
P-14	2W Analog Loop w/NP Non-Design>=2-10 circuits/Non-Dispatch/TN(days)								
B 226 13.1.1	2W Analog Loop w/NP Non-Design<=10 circuits/Non-Dispatch/TN(days)								
P-14	2W Analog Loop w/NP Non-Design<=10 circuits/Non-Dispatch/TN(days)								
B 226 13.1.2	2W Analog Loop w/NP Non-Design>=10 circuits/Non-Dispatch/TN(days)								
P-14	2W Analog Loop w/NP Non-Design>=10 circuits/Non-Dispatch/TN(days)								
B 226 13.2.1	2W Analog Loop w/NP Non-Design<=10 circuits/Non-Dispatch/TN(days)								
P-14	2W Analog Loop w/NP Non-Design<=10 circuits/Non-Dispatch/TN(days)								
B 226 13.2.2	2W Analog Loop w/NP Non-Design>=10 circuits/Non-Dispatch/TN(days)								
P-14	2W Analog Loop w/NP Non-Design>=10 circuits/Non-Dispatch/TN(days)								
B 226 14.1.1	P-10 Other Design<10 circuits/Non-Dispatch/TN(days)								
B 226 14.1.2	P-10 Other Design<10 circuits/Non-Dispatch/TN(days)								
B 226 14.2.1	P-10 Other Design>10 circuits/Non-Dispatch/TN(days)								
B 226 14.2.2	P-10 Other Design>10 circuits/Non-Dispatch/TN(days)								
B 226 15.1.1	P-10 Other Non-Design<10 circuits/Dispatch/TN(days)								
B 226 15.1.2	P-10 Other Non-Design<10 circuits/Non-Dispatch/TN(days)								
B 226 15.2.1	P-10 Other Non-Design>10 circuits/Dispatch/TN(days)								
B 226 15.2.2	P-10 Other Non-Design>10 circuits/Non-Dispatch/TN(days)								
B 226 16.1.1	P-10 NNP (Standalone)<10 circuits/Dispatch/TN(days)								
B 226 16.1.2	P-10 NNP (Standalone)<10 circuits/Non-Dispatch/TN(days)								
B 226 16.2.1	P-10 NNP (Standalone)>10 circuits/Dispatch/TN(days)								
B 226 16.2.2	P-10 NNP (Standalone)>10 circuits/Non-Dispatch/TN(days)								
B 226 17.1.1	P-14 LNP (Standalone)<10 circuits/Dispatch/TN(days)								
B 226 17.1.2	P-14 LNP (Standalone)>10 circuits/Non-Dispatch/TN(days)								
B 226 17.2.1	P-14 LNP (Standalone)>=10 circuits/Dispatch/TN(days)								
B 226 17.2.2	P-14 LNP (Standalone)>=10 circuits/Non-Dispatch/TN(days)								
B 226 18.1.1	P-10 Digital Loop < DS1<10 circuits/Non-Dispatch/TN(days)								
B 226 18.1.2	P-10 Digital Loop < DS1<10 circuits/Non-Dispatch/TN(days)								
B 226 18.2.1	P-10 Digital Loop < DS1>=10 circuits/Non-Dispatch/TN(days)								
B 226 18.2.2	P-10 Digital Loop >= DS1<10 circuits/Non-Dispatch/TN(days)								
B 226 19.1.1	P-10 Digital Loop >= DS1<10 circuits/Non-Dispatch/TN(days)								
B 226 19.1.2	P-10 Digital Loop >= DS1>=10 circuits/Non-Dispatch/TN(days)								
B 226 19.2.1	P-10 Digital Loop >= DS1>=10 circuits/Non-Dispatch/TN(days)								
B 226 19.2.2	P-10 Digital Loop >= DS1>=10 circuits/Non-Dispatch/TN(days)								

Total Service Order Cycle Time - Non-Mechanized

P-10	Benchmark Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
B 226 1.1.1	P-10 Switch Ports<10 circuits/Non-Dispatch/TN(days)								
B 226 1.1.2	P-10 Switch Ports>=10 circuits/Dispatch/TN(days)								
B 226 2.1	P-10 Switch Ports>=10 circuits/Non-Dispatch/TN(days)								
P-10	Switch Ports>=10 circuits/Non-Dispatch/TN(days)								
B 226 2.2	P-10 Local Interoffice Transport<10 circuits/Dispatch/TN(days)								
B 226 2.2.1	P-10 Local Interoffice Transport>=10 circuits/Non-Dispatch/TN(days)								
B 226 2.2.2	P-10 Local Interoffice Transport>=10 circuits/Non-Dispatch/TN(days)								
B 226 3.1.1	P-10 Loop + Port Combinations<10 circuits/Non-Dispatch/TN(days)								
B 226 3.1.2	P-10 Loop + Port Combinations>=10 circuits/Non-Dispatch/TN(days)								
B 226 3.2.1	P-10 Loop + Port Combinations>=10 circuits/Non-Dispatch/TN(days)								
B 226 3.2.2	P-10 Loop + Port Combinations>=10 circuits/Non-Dispatch/TN(days)								
B 226 4.1.1	P-10 Combo Other<10 circuits/Dispatch/TN(days)								
B 226 4.1.2	P-10 Combo Other<10 circuits/Non-Dispatch/TN(days)								
B 226 4.2.1	P-10 Combo Other>=10 circuits/Dispatch/TN(days)								

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Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z-Score	Equity
P-10 Combo Other/=>10 circuits/Non-Dispatch/N(days)								
B.26.4.22 P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/N(days)								
B.26.5.1.1 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/N(days)								
B.26.5.1.2 P-10 xDSL (ADSL, HDSL and UCL)/<10 circuits/Non-Dispatch/N(days)								
B.26.5.2.1 P-10 xDSL (ADSL, HDSL and UCL)/>=10 circuits/Non-Dispatch/N(days)								
B.26.5.2.2 P-10 xDSL (ADSL, HDSL and UCL)/<=10 circuits/Non-Dispatch/N(days)								
B.26.6.1.1 P-10 UNE ISDN/<10 circuits/Dispatch/N(days)								
B.26.6.1.2 P-10 UNE ISDN/<10 circuits/Dispatch/Non-Dispatch/N(days)								
B.26.6.2.1 P-10 UNE ISDN/>=10 circuits/Dispatch/N(days)								
B.26.6.2.2 P-10 Line Sharing/<10 circuits/Non-Dispatch/N(days)								
B.26.7.1.1 P-10 Line Sharing/<10 circuits/Non-Dispatch/N(days)								
B.26.7.1.2 P-10 Line Sharing/>=10 circuits/Non-Dispatch/N(days)								
B.26.7.2.1 P-10 Line Sharing/<10 circuits/Dispatch/N(days)								
B.26.7.2.2 P-10 Line Sharing/>=10 circuits/Non-Dispatch/N(days)								
B.26.8.1.1 P-10 2W Analog Loop Design/<10 circuits/Non-Dispatch/N(days)								
B.26.8.1.2 P-10 2W Analog Loop Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.8.2.1 P-10 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/N(days)								
B.26.8.2.2 P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.89.1.1 P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.9.1.2 P-10 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.9.2.1 P-10 2W Analog Loop Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.9.2.2 P-10 2W Analog Loop Non-Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.10.1.1 P-10 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/N(days)								
B.26.10.1.2 P-10 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.10.2.1 P-10 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/N(days)								
B.26.10.2.2 P-10 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.11.1.1 P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.11.1.2 P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.11.2.1 P-10 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.11.2.2 P-10 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.12.1.1 P-14 2W Analog Loop w/INP Design/<10 circuits/Non-Dispatch/N(days)								
B.26.12.1.2 P-14 2W Analog Loop w/INP Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.12.2.1 P-14 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.12.2.2 P-14 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.13.1.1 P-14 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.13.1.2 P-14 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.13.2.1 P-14 2W Analog Loop w/INP Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.13.2.2 P-14 2W Analog Loop w/INP Non-Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.14.1.1 P-10 Other Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.14.1.2 P-10 Other Non-Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.14.2.1 P-10 Other Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.14.2.2 P-10 Other Non-Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.15.1.1 P-10 Other Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.15.1.2 P-10 Other Non-Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.15.2.1 P-10 Other Non-Design/<10 circuits/Non-Dispatch/N(days)								
B.26.15.2.2 P-10 Other Non-Design/>=10 circuits/Non-Dispatch/N(days)								
B.26.16.1.1 P-10 INP (Standalone)/<10 circuits/Non-Dispatch/N(days)								
B.26.16.1.2 P-10 INP (Standalone)/>=10 circuits/Non-Dispatch/N(days)								
B.26.16.2.1 P-10 INP (Standalone)/<10 circuits/Non-Dispatch/N(days)								
B.26.16.2.2 P-10 INP (Standalone)/>=10 circuits/Non-Dispatch/N(days)								
B.26.17.1.1 P-14 LNP (Standalone)/<10 circuits/Non-Dispatch/N(days)								
B.26.17.1.2 P-14 LNP (Standalone)/>=10 circuits/Non-Dispatch/N(days)								
B.26.17.2.1 P-14 LNP (Standalone)/<10 circuits/Non-Dispatch/N(days)								
B.26.17.2.2 P-14 LNP (Standalone)/>=10 circuits/Non-Dispatch/N(days)								
B.26.18.1.1 P-10 Digital Loop < DS/1<10 circuits/Non-Dispatch/N(days)								
B.26.18.2.1 P-10 Digital Loop > DS/1<10 circuits/Non-Dispatch/N(days)								
B.26.18.2.2 P-10 Digital Loop > DS/1<10 circuits/Non-Dispatch/N(days)								
B.26.19.1.1 P-10 Digital Loop > DS/1<10 circuits/Non-Dispatch/N(days)								
B.26.19.1.2 P-10 Digital Loop > DS/1<10 circuits/Non-Dispatch/N(days)								

P-10 Diagnostic								
B.26.5.1.1 P-10 Diagnostic								
B.26.5.2.1 P-10 Diagnostic								
B.26.5.2.2 P-10 Diagnostic								
B.26.6.1.1 P-10 Diagnostic								
B.26.6.1.2 P-10 Diagnostic								
B.26.6.2.1 P-10 Diagnostic								
B.26.6.2.2 P-10 Diagnostic								
B.26.7.1.1 P-10 Diagnostic								
B.26.7.1.2 P-10 Diagnostic								
B.26.7.2.1 P-10 Diagnostic								
B.26.7.2.2 P-10 Diagnostic								
B.26.8.1.1 P-10 Diagnostic								
B.26.8.1.2 P-10 Diagnostic								
B.26.8.2.1 P-10 Diagnostic								
B.26.8.2.2 P-10 Diagnostic								
B.26.89.1.1 P-10 Diagnostic								
B.26.9.1.2 P-10 Diagnostic								
B.26.9.2.1 P-10 Diagnostic								
B.26.9.2.2 P-10 Diagnostic								
B.26.10.1.1 P-10 Diagnostic								
B.26.10.1.2 P-10 Diagnostic								
B.26.10.2.1 P-10 Diagnostic								
B.26.10.2.2 P-10 Diagnostic								
B.26.11.1.1 P-10 Diagnostic								
B.26.11.1.2 P-10 Diagnostic								
B.26.11.2.1 P-10 Diagnostic								
B.26.11.2.2 P-10 Diagnostic								
B.26.12.1.1 P-14 Diagnostic								
B.26.12.1.2 P-14 Diagnostic								
B.26.12.2.1 P-14 Diagnostic								
B.26.12.2.2 P-14 Diagnostic								
B.26.13.1.1 P-14 Diagnostic								
B.26.13.1.2 P-14 Diagnostic								
B.26.13.2.1 P-14 Diagnostic								
B.26.13.2.2 P-14 Diagnostic								
B.26.14.1.1 P-10 Diagnostic								
B.26.14.1.2 P-10 Diagnostic								
B.26.14.2.1 P-10 Diagnostic								
B.26.14.2.2 P-10 Diagnostic								
B.26.15.1.1 P-10 Diagnostic								
B.26.15.1.2 P-10 Diagnostic								
B.26.15.2.1 P-10 Diagnostic								
B.26.15.2.2 P-10 Diagnostic								
B.26.16.1.1 P-10 Diagnostic								
B.26.16.1.2 P-10 Diagnostic								
B.26.16.2.1 P-10 Diagnostic								
B.26.16.2.2 P-10 Diagnostic								
B.26.17.1.1 P-14 Diagnostic								
B.26.17.1.2 P-14 Diagnostic								
B.26.17.2.1 P-14 Diagnostic								
B.26.17.2.2 P-14 Diagnostic								
B.26.18.1.1 P-10 Diagnostic								
B.26.18.2.1 P-10 Diagnostic								
B.26.18.2.2 P-10 Diagnostic								
B.26.19.1.1 P-10 Diagnostic								
B.26.19.1.2 P-10 Diagnostic								

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	Total Service Order Cycle Time (Offered) - Mechanized	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z Score	Equity
B226.19.21	P-10 Digital Loop >= DS1 />=10 circuits/Dispatch/N(days)									
B226.19.22	P-10 Digital Loop >= DS1 />=10 circuits/Non-Dispatch/N(days)									
B228.11.1	P-10 Switch Ports<10 circuits/Dispatch/N(days)	Diagnostic								
B228.11.2	P-10 Switch Ports<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.12.1	P-10 Switch Ports>=10 circuits/Dispatch/N(days)	Diagnostic								
B228.12.2	P-10 Switch Ports>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.2.1	P-10 Local Interoffice Transport<10 circuits/Dispatch/N(days)	Diagnostic								
B228.2.12	P-10 Local Interoffice Transport<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.2.2.1	P-10 Local Interoffice Transport>=10 circuits/Dispatch/N(days)	Diagnostic								
B228.2.2.2	P-10 Local Interoffice Transport>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.3.1.1	P-10 Loop + Port Combinations<10 circuits/Dispatch/N(days)	Diagnostic								
B228.3.1.2	P-10 Loop + Port Combinations<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.3.2.1	P-10 Loop + Port Combinations>=10 circuits/Dispatch/N(days)	Diagnostic								
B228.3.2.2	P-10 Loop + Port Combinations>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.4.1.1	P-10 Combo Other<10 circuits/Dispatch/N(days)	Diagnostic								
B228.4.12	P-10 Combo Other<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.4.2.1	P-10 Combo Other>=10 circuits/Dispatch/N(days)	Diagnostic								
B228.4.2.2	P-10 Combo Other>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.5.1.1	P-10 xDSL (ADSL, HDSL and UCL)<10 circuits/Dispatch/N(days)	Diagnostic								
B228.5.1.2	P-10 xDSL (ADSL, HDSL and UCL)<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.5.2.1	P-10 xDSL (ADSL, HDSL and UCL)>=10 circuits/Dispatch/N(days)	Diagnostic								
B228.5.2.2	P-10 xDSL (ADSL, HDSL and UCL)>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.6.1.1	P-10 UNE ISDN<10 circuits/Dispatch/N(days)	Diagnostic								
B228.6.1.2	P-10 UNE ISDN<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.6.2.1	P-10 UNE ISDN>=10 circuits/Dispatch/N(days)	Diagnostic								
B228.7.1.1	P-10 Line Sharing<10 circuits/Dispatch/N(days)	Diagnostic								
B228.7.1.2	P-10 Line Sharing<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.7.2.1	P-10 Line Sharing>=10 circuits/Dispatch/N(days)	Diagnostic								
B228.7.2.2	P-10 Line Sharing>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.8.1.1	P-10 2W Analog Loop Design<10 circuits/Dispatch/N(days)	Diagnostic								
B228.8.1.2	P-10 2W Analog Loop Design<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.8.2.1	P-10 2W Analog Loop Design>=10 circuits/Dispatch/N(days)	Diagnostic								
B228.8.2.2	P-10 2W Analog Loop Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.9.1.1	P-10 2W Analog Loop Non-Design<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.9.1.2	P-10 2W Analog Loop Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.9.2.1	P-10 2W Analog Loop W/WNP Non-Design<10 circuits/Dispatch/N(days)	Diagnostic								
B228.9.2.2	P-10 2W Analog Loop W/WNP Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.10.1.1	P-10 2W Analog Loop W/WNP Design<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.10.1.2	P-10 2W Analog Loop W/WNP Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.10.2.1	P-10 2W Analog Loop W/WNP Design<10 circuits/Dispatch/N(days)	Diagnostic								
B228.10.2.2	P-10 2W Analog Loop W/WNP Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.11.1.1	P-10 2W Analog Loop W/WNP Non-Design<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.11.1.2	P-10 2W Analog Loop W/WNP Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.11.2.1	P-10 2W Analog Loop W/WNP Non-Design<10 circuits/Dispatch/N(days)	Diagnostic								
B228.11.2.2	P-10 2W Analog Loop W/WNP Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.12.1.1	P-14 2W Analog Loop w/WNP Non-Design<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.12.2.1	P-14 2W Analog Loop w/WNP Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.12.2.2	P-14 2W Analog Loop w/WNP Design<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.13.1.1	P-14 2W Analog Loop w/WNP Non-Design<10 circuits/Dispatch/N(days)	Diagnostic								
B228.13.1.2	P-14 2W Analog Loop w/WNP Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.13.2.1	P-14 2W Analog Loop w/WNP Non-Design<10 circuits/Dispatch/N(days)	Diagnostic								
B228.13.2.2	P-14 2W Analog Loop w/WNP Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.14.1.1	P-10 Other Design<10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.14.1.2	P-10 Other Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
B228.14.2.1	P-10 Other Design<10 circuits/Dispatch/N(days)	Diagnostic								
B228.14.2.2	P-10 Other Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								

	Total Service Order Cycle Time (Offered) - Mechanized	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z Score	Equity
P-10	Digital Loop >= DS1 />=10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Digital Loop >= DS1 />=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Switch Ports<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Switch Ports<10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Switch Ports>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Local Interoffice Transport<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Local Interoffice Transport<10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Local Interoffice Transport>=10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Local Interoffice Transport>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Loop + Port Combinations<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Loop + Port Combinations<10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Loop + Port Combinations>=10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Loop + Port Combinations>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Combo Other<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Combo Other<10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Combo Other>=10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Combo Other>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	xDSL (ADSL, HDSL and UCL)<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	xDSL (ADSL, HDSL and UCL)<10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	xDSL (ADSL, HDSL and UCL)>=10 circuits/Dispatch/N(days)	Diagnostic								
P-10	xDSL (ADSL, HDSL and UCL)>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	UNE ISDN<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	UNE ISDN<10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	UNE ISDN>=10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Line Sharing<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Line Sharing<10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Line Sharing>=10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Line Sharing>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop Design<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop Design<10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop Design>=10 circuits/Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop Non-Design<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop W/WNP Non-Design<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop W/WNP Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop W/WNP Non-Design<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop W/WNP Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop w/WNP Non-Design<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop w/WNP Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop w/WNP Non-Design<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	2W Analog Loop w/WNP Non-Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Other Design<10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Other Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Other Design<10 circuits/Dispatch/N(days)	Diagnostic								
P-10	Other Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Other Design<10 circuits/Non-Dispatch/N(days)	Diagnostic								
P-10	Other Design>=10 circuits/Non-Dispatch/N(days)	Diagnostic								

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Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z Score	Equity
P-10 Other Non-Design<10 circuits/Dispatch/TN(days)								Diagnostic
B 2.28.15.1.1								Diagnostic
B 2.28.15.1.2								Diagnostic
B 2.28.15.2.1								Diagnostic
B 2.28.15.2.2								Diagnostic
B 2.28.16.1.1								Diagnostic
B 2.28.16.1.2								Diagnostic
B 2.28.16.1.2								Diagnostic
B 2.28.16.2.1								Diagnostic
B 2.28.16.2.2								Diagnostic
B 2.28.17.1.1								Diagnostic
B 2.28.17.1.2								Diagnostic
B 2.28.17.2.1								Diagnostic
B 2.28.17.2.2								Diagnostic
B 2.28.18.1.1								Diagnostic
B 2.28.18.1.2								Diagnostic
B 2.28.18.2.1								Diagnostic
B 2.28.18.2.2								Diagnostic
B 2.28.19.1.1								Diagnostic
B 2.28.19.1.2								Diagnostic
B 2.28.19.2.1								Diagnostic
B 2.28.19.2.2								Diagnostic
Total Service Order/Cycle Time (offered) - Partially Mechanized								
P-10 Switch Pons<10 circuits/Dispatch/TN(days)								Diagnostic
B 2.29.1.1.2								Diagnostic
B 2.29.1.2.1								Diagnostic
B 2.29.1.2.2								Diagnostic
B 2.29.2.1.1								Diagnostic
B 2.29.2.1.2								Diagnostic
B 2.29.2.2.1								Diagnostic
B 2.29.2.2.2								Diagnostic
B 2.29.3.1.1								Diagnostic
B 2.29.3.1.2								Diagnostic
B 2.29.3.2.1								Diagnostic
B 2.29.3.2.2								Diagnostic
B 2.29.4.1.1								Diagnostic
B 2.29.4.1.2								Diagnostic
B 2.29.4.2.1								Diagnostic
B 2.29.4.2.2								Diagnostic
B 2.29.5.1.1			xDSL (ADSL, HDSL, and UDSL)<10 circuits/Dispatch/TN(days)					Diagnostic
B 2.29.5.1.2			xDSL (ADSL, HDSL, and UDSL)<=10 circuits/Non-Dispatch/TN(days)					Diagnostic
B 2.29.5.2.1			xDSL (ADSL, HDSL, and UDSL)>=10 circuits/Dispatch/TN(days)					Diagnostic
B 2.29.5.2.2			xDSL (ADSL, HDSL, and UDSL)>=10 circuits/Non-Dispatch/TN(days)					Diagnostic
B 2.29.6.1.1			UNE ISDN<10 circuits/Dispatch/TN(days)					Diagnostic
B 2.29.6.1.2			UNE ISDN<=10 circuits/Non-Dispatch/TN(days)					Diagnostic
B 2.29.6.2.1			P-10 UNE ISDN>=10 circuits/Dispatch/TN(days)					Diagnostic
B 2.29.6.2.2			P-10 UNE ISDN>=10 circuits/Non-Dispatch/TN(days)					Diagnostic
B 2.29.7.1.1			P-10 Line Sharing<10 circuits/Dispatch/TN(days)					Diagnostic
B 2.29.7.1.2			P-10 Line Sharing<=10 circuits/Non-Dispatch/TN(days)					Diagnostic
B 2.29.7.2.1			P-10 Line Sharing>=10 circuits/Dispatch/TN(days)					Diagnostic
B 2.29.7.2.2			P-10 2W Analog Loop Design<10 circuits/Dispatch/TN(days)					Diagnostic
B 2.29.8.1.1			P-10 2W Analog Loop Design<=10 circuits/Non-Dispatch/TN(days)					Diagnostic
B 2.29.8.1.2			P-10 2W Analog Loop Design>=10 circuits/Dispatch/TN(days)					Diagnostic
B 2.29.8.2.1			P-10 2W Analog Loop Design>=10 circuits/Non-Dispatch/TN(days)					Diagnostic
B 2.29.8.2.2			P-10 2W Analog Loop Non-Design<10 circuits/Dispatch/TN(days)					Diagnostic
B 2.29.9.1.1			P-10 2W Analog Loop Non-Design<=10 circuits/Non-Dispatch/TN(days)					Diagnostic
B 2.29.9.1.2			P-10 2W Analog Loop Non-Design>=10 circuits/Dispatch/TN(days)					Diagnostic
B 2.29.9.2.1			P-10 2W Analog Loop Non-Design>=10 circuits/Non-Dispatch/TN(days)					Diagnostic
B 2.29.9.2.2			P-10 2W Analog Loop WINP Design<10 circuits/Dispatch/TN(days)					Diagnostic
B 2.29.10.1.1			P-10 2W Analog Loop WINP Design<=10 circuits/Non-Dispatch/TN(days)					Diagnostic
B 2.29.10.1.2			P-10 2W Analog Loop wINP Design<10 circuits/Non-Dispatch/TN(days)					Diagnostic

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	P-10	2W Analog Loop w/LNP Design/n<=10 circuits/Non-Dispatch/TN(days)											
B2.29.10.2.1	P-10	2W Analog Loop w/LNP Design/n>=10 circuits/Non-Dispatch/TN(days)											
B2.29.10.2.2	P-10	2W Analog Loop w/LNP Non-Design/n<=10 circuits/Non-Dispatch/TN(days)											
B2.29.11.1.1	P-10	2W Analog Loop w/LNP Non-Design/n>=10 circuits/Non-Dispatch/TN(days)											
B2.29.11.1.2	P-10	2W Analog Loop w/LNP Non-Design/n<=10 circuits/Non-Dispatch/TN(days)											
B2.29.11.2.1	P-10	2W Analog Loop w/LNP Non-Design/n>=10 circuits/Non-Dispatch/TN(days)											
B2.29.11.2.2	P-10	2W Analog Loop w/LNP Non-Design/n<=10 circuits/Non-Dispatch/TN(days)											
B2.29.12.1.1	P-14	2W Analog Loop w/LNP Non-Design/n>=10 circuits/Non-Dispatch/TN(days)											
B2.29.12.1.2	P-14	2W Analog Loop w/LNP Design/n<=10 circuits/Non-Dispatch/TN(days)											
B2.29.12.2.1	P-14	2W Analog Loop w/LNP Design/n>=10 circuits/Non-Dispatch/TN(days)											
B2.29.13.1.1	P-14	2W Analog Loop w/LNP Non-Design/n<=10 circuits/Non-Dispatch/TN(days)											
B2.29.13.1.2	P-14	2W Analog Loop w/LNP Non-Design/n>=10 circuits/Non-Dispatch/TN(days)											
B2.29.13.2.1	P-14	2W Analog Loop w/LNP Non-Design/n<=10 circuits/Non-Dispatch/TN(days)											
B2.29.13.2.2	P-14	2W Analog Loop w/LNP Non-Design/n>=10 circuits/Non-Dispatch/TN(days)											
B2.29.14.1.1	P-10	Other Design/n<=10 circuits/Non-Dispatch/TN(days)											
B2.29.14.1.2	P-10	Other Design/n>=10 circuits/Non-Dispatch/TN(days)											
B2.29.14.2.1	P-10	Other Design/n<=10 circuits/Non-Dispatch/TN(days)											
B2.29.14.2.2	P-10	Other Design/n>=10 circuits/Non-Dispatch/TN(days)											
B2.29.15.1.1	P-10	Other Non-Design/n<=10 circuits/Non-Dispatch/TN(days)											
B2.29.15.1.2	P-10	Other Non-Design/n>=10 circuits/Non-Dispatch/TN(days)											
B2.29.15.2.1	P-10	Other Non-Design/n<=10 circuits/Non-Dispatch/TN(days)											
B2.29.15.2.2	P-10	Other Non-Design/n>=10 circuits/Non-Dispatch/TN(days)											
B2.29.16.1.1	P-10	INP (Standalone)<10 circuits/Non-Dispatch/TN(days)											
B2.29.16.1.2	P-10	INP (Standalone)<10 circuits/Non-Dispatch/TN(days)											
B2.29.16.2.1	P-10	INP (Standalone)>=10 circuits/Non-Dispatch/TN(days)											
B2.29.16.2.2	P-10	INP (Standalone)>=10 circuits/Non-Dispatch/TN(days)											
B2.29.17.1.1	P-14	LNP (Standalone)<10 circuits/Non-Dispatch/TN(days)											
B2.29.17.1.2	P-14	LNP (Standalone)<10 circuits/Non-Dispatch/TN(days)											
B2.29.17.2.1	P-14	LNP (Standalone)>=10 circuits/Non-Dispatch/TN(days)											
B2.29.17.2.2	P-14	LNP (Standalone)>=10 circuits/Non-Dispatch/TN(days)											
B2.29.18.1.1	P-10	Digital Loop < DS 1/<10 circuits/Non-Dispatch/TN(days)											
B2.29.18.1.2	P-10	Digital Loop < DS 1/<10 circuits/Non-Dispatch/TN(days)											
B2.29.18.2.1	P-10	Digital Loop < DS 1/>=10 circuits/Non-Dispatch/TN(days)											
B2.29.18.2.2	P-10	Digital Loop < DS 1/>=10 circuits/Non-Dispatch/TN(days)											
B2.29.19.1.1	P-10	Digital Loop == DS 1/<10 circuits/Non-Dispatch/TN(days)											
B2.29.19.1.2	P-10	Digital Loop == DS 1/>=10 circuits/Non-Dispatch/TN(days)											
B2.29.19.2.1	P-10	Digital Loop == DS 1/<=10 circuits/Non-Dispatch/TN(days)											
B2.29.19.2.2	P-10	Digital Loop == DS 1/>=10 circuits/Non-Dispatch/TN(days)											

Total Service Order Cycle Time (offered) - Non-Mechanized

P-10	Switch Ports<10 circuits/Non-Dispatch/TN(days)												
B2.30.1.1.1	P-10	Switch Ports>10 circuits/Non-Dispatch/TN(days)											
B2.30.1.1.2	P-10	Switch Ports>10 circuits/Non-Dispatch/TN(days)											
B2.30.1.2.1	P-10	Local Interoffice Transport<10 circuits/Non-Dispatch/TN(days)											
B2.30.2.1.1	P-10	Local Interoffice Transport<10 circuits/Non-Dispatch/TN(days)											
B2.30.2.1.2	P-10	Local Interoffice Transport<10 circuits/Non-Dispatch/TN(days)											
B2.30.2.2.1	P-10	Local Interoffice Transport<10 circuits/Non-Dispatch/TN(days)											
B2.30.2.2.2	P-10	Loop + Port Combinations<10 circuits/Non-Dispatch/TN(days)											
B2.30.3.1.1	P-10	Loop + Port Combinations<10 circuits/Non-Dispatch/TN(days)											
B2.30.3.1.2	P-10	Loop + Port Combinations<10 circuits/Non-Dispatch/TN(days)											
B2.30.3.2.1	P-10	Loop + Port Combinations<10 circuits/Non-Dispatch/TN(days)											
B2.30.3.2.2	P-10	Combo Other<10 circuits/Non-Dispatch/TN(days)											
B2.30.4.1.1	P-10	Combo Other<10 circuits/Non-Dispatch/TN(days)											
B2.30.4.1.2	P-10	Combo Other>=10 circuits/Non-Dispatch/TN(days)											
B2.30.4.2.1	P-10	Combo Other>=10 circuits/Non-Dispatch/TN(days)											
B2.30.4.2.2	P-10	DSL (ADSL, HDSL and UCL)<10 circuits/Non-Dispatch/TN(days)											
B2.30.5.1.1	P-10	DSL (ADSL, HDSL and UCL)<10 circuits/Non-Dispatch/TN(days)											
B2.30.5.1.2	P-10	DSL (ADSL, HDSL and UCL)<=10 circuits/Non-Dispatch/TN(days)											
B2.30.5.2.1	P-10	DSL (ADSL, HDSL and UCL)<=10 circuits/Non-Dispatch/TN(days)											
B2.30.5.2.2	P-10	DSL (ADSL, HDSL and UCL)<=10 circuits/Non-Dispatch/TN(days)											

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Benchmark / Analog	BTS Measure	BTS Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z-Score	Equity	[Redacted]					
									[Redacted]					
P-10 UNE ISDN<10 circuits/Dispatch/(N-days)														
B 2.30.6.1.1	P-10 LINE ISDN<10 circuits/Non-Dispatch/(N-days)													
B 2.30.6.1.2	P-10 LINE ISDN>=10 circuits/Dispatch/(N-days)													
B 2.30.6.2.1	P-10 UNE ISDN>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.6.2.2	P-10 UNE ISDN<10 circuits/Non-Dispatch/(N-days)													
B 2.30.7.1.1	P-10 Line Sharing<10 circuits/Dispatch/(N-days)													
B 2.30.7.1.2	P-10 Line Sharing<10 circuits/Non-Dispatch/(N-days)													
B 2.30.7.2.1	P-10 Line Sharing<=10 circuits/Dispatch/(N-days)													
B 2.30.7.2.2	P-10 2W Analog Loop Design<10 circuits/Dispatch/(N-days)													
B 2.30.8.1.1	P-10 2W Analog Loop Design<10 circuits/Non-Dispatch/(N-days)													
B 2.30.8.1.2	P-10 2W Analog Loop Design<=10 circuits/Non-Dispatch/(N-days)													
B 2.30.8.2.1	P-10 2W Analog Loop Design>=10 circuits/Dispatch/(N-days)													
B 2.30.8.2.2	P-10 2W Analog Loop Design>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.9.1.1	P-10 2W Analog Loop Non-Design<10 circuits/Dispatch/(N-days)													
B 2.30.9.1.2	P-10 2W Analog Loop Non-Design<10 circuits/Non-Dispatch/(N-days)													
B 2.30.9.2.1	P-10 2W Analog Loop Non-Design>=10 circuits/Dispatch/(N-days)													
B 2.30.9.2.2	P-10 2W Analog Loop Non-Design>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.10.1.1	P-10 2W Analog Loop WINP Design<10 circuits/Dispatch/(N-days)													
B 2.30.10.1.2	P-10 2W Analog Loop WINP Design<10 circuits/Non-Dispatch/(N-days)													
B 2.30.10.2.1	P-10 2W Analog Loop WINP Design>=10 circuits/Dispatch/(N-days)													
B 2.30.10.2.2	P-10 2W Analog Loop WINP Design>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.11.1.1	P-10 2W Analog Loop WINP Non-Design<10 circuits/Dispatch/(N-days)													
B 2.30.11.1.2	P-10 2W Analog Loop WINP Non-Design>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.11.2.1	P-10 2W Analog Loop WINP Non-Design<10 circuits/Dispatch/(N-days)													
B 2.30.11.2.2	P-10 2W Analog Loop WINP Non-Design>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.12.1.1	P-14 2W Analog Loop WINP Design<10 circuits/Dispatch/(N-days)													
B 2.30.12.1.2	P-14 2W Analog Loop WINP Design>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.12.2.1	P-14 2W Analog Loop WINP Non-Design<10 circuits/Dispatch/(N-days)													
B 2.30.12.2.2	P-14 2W Analog Loop WINP Non-Design>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.13.1.1	P-14 2W Analog Loop WINP Non-Design<10 circuits/Dispatch/(N-days)													
B 2.30.13.1.2	P-14 2W Analog Loop WINP Non-Design>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.13.2.1	P-14 2W Analog Loop WINP Non-Design<10 circuits/Dispatch/(N-days)													
B 2.30.13.2.2	P-14 2W Analog Loop WINP Non-Design>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.14.1.1	P-10 Other Design<10 circuits/Dispatch/(N-days)													
B 2.30.14.1.2	P-10 Other Design<10 circuits/Non-Dispatch/(N-days)													
B 2.30.14.2.1	P-10 Other Non-Design>=10 circuits/Dispatch/(N-days)													
B 2.30.14.2.2	P-10 Other Non-Design><10 circuits/Non-Dispatch/(N-days)													
B 2.30.15.1.1	P-10 INP (Standalone)<10 circuits/Dispatch/(N-days)													
B 2.30.15.1.2	P-10 INP (Standalone)>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.15.2.1	P-10 INP (Standalone)<10 circuits/Dispatch/(N-days)													
B 2.30.15.2.2	P-10 Other Non-Design>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.16.1.1	P-10 INP (Standalone)<10 circuits/Dispatch/(N-days)													
B 2.30.16.1.2	P-10 INP (Standalone)>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.16.2.1	P-10 INP (Standalone)>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.16.2.2	P-10 Digital Loop < DS1/10 circuits/Non-Dispatch/(N-days)													
B 2.30.17.1.1	P-14 INP (Standalone)<10 circuits/Dispatch/(N-days)													
B 2.30.17.1.2	P-14 INP (Standalone)>= DS1/10 circuits/Dispatch/(N-days)													
B 2.30.17.2.1	P-14 INP (Standalone)>=10 circuits/Dispatch/(N-days)													
B 2.30.17.2.2	P-14 INP (Standalone)>=10 circuits/Non-Dispatch/(N-days)													
B 2.30.18.1.1	P-10 Digital Loop < DS1/10 circuits/Non-Dispatch/(N-days)													
B 2.30.18.1.2	P-10 Digital Loop < DS1/10 circuits/Dispatch/(N-days)													
B 2.30.18.2.1	P-10 Digital Loop < DS1/10 circuits/Non-Dispatch/(N-days)													
B 2.30.18.2.2	P-10 Digital Loop < DS1/10 circuits/Dispatch/(N-days)													
B 2.30.19.1.1	P-10 Digital Loop > DS1/10 circuits/Non-Dispatch/(N-days)													
B 2.30.19.1.2	P-10 Digital Loop > DS1/10 circuits/Dispatch/(N-days)													
B 2.30.19.2.1	P-10 Digital Loop > DS1/10 circuits/Non-Dispatch/(N-days)													
B 2.30.19.2.2	P-10 Digital Loop > DS1/10 circuits/Dispatch/(N-days)													
P-13 [NP/N%]														

> 95% w in 15 min

Disconnect Timeless

B 2.31.1

NO

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		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z-Score	Equity
% Cooperative Test Attempts for xDSL										
P-8	xDSL (ADSL, HDSL, and UCL) (N%)	=> 95% of requests								
P-8	xDSL Other (N%)	=> 95% of requests								
Service Order Accuracy										
P-11	Design (Specials)<>10 circuits/Dispatch(N%)	>= 95%								
P-11	Design (Specials)<10 circuits/Non-Dispatch(N%)	>= 95%								
P-11	Design (Specials)>=10 circuits/Dispatch(N%)	>= 95%								
P-11	Design (Specials)>=10 circuits/Non-Dispatch(N%)	>= 95%								
P-11	Loops Non-Design<10 circuits/Dispatch(N%)	>= 95%								
P-11	Loops Non-Design<10 circuits/Non-Dispatch(N%)	>= 95%								
P-11	Loops Non-Design>=10 circuits/Dispatch(N%)	>= 95%								
P-11	Loops Non-Design>=10 circuits/Non-Dispatch(N%)	>= 95%								
Unbundled Network Elements - Maintenance and Repair										
Missed Repair Appointments										
M&R-1	Switch Ports/Dispatch(N%)	6.98%	52.258							
M&R-1	Switch Ports/Non-Dispatch(N%)	1.72%	28.160							
B-3.1.1.1	M&R-1 Local Interoffice Transport/Dispatch(N%)	0.00%	483	0.00%	1					
B-3.1.1.2	M&R-1 Local Interoffice Transport/Non-Dispatch(N%)	0.34%	292	0.00%	3					
B-3.1.2.1	M&R-1 Line + Port Combinations/Dispatch(N%)	6.98%	52.705	6.56%	579					
B-3.1.2.2	M&R-1 Line + Port Combinations/Non-Dispatch(N%)	1.76%	28.427	4.74%	380					
M&R-1	M&R-1 Loop + Port Combinations/Dispatch(N%)	6.90%	54.426							
M&R-1	M&R-1 Combo Other/Dispatch(N%)	6.90%	54.426							
M&R-1	M&R-1 ADSL (ADSL, HDSL, and UCL)/Dispatch(N%)	10.47%	253	4.17%	24					
M&R-1	M&R-1 ADSL (ADSL, HDSL, and UCL)/Non-Dispatch(N%)	0.00%	44	0.00%	12					
B-3.1.6.1	M&R-1 UNE (SDN) Dispatch(N%)	11.25%	480	20.00%	10					
B-3.1.6.2	M&R-1 UNE (SDN) Non-Dispatch(N%)	3.05%	492	0.00%	7					
M&R-1	M&R-1 Line Shaping/Dispatch(N%)	10.47%	258							
B-3.1.7.1	M&R-1 Line Shaping/Non-Dispatch(N%)	0.00%	44	11.11%	9					
B-3.1.7.2	M&R-1 2W Analog Loop Design/Dispatch(N%)	6.98%	52.705	9.76%	369					
M&R-1	M&R-1 2W Analog Loop Design/Non-Dispatch(N%)	6.98%	52.705	2.06%	97					
B-3.1.8.1	M&R-1 2W Analog Loop Non-Design/Dispatch(N%)	6.98%	52.997							
B-3.1.8.2	M&R-1 2W Analog Loop Non-Design/Non-Dispatch(N%)	6.98%	52.997							
B-3.1.9.1	M&R-1 2W Analog Loop Non-Design/Dispatch(N%)	6.98%	52.997							
B-3.1.9.2	M&R-1 2W Analog Loop Non-Design/Non-Dispatch(N%)	6.98%	52.997							
M&R-1	M&R-1 Other Design/Dispatch(N%)	1.01%	1.721	3.23%	93					
B-3.1.10.1	M&R-1 Other Design/Non-Dispatch(N%)	1.10%	1.659	0.00%	45					
B-3.1.10.2	M&R-1 Other Non-Design/Dispatch(N%)	6.98%	52.705	0.00%	16					
M&R-1	M&R-1 Other Non-Design/Non-Dispatch(N%)	1.76%	28.427	0.00%	16					
B-3.1.11.2	M&R-1 [NIP (Standalone)]/Dispatch(N%)	6.98%	52.258							
M&R-1	M&R-1 [NIP (Standalone)]/Non-Dispatch(N%)	1.72%	28.160							
Customer Trouble Report Rate										
M&R-2	M&R-2 Switch Ports/Dispatch(N%)	2.13%	2.456.066							
M&R-2	M&R-2 Local Interoffice Transport/Dispatch(N%)	1.15%	2.456.066							
B-3.2.2.1	M&R-2 Local Interoffice Transport/Non-Dispatch(N%)	2.26%	21.355	0.68%	148					
B-3.2.2.2	M&R-2 Line + Port Combinations/Dispatch(N%)	1.37%	21.355	2.03%	148					
M&R-2	M&R-2 Line + Port Combinations/Non-Dispatch(N%)	2.03%	2.594.635	1.75%	33.071					
B-3.2.3.2	M&R-2 Loop + Port Combinations/Dispatch(N%)	1.10%	2.594.635	1.15%	33.071					
M&R-2	M&R-2 Combo Other/Dispatch(N%)	1.82%	2.993.592							
M&R-2	M&R-2 Combo Other/Non-Dispatch(N%)	1.82%	2.993.592							
B-3.2.4.2	M&R-2 ADSL (ADSL, HDSL, and UCL)/Dispatch(N%)	0.98%	30.172	0.93%	2.589					
B-3.2.5.1	M&R-2 ADSL (ADSL, HDSL, and UCL)/Non-Dispatch(N%)	0.15%	30.172	0.46%	2.589					
B-3.2.5.2	M&R-2 UNE (SDN) Dispatch(N%)	2.01%	23.834	0.69%	1.456					
B-3.2.6.1	M&R-2 UNE (SDN) Non-Dispatch(N%)	2.08%	23.834	0.48%	1.456					
M&R-2	M&R-2 Line Shaping/Dispatch(N%)	0.98%	30.172	0.00%	292					
B-3.2.7.1	M&R-2 Line Shaping/Non-Dispatch(N%)	0.15%	30.172	3.08%	292					
B-3.2.7.2	M&R-2 2W Analog Loop Design/Dispatch(N%)	2.03%	2.594.635	0.86%	42.785					
M&R-2	M&R-2 2W Analog Loop Design/Non-Dispatch(N%)	2.03%	2.594.635	0.23%	42.785					

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		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z-Score	Equity
M&R-2	2W Analog Loop Non-Design/Dispatch/(N%)	R&B (POTS) excl SB FT	2.12%	2,455,066	0.00%	197	0.01038	2.0445		YES
B3.2.9.1	2W Analog Loop Non-Design/Non-Dispatch/(N%)	R&B (POTS) excl SB FT	1.02%	2,455,066	2.31%		0.00104	-1.0381		NO
B3.2.9.2	Other Design/Dispatch/(N%)	Design	0.43%	398,957	4.032		0.00014	-0.9978		YES
B3.2.10.1	Other Non-Design/Non-Dispatch/(N%)	Design	0.4%	398,957	1.12%		0.00014	-0.9978		YES
B3.2.10.2	Other Non-Design/Dispatch/(N%)	R&B	2.03%	2,594,635	1.01%	1,590	0.00358	2.8659	YES	YES
M&R-2	Other Non-Design/Non-Dispatch/(N%)	R&B	1.10%	2,594,635	1.01%	1,590	0.00293	3.0402	YES	YES
B3.2.11.1	LNP (Standalone)/Dispatch/(N%)	R&B (POTS)	2.13%	2,455,066						
M&R-2	LNP (Standalone)/Non-Dispatch/(N%)	R&B (POTS)	1.15%	2,455,066						
<i>Maintenance Average Duration</i>										
B3.3.1.1	M&R-3 Switch Ports/Dispatch/(N)hours	R&B (POTS)	25.81	52,258						
M&R-3	Switch Ports/Non-Dispatch/(N)hours	R&B (POTS)	11.65	28,160						
B3.3.2.1	M&R-3 Local Interoffice Transport/Dispatch/(N)hours	DS1/DS3	4.13	483	0.28		1	3.147	3,14986	1,2220
B3.3.2.2	M&R-3 Local Interoffice Transport/Non-Dispatch/(N)hours	DS1/DS3	2.07	292	0.54		3	2.61	1,35018	0.9844
M&R-3	Loop + Port Combinations/Dispatch/(N)hours	R&B	25.69	52,705	4.15	579	22,155	0.82579	12,4583	YES
B3.3.3.1	M&R-3 Loop + Port Combinations/Non-Dispatch/(N)hours	R&B	11.59	28,127	4.45	380	15,759	0.81381	8,7700	YES
B3.3.3.2	M&R-3 Combo Other/Dispatch/(N)hours	R&B-D	25.09	54,426						
B3.3.4.1	M&R-3 Combo Other/Non-Dispatch/(N)hours	R&B-D	25.08	54,426						
M&R-3	ADSL (ADSL, HDSL and UCL)/Dispatch/(N)hours	ADSL to Retail	6,188	258	7.40	24	40,101	8,55786	6,3657	YES
B3.3.5.2	M&R-3 XDSL (ADSL, HDSL and UCL)/Non-Dispatch/(N)hours	ADSL to Retail	16.97	44	4.52	12	30,058	9,78901	1,2713	YES
M&R-3	ISDN - BRI	ISDN - BRI	11.17	480	8.74	10	13,922	4,44874	0,5461	YES
B3.3.6.1	M&R-3 UNE - ISDN	ISDN - BRI	3.91	492	11.26	7	11,151	4,24471	-0,7321	NO
B3.3.6.2	M&R-3 Line Sharing/Dispatch/(N)hours	ADSL to Retail	61.88	258			40,101			
B3.3.7.1	M&R-3 Line Sharing/Non-Dispatch/(N)hours	ADSL to Retail	16.97	44	9.55	9	30,058	10,98643	0,6749	YES
B3.3.7.2	M&R-3 Line Sharing/Loop Design/Non-Dispatch/(N)hours	R&B - Disp	26.69	52,705	10.49	369	22,155	1,15739	1,1281	YES
B3.3.8.1	M&R-3 2W Analog Loop Design/Non-Dispatch/(N)hours	R&B - Disp	25.69	52,705	4.27	97	22,155	2,25160	9,5089	YES
B3.3.8.2	M&R-3 2W Analog Loop Design/Non-Dispatch/(N)hours	R&B (POTS) excl SB FT	25.79	52,097						
M&R-3	2W Analog Loop Non-Design/Non-Dispatch/(N)hours	R&B (POTS) excl SB FT	11.88	25,114						
B3.3.9.1	M&R-3 Other Design/Dispatch/(N)hours	Design	6.67	1,721	6.50	93	22,352	2,37857	0,0714	YES
M&R-3	Other Design/Non-Dispatch/(N)hours	Design	2.75	1,629	3.59	45	13,739	2,07619	-0,4041	YES
B3.3.10.1	M&R-3 Other Non-Design/Dispatch/(N)hours	R&B	26.69	52,705	12.84	16	22,155	5,31987	2,1389	YES
M&R-3	Other Non-Design/Non-Dispatch/(N)hours	R&B	11.59	28,427	7.72	16	15,759	3,94088	0,9822	YES
B3.3.11.1	M&R-3 LNP (Standalone)/Dispatch/(N)hours	R&B (POTS)	25.81	52,258						
B3.3.12.1	M&R-3 LNP (Standalone)/Non-Dispatch/(N)hours	R&B (POTS)	11.65	28,160						
<i>% Report Troubles within 30 Days</i>										
M&R-4	Switch Ports/Dispatch/(N%)	R&B (POTS)	21.94%	52,258						
M&R-4	Switch Ports/Non-Dispatch/(N%)	R&B (POTS)	18.39%	28,160						
B3.4.1.1	M&R-4 Local Interoffice Transport/Dispatch/(N%)	DS1/DS3	33.33%	483	100.00%	1				
M&R-4	Local Interoffice Transport/Non-Dispatch/(N%)	DS1/DS3	26.37%	292	33.33%	3				
B3.4.2.1	M&R-4 Loop + Port Combinations/Dispatch/(N%)	R&B	21.87%	52,705	16.06%	579				
M&R-4	Loop + Port Combinations/Non-Dispatch/(N%)	R&B	18.38%	28,427	17.33%	380				
B3.4.3.2	M&R-4 Combo Other/Dispatch/(N%)	R&B-D	22.37%	54,426						
M&R-4	Combo Other/Non-Dispatch/(N%)	ADSL to Retail	15.50%	258	16.67%	24				
B3.4.4.2	M&R-4 XDSL (ADSL, HDSL and UCL)/Dispatch/(N%)	R&B - Disp	15.91%	44	8.33%	12				
M&R-4	XDSL (ADSL, HDSL and UCL)/Non-Dispatch/(N%)	ISDN - BRI	27.08%	480	20.00%	10				
B3.4.5.1	M&R-4 UNE (SDN/ISDN)/Dispatch/(N%)	ISDN - BRI	22.97%	492	14.29%	7				
M&R-4	UNE (SDN/ISDN)/Non-Dispatch/(N%)	ADSL to Retail	15.50%	258						
B3.4.7.2	M&R-4 Line Sharing/Dispatch/(N%)	R&B - Disp	15.91%	44	55.56%	9				
M&R-4	Line Sharing/Non-Dispatch/(N%)	ISDN - BRI	21.87%	52,705	13.28%	369				
B3.4.8.1	M&R-4 2W Analog Loop Design/Dispatch/(N%)	R&B	21.87%	52,705	11.34%	97				
M&R-4	2W Analog Loop Design/Non-Dispatch/(N%)	ISDN - BRI	21.88%	52,957	12.50%	16				
B3.4.9.1	M&R-4 2W Analog Loop Non-Design/Dispatch/(N%)	ISDN - BRI	18.52%	25,114	28.42%	16				
M&R-4	2W Analog Loop Non-Design/Non-Dispatch/(N%)	ADSL to Retail	15.71%	1,721	30.11%	93				
B3.4.10.1	M&R-4 Other Design/Dispatch/(N%)	Design	3.46%	1,629	22.22%	45				
M&R-4	Other Design/Non-Dispatch/(N%)	R&B	21.87%	52,705	12.50%	369				
B3.4.11.1	M&R-4 Other Non-Design/Dispatch/(N%)	R&B	18.36%	28,427	12.50%	16				
M&R-4	Other Non-Design/Non-Dispatch/(N%)	R&B	21.94%	52,258	22.22%	1,1281				
B3.4.12.2	M&R-4 [NP (Standalone)]/Dispatch/(N%)	R&B (POTS)	18.39%	28,160						

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	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
Out of Service > 24 hours									
M&R-5 Switch Ports/Dispatch/TN(%)	R&B (POTS)	36.42%	34.035						
B.3.5.1.2 M&R-5 Switch Ports/Non-Dispatch/TN(%)	R&B (POTS)	17.19%	10.724						
B.3.5.2.1 M&R-5 Local Interoffice Transport/Dispatch/TN(%)	DS/IDS3	0.00%	483	0.00%		1			YES
B.3.5.2.2 M&R-5 Local Interoffice Transport/Non-Dispatch/TN(%)	DS/IDS3	0.34%	292	0.00%		3			YES
B.3.5.3.1 M&R-5 Loop + Port Combinations/Dispatch/TN(%)	R&B	36.17%	34.312	15.07%		365			0.02528 8.3453 YES
B.3.5.3.2 M&R-5 Loop + Port Combinations/Non-Dispatch/TN(%)	R&B	17.02%	10.845	0.73%		137			0.03231 5.0422 YES
M&R-5 Combo Other/Dispatch/TN(%)	R&B&D - Disp	34.53%	36.033						
B.3.5.4.1 M&R-5 Combo Other/Non-Dispatch/TN(%)	R&B&D - Disp	34.63%	35.033	4.17%		24			
B.3.5.4.2 M&R-5 xDSL (ADSL, HDSL and UCL) Dispatch/TN(%)	ADSL to Retail								
B.3.5.5.1 M&R-5 xDSL (ADSL, HDSL and UCL) Non-Dispatch/TN(%)	ADSL to Retail								
B.3.5.5.2 M&R-5 xDSL (ADSL, HDSL and UCL) Non-Dispatch/TN(%)	ISDN - BRI	1.27%	479	0.00%		12			0.01015 -0.8638 YES
B.3.5.6.1 M&R-5 UNE ISDN/Non-Dispatch/TN(%)	ISDN - BRI	3.05%	482	0.00%		7			0.08544 0.4659 YES
B.3.5.6.2 M&R-5 UNE ISDN/Non-Dispatch/TN(%)	ADSL to Retail								
B.3.5.7.1 M&R-5 Line Sharing/Dispatch/TN(%)	ADSL to Retail								
B.3.5.7.2 M&R-5 Line Sharing/Non-Dispatch/TN(%)	R&B - Disp								
B.3.5.8.1 M&R-5 2W Analog Loop Design/Dispatch/TN(%)	R&B - Disp	36.17%	34.312	9.76%		369			0.02515 10.5029 YES
B.3.5.8.2 M&R-5 2W Analog Loop Design/Non-Dispatch/TN(%)	R&B - Disp	36.17%	34.312	2.06%		97			0.04895 6.9811 YES
B.3.5.9.1 M&R-5 2W Analog Loop Non-Design/Dispatch/TN(%)	R&B (POTS) excl SB FT								
B.3.5.9.2 M&R-5 2W Analog 1,000 Non-Design/Non-Dispatch/TN(%)	R&B (POTS) excl SB FT	17.34%	10.320						
B.3.5.10.1 M&R-5 Other Design/Dispatch/TN(%)	Design	4.01%	1.721	3.23%		93			0.02069 0.3751 YES
B.3.5.10.2 M&R-5 Other Design/Non-Dispatch/TN(%)	Design	1.10%	1.629	0.00%		45			0.01580 0.6985 YES
B.3.5.11.1 M&R-5 Other Non Design/Dispatch/TN(%)	R&B	36.17%	34.312	27.27%		11			0.14490 0.6139 YES
B.3.5.11.2 M&R-5 Other Non Design/Non-Dispatch/TN(%)	R&B	17.02%	10.845	0.00%		9			0.12533 1.3582 YES
M&R-5 LNP (Standalone)/Dispatch/TN(%)	R&B (POTS)	36.42%	34.035						
M&R-5 LNP (Standalone)/Non-Dispatch/TN(%)	R&B (POTS)	17.13%	10.724						

Unbundled Network Elements - Billing

Invoice Accuracy	BST - State	95.83%	\$199,623.100	99.93%	\$890,818				
Mean Time to Deliver Invoices - CRIS	BST - Region	3.72	1	3.21	1.248				

B.4.1						0.00011	96.0326		YES
B.4.2									

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		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
Local Interconnection Trunks - Ordering										
% Rejected Service Requests										
C.1.1	D-7 [Local Interconnection Trunks](N%)	Diagnostic		68.42%	19					Diagnostic
C.1.2	D-8 [Local Interconnection Trunks](N%)			>= 85% w in 4 days		12				YES
C.1.3	D-9 [Local Interconnection Trunks](N%)			>= 95% w in 10 days		21				YES
C.1.4	D-11 [Local Interconnection Trunks](N%)			>= 95%		12				YES
C.1.5	D-11 [Local Interconnection Trunks](N%)			>= 95%						
Local Interconnection Trunks - Provisioning										
Order Completion Interval										
C.2.1	P-4 [Local Interconnection Trunks](N)days	Party w Retail		16.51	35	11.00	14	6.738	2.13704	2.5803
C.2.2	P-7 [Local Interconnection Trunks](N)days	Party w Retail								Not Applicable for Trunks
C.2.3	P-7 [Local Interconnection Trunks](N)days	Party w Retail								Not Applicable for Trunks
C.2.4	P-2 [Local Interconnection Trunks](N)hours	Party w Retail		95% >= 48 hrs						Not Applicable for Trunks
C.2.5	P-3 [Local Interconnection Trunks](N)%	Party w Retail		5.71%	35	0.00%	20		0.06506	0.8783
C.2.6	P-9 [Local Interconnection Trunks](N)%	Party w Retail		0.00%	2.618	0.00%	1.900		0.00000	—
C.2.7	P-5 [Local Interconnection Trunks](N)hours	Party w Retail		70.08	24	45.35	13	140.161	48.26708	0.5120
C.2.8	P-10 [Local Interconnection Trunks](N)days	Diagnostic								Under development
C.2.9	P-10 [Local Interconnection Trunks](N)days	Diagnostic								Under development
Service Order Accuracy										
C.2.11.1	P-11 Local Interconnection Trunks<10 circuits/Dispatch/(N)%			>= 95%		75.00%	4			No
C.2.11.2	P-11 Local Interconnection Trunks<10 circuits/Non-Dispatch/(N)%			>= 95%		100.00%	12			Yes
C.2.11.2.1	P-11 Local Interconnection Trunks>=10 circuits/Dispatch/(N)%			>= 95%		83.33%	6			No
C.2.11.2.2	P-11 Local Interconnection Trunks>=10 circuits/Non-Dispatch/(N)%			>= 95%		92.31%	13			No
Local Interconnection Trunks - Maintenance and Repair										
Missed Repair Appointments										
M.R.-1	M.R.-1 [Local Interconnection Trunks/Dispatch/(N)%]	Party w Retail		0.00%	1				0.00000	—
M.R.-1	M.R.-1 [Local Interconnection Trunks/Non-Dispatch/(N)%]	Party w Retail		0.00%	55	0.00%	1			YES
Customer Trouble Report Rate										
C.3.2.1	M.R.2 [Local Interconnection Trunks/Dispatch/(N)%]	Party w Retail		0.00%	155.270	0.00%	44.692		0.00001	0.4726
C.3.2.2	M.R.2 [Local Interconnection Trunks/Non-Dispatch/(N)%]	Party w Retail		0.00%	155.270	0.00%	44.692		0.00010	3.2846
Maintenance Average Duration										

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		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z-Score	Equity
M&R-3	Local Interconnection Trunks Dispatch TN(hours)	Party w Retail	2.7	1			0.000	2.42989	0.1953	
M&R-3	Local Interconnection Trunks Non-Dispatch TN(hours)	Party w Retail	0.98	55	0.52	1	2.408			YES
% Repeat Troubles within 30 Days										
M&R-4	Local Interconnection Trunks Dispatch TN(%)	Party w Retail	0.00%	1						
M&R-4	Local Interconnection Trunks Non-Dispatch TN(%)	Party w Retail	5.45%	55	0.00%	1		0.22915	0.2380	YES
Out of Service > 24 hours										
M&R-5	Local Interconnection Trunks Dispatch TN(%)	Party w Retail	0.00%	1						
M&R-5	Local Interconnection Trunks Non-Dispatch TN(%)	Party w Retail	0.00%	55	0.00%	1		0.00000		YES
Local Interconnection Trunks - Billing										
<i>Invoice Accuracy</i>										
B-1	TN(%)	BST - State	98.83%	\$199,623,100	98.43%	\$3,505,369	0.00006	64,6593		NO
<i>Mean Time to Deliver Invoices - CABS</i>										
B-2	Region(Calendar days)	BST - Region	429	1	3.73	3,213				YES
LOCAL INTERCONNECTION TRUNKS - TRUNK BLOCKING										
<i>Trunk Group Performance - Aggregate</i>										
TGP-1	TN		>0.5% diff 2 consec. Hrs		0					YES

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Operations Support Systems - Pre-Ordering

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z-Score	Equity
% Interface Availability - CLEC									
D.1.1.1	OS-2 EDIRegion(%)	=> 99.5%							YES
D.1.1.2	OS-2 HAIRRegion(%)	=> 99.5%							YES
D.1.1.3	OS-2 LENSRegion(%)	=> 99.5%							YES
D.1.1.4	OS-2 LEO MAINFRAME/Region(%)	=> 99.5%							YES
D.1.1.5	OS-2 LEO UNIXRegion(%)	=> 99.5%							YES
D.1.1.6	OS-2 LESORegion(%)	=> 99.5%							YES
D.1.1.7	OS-2 TAGRegion(%)	=> 99.5%							YES
D.1.1.8	OS-2 PSIMMSRegion(%)	=> 99.5%							YES
% Interface Availability - BST & CLEC									
D.1.2.1	OS-2 ATLASOFFRegion(%)	=> 99.5%							YES
D.1.2.2	OS-2 BOCRISRegion(%)	=> 99.5%							YES
D.1.2.3	OS-2 DSAPRegion(%)	=> 99.5%							YES
D.1.2.4	OS-2 RSAGRegion(%)	=> 99.5%							YES
D.1.2.5	OS-2 SOCSSRegion(%)	=> 99.5%							YES
D.1.2.6	OS-2 SONGSRegion(%)	=> 99.5%							YES
D.1.2.7	OS-2 DOERRegion(%)	=> 99.5%							YES
Average Response Interval - CLEC (LENS) (BST Measure includes Additional 2 Seconds)									
D.1.3.1.1	OS-1 RSAG, by TN/Region (seconds)	2.92	1.212,504	1.66	241,323				
D.1.3.1.2	OS-1 RSAG, by TN/Region (seconds)	3.27	7,414	1.66	241,323				
D.1.3.2.1	OS-1 RSAG, by ADDRRegion (seconds)	3.04	4,294,956	1.45	169,294				
D.1.3.2.2	OS-1 RSAG, by ADDRRegion (seconds)	5.75	612,349	1.45	169,294				
D.1.3.3.1	OS-1 ATLASRegion (seconds)	5.12	349,803	1.08	64,940				
D.1.3.3.2	OS-1 ATLASRegion (seconds)	2.64	293,122	1.08	64,940				
D.1.3.4.1	OS-1 DSAPRegion (seconds)	2.69	558,850	0.69	604				
D.1.3.4.2	OS-1 DSAPRegion (seconds)	2.70	301,692	0.69	604				
D.1.3.5.1	OS-1 HALCRISRegion (seconds)	3.66	2,461,808	13.09	583,242				
D.1.3.5.2	OS-1 HALCRISRegion (seconds)	3.23	472,321	13.09	583,242				
D.1.3.6.1	OS-1 COFFINSORegion (seconds)	4.09	1,925,182	0.94	35,689				
D.1.3.6.2	OS-1 COFFINSORegion (seconds)	4.36	622,170	0.94	35,689				
D.1.3.7.1	OS-1 PSIMSTORERegion (seconds)	4.09	1,925,182	0.11	69,519				
D.1.3.7.2	OS-1 PSIMSTORERegion (seconds)	4.36	622,170	0.11	69,519				
Average Response Interval - CLEC (TAG) (BST Measure includes Additional 2 Seconds)									
D.1.4.1.1	OS-1 RSAG, by TN/Region (seconds)	2.92	1.212,504	1.86	132,711				
D.1.4.1.2	OS-1 RSAG, by TN/Region (seconds)	3.27	7,414	1.86	132,711				
D.1.4.2.1	OS-1 RSAG, by ADDRRegion (seconds)	3.04	4,294,956	1.83	438,474				
D.1.4.2.2	OS-1 RSAG, by ADDRRegion (seconds)	5.75	612,349	1.83	438,474				
D.1.4.3.1	OS-1 ATLAS-MIHRegion (seconds)								Diagnostic
D.1.4.3.2	OS-1 ATLAS-MIHRegion (seconds)								Diagnostic
D.1.4.4.1	OS-1 ATLAS-DIDRegion (seconds)								Diagnostic
D.1.4.4.2	OS-1 ATLAS-DIDRegion (seconds)								Diagnostic
D.1.4.5.1	OS-1 ATLAS-TNRegion (seconds)								Diagnostic
D.1.4.5.2	OS-1 ATLAS-TNRegion (seconds)								Diagnostic
D.1.4.6.1	OS-1 DSAPRegion (seconds)								YES
D.1.4.6.2	OS-1 DSAPRegion (seconds)								YES
D.1.4.7.1	OS-1 CRSECSRRegion (seconds)								YES
D.1.4.7.2	OS-1 CRSECSRRegion (seconds)								YES
D.1.4.8.1	OS-1 CRSENTRegion (seconds)								YES
D.1.4.8.2	OS-1 CRSENTRegion (seconds)								YES
D.1.4.9.1	OS-1 CRSECSRRegion (seconds)								YES
D.1.4.9.2	OS-1 CRSECSRRegion (seconds)								YES
<i>This data not applicable after 6-1-2001, see D.1.4.8.1</i>									
<i>This data not applicable after 6-1-2001, see D.1.4.8.2</i>									
D.1.5.1	OS-1 CRSACCTSRegion (seconds)	3.66	2,461,808	1.32	1,805				
D.1.5.2	OS-1 CRSACCTSRegion (seconds)	3.23	472,421	1.32	1,805				

Operations Support Systems - Maintenance and Repair

BellSouth Monthly State Summary

Tennessee, June 2001

% Interface Availability - BST		Benchmark / Analog		BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z-Score	Equity
D 21.1				=> 99.5%							YES
D 22.1	% Interface Availability - CLEC			=> 99.5%							YES
D 22.2	OS S-3 LMOS HOST(Region%)			=> 99.5%							YES
	OS S-3 LNP(Region%)			=> 99.5%							YES
	OS S-3 MARCH(Region%)			=> 99.5%							YES
	OS S-3 OSPCMR(Region%)			=> 99.5%							YES
	OS S-3 Predictor(Region%)			=> 99.5%							YES
	OS S-3 SOCS(Region%)			=> 99.5%							YES
	Average Response Interval										
D 24.1.1	OS S-4 CRIS(Region%) <= 4 Seconds										
D 24.1.2	OS S-4 CRIS(Region%) <= 10 Seconds										
D 24.1.3	OS S-4 CRIS(Region%) > 10 Seconds										
D 24.2.1	OS S-4 DLETH(Region%) <= 4 Seconds										
D 24.2.2	OS S-4 DLETH(Region%) <= 10 Seconds										
D 24.2.3	OS S-4 DLETH(Region%) > 10 Seconds										
D 24.3.1	OS S-4 DLIR(Region%) <= 4 Seconds										
D 24.3.2	OS S-4 DLIR(Region%) <= 10 Seconds										
D 24.3.3	OS S-4 DLIR(Region%) > 10 Seconds										
D 24.4.1	OS S-4 LMOS(Region%) <= 4 Seconds										
D 24.4.2	OS S-4 LMOS(Region%) <= 10 Seconds										
D 24.4.3	OS S-4 LMOS(Region%) > 10 Seconds										
D 24.5.1	OS S-4 LMOSupd(Region%) <= 4 Seconds										
D 24.5.2	OS S-4 LMOSupd(Region%) <= 10 Seconds										
D 24.5.3	OS S-4 LMOSupd(Region%) > 10 Seconds										
D 24.6.1	OS S-4 LNPRE(Region%) <= 4 Seconds										
D 24.6.2	OS S-4 LNPRE(Region%) <= 10 Seconds										
D 24.6.3	OS S-4 LNPRE(Region%) > 10 Seconds										
D 24.7.1	OS S-4 MARCH(Region%) <= 4 Seconds										
D 24.7.2	OS S-4 MARCH(Region%) <= 10 Seconds										
D 24.7.3	OS S-4 MARCH(Region%) > 10 Seconds										
D 24.8.1	OS S-4 OSPCMR(Region%) <= 4 Seconds										
D 24.8.2	OS S-4 OSPCMR(Region%) <= 10 Seconds										
D 24.8.3	OS S-4 OSPCMR(Region%) > 10 Seconds										
D 24.9.1	OS S-4 Predictor(Region%) <= 4 Seconds										
D 24.9.2	OS S-4 Predictor(Region%) <= 10 Seconds										
D 24.9.3	OS S-4 Predictor(Region%) > 10 Seconds										
D 24.10.1	OS S-4 SOCS(Region%) <= 4 Seconds										
D 24.10.2	OS S-4 SOCS(Region%) <= 10 Seconds										
D 24.10.3	OS S-4 SOCS(Region%) > 10 Seconds										
D 24.11.1	OS S-4 NIN(Region%) <= 4 Seconds										
D 24.11.2	OS S-4 NIN(Region%) <= 10 Seconds										
D 24.11.3	OS S-4 NIN(Region%) > 10 Seconds										

% Interface Availability - BST		Benchmark / Analog		BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z-Score	Equity
D 21.1				=> 99.5%							YES
D 22.1	% Interface Availability - CLEC			=> 99.5%							YES
D 22.2	OS S-3 LMOS HOST(Region%)			=> 99.5%							YES
	OS S-3 LNP(Region%)			=> 99.5%							YES
	OS S-3 MARCH(Region%)			=> 99.5%							YES
	OS S-3 OSPCMR(Region%)			=> 99.5%							YES
	OS S-3 Predictor(Region%)			=> 99.5%							YES
	OS S-3 SOCS(Region%)			=> 99.5%							YES
	Average Response Interval										
D 24.1.1	OS S-4 CRIS(Region%) <= 4 Seconds										
D 24.1.2	OS S-4 CRIS(Region%) <= 10 Seconds										
D 24.1.3	OS S-4 CRIS(Region%) > 10 Seconds										
D 24.2.1	OS S-4 DLETH(Region%) <= 4 Seconds										
D 24.2.2	OS S-4 DLETH(Region%) <= 10 Seconds										
D 24.2.3	OS S-4 DLETH(Region%) > 10 Seconds										
D 24.3.1	OS S-4 DLIR(Region%) <= 4 Seconds										
D 24.3.2	OS S-4 DLIR(Region%) <= 10 Seconds										
D 24.3.3	OS S-4 DLIR(Region%) > 10 Seconds										
D 24.4.1	OS S-4 LMOS(Region%) <= 4 Seconds										
D 24.4.2	OS S-4 LMOS(Region%) <= 10 Seconds										
D 24.4.3	OS S-4 LMOS(Region%) > 10 Seconds										
D 24.5.1	OS S-4 LMOSupd(Region%) <= 4 Seconds										
D 24.5.2	OS S-4 LMOSupd(Region%) <= 10 Seconds										
D 24.5.3	OS S-4 LMOSupd(Region%) > 10 Seconds										
D 24.6.1	OS S-4 LNPRE(Region%) <= 4 Seconds										
D 24.6.2	OS S-4 LNPRE(Region%) <= 10 Seconds										
D 24.6.3	OS S-4 LNPRE(Region%) > 10 Seconds										
D 24.7.1	OS S-4 MARCH(Region%) <= 4 Seconds										
D 24.7.2	OS S-4 MARCH(Region%) <= 10 Seconds										
D 24.7.3	OS S-4 MARCH(Region%) > 10 Seconds										
D 24.8.1	OS S-4 OSPCMR(Region%) <= 4 Seconds										
D 24.8.2	OS S-4 OSPCMR(Region%) <= 10 Seconds										
D 24.8.3	OS S-4 OSPCMR(Region%) > 10 Seconds										
D 24.9.1	OS S-4 Predictor(Region%) <= 4 Seconds										
D 24.9.2	OS S-4 Predictor(Region%) <= 10 Seconds										
D 24.9.3	OS S-4 Predictor(Region%) > 10 Seconds										
D 24.10.1	OS S-4 SOCS(Region%) <= 4 Seconds										
D 24.10.2	OS S-4 SOCS(Region%) <= 10 Seconds										
D 24.10.3	OS S-4 SOCS(Region%) > 10 Seconds										
D 24.11.1	OS S-4 NIN(Region%) <= 4 Seconds										
D 24.11.2	OS S-4 NIN(Region%) <= 10 Seconds										
D 24.11.3	OS S-4 NIN(Region%) > 10 Seconds										

BellSouth Monthly State Summary
Tennessee, June 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	Z-Score	Equity
COLLOCATION - Collocation									
Average Response Time									
E.1.1.1	C-1 Virtual/N (calendar days)					<= 20 days			
E.1.1.2	C-1 Virtual Augments for Line Sharing or Line Splitting/T/N (business days)					<= 23 days			
E.1.1.3	C-1 Physical-Caged/N (business days)					<= 23 days			
E.1.1.4	C-1 Physical-Cageless/T/N (business days)					<= 23 days			
Average Arrangement Time									
E.1.2.1	C-2 Virtual-Ordinary/T/N (calendar days)					<= 50 days			
E.1.2.2	C-2 Virtual-Extraordinary/T/N (calendar days)					<= 75 days			
E.1.2.3	C-2 Virtual Augments for Line Sharing or Line Splitting/T/N (business days)					<= 45 days			
E.1.2.4	C-2 Physical Caged-Ordinary/T/N (business days)					<= 76 days			
E.1.2.5	C-2 Physical Caged-Extraordinary/T/N (business days)					<= 91 days			
E.1.2.6	C-2 Physical Cageless-Ordinary/T/N (business days)					<= 76 days			
E.1.2.7	C-2 Physical Cageless-Extraordinary/T/N (business days)					<= 91 days			
% Due Dates Missed									
E.1.3.1	C-3 Virtual/T/N (%)					< 5% missed			
E.1.3.2	C-3 Physical/T/N (%)					< 5% missed			
E.1.3.3	C-3 Virtual Augments for Line Sharing or Line Splitting/T/N (%)					< 5% missed			

BellSouth Monthly State Summary
Tennessee, June 2001

	Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
General - Flow Through									
F.1.1.1									
O.3	Summary/Region(%)					88.16%	230.255		Diagnostic
F.1.1.2	Aggregate/Region(%)					88.16%	230.255		Diagnostic
O.3	Residence/Region(%)					92.21%	172.960		No
O.3	Business/Region(%)					57.26%	6.507		No
O.3	UNE/Region(%)					78.35%	50.788		No
% Flow Through Service Requests - Achieved									
F.1.2.1									
O.3	Summary/Region(%)					72.88%	278.519		Diagnostic
O.3	Aggregate/Region(%)					72.88%	278.519		Diagnostic
O.3	Residence/Region(%)					79.67%	200.170		Diagnostic
O.3	Business/Region(%)					41.13%	9.059		Diagnostic
O.3	UNE/Region(%)					57.41%	69.280		Diagnostic
% Flow Through Service Requests - LNP									
F.1.3.1									
O.3	Summary/Region(%)					91.83%	8.854		YES
O.3	Aggregate/Region(%)					91.83%	8.854		YES
O.3	Residence/Region(%)								Diagnostic
O.3	Business/Region(%)								Diagnostic
General - Pre-Ordering									
F.2.1.1									
PQ-1	Loop/STN(%)					100.00%	1		YES
Loop Makeup Inquiry (Manual)									
F.2.2.1									
PQ-2	Loop/STN(%)					100.00%	287		YES
Loop Makeup Inquiry (Electronic)									
General - Ordering									
F.3.1.1									
O.10	xDSL (ADSL, HDSL, and ULL) STN(%)					= 95% w in 5 bus days			
O.10	Local Interoffice Transport STN(%)					= 95% w in 5 bus days			
Service Inquiry with Firm Order									
F.4.1									
PQ-1	Parity w Retail					98.00%	138		YES
General - Ordering									
F.4.2									
PQ-2	Average Speed of Answer					134.12	6,948,805	65.30	33.786
General - Maintenance Center									
F.5.1									
PQ-1	Average Answer Time					143.87	1,829,998	28.66	107.969
General - Operator Services (Toll)									
F.6.1									
PQ-1	Average Speed to Answer					5.32			PBD
% Answered in 10 seconds									
F.6.2									
PQ-2	DS-2 TTN(%)					79.60%			PBD
General - Directory Assistance									
Average Speed to Answer									

BellSouth Monthly State Summary
Tennessee, June 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
F.7.1	[DA:1] [TN(seconds)]	PBD		6.24						PBD
F.7.2	% Answered in 10 Seconds	PBD		77.20%						PBD
	[DA:2] [TN(%)]									
	General - E911									
F.8.1	Mean Interval	PBD		1.50	1.917					PBD
F.8.2	E-3 [TN(hours)]	PBD		96.35%	219.851					PBD
F.8.3	% Accuracy	PBD								PBD
	[E:2] [TN(%)]									
	% Timeliness	PBD								PBD
F.8.4	E-1 [TN(%)]	PBD		100.00%	1.917					PBD
	General - Billing									
F.9.1	Usage Data Delivery Accuracy	Parity w Retail		99.65%	6.084	100.00%	14.987		0.00089	-3.8726
F.9.2	B-3 [Region(%)]	Parity w Retail		97.39%	36.844	98.21%	193.986	.733	0.00083	-9.8353
F.9.3	Usage Data Delivery Timeliness	Parity w Retail		99.78%	36.844	99.94%	193.986	.433		YES
F.9.4	B-5 [Region(%)]	Parity w Retail		75.14%	\$7.734.	107	90.45%	\$67.331	0.00336	-45.6397
		Parity w Retail		>= 90%			86.75%	\$65.725		YES
				>= 90%			92.44%	\$1.455		YES
F.9.5.1	Recurring Charge Completeness	Parity w Retail		79.12%	\$10.683.	734	98.36%	\$90.737	0.00297	-6.48743
F.9.5.2	B-7 [Retail(%)]			>= 90%			98.45%	\$330.256		YES
F.9.5.3	B-7 [UNE/TN(%)]			>= 90%			91.01%	\$241.034		YES
	B-7 [Interconnection/TN(%)]									
F.9.6.1	Non-Recurring Charge Completeness									
F.9.6.2	B-8 [Retail(%)]									
F.9.6.3	B-8 [UNE/TN(%)]									
	B-8 [Interconnection/TN(%)]									
	General - Change Management									
F.10.1	% Software Release Notices Sent On Time									YES
	CM-1 [TN(%)]									
F.10.2	Average Software Release Notice Delay Days									
	= 22 bus days prior to release									
F.10.3	% Change Management Documentation Sent On Time									
	CM-3 [TN(%)]									
F.10.4	% Change Management Documentation (Defects, Corrections, etc.) Sent On Time									YES
	CM-3B [TN(%)]									
F.10.5	Average Documentation Release Delay Days									
	= 22 bus days prior to release									
F.10.6	% CLEC Interface Outages Sent within 15 Minutes									YES
	CM-5 [TN(%)]									
	General - Ordering									
F.12.1	Acknowledgement of Message Timeliness									
	O-1 [EDI/Region(%)]									
	= 90% w in 30 min									

BellSouth Monthly State Summary
Tennessee, June 2001

		Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
F.12.1.2	[D-1] TAGRegion(%)				99.96%	127,390				YES
	Acknowledgement Message Completeness									
F.12.2.1	[D-2] EDIRegion(%)				97.14%	58,137				NO
F.12.2.2	[D-2] TAGRegion(%)				99.96%	127,390				NO
	General - Database Updates									
	Average Database Update Interval									
F.13.1.1	[D-1] LIDB/T(N)hours	PBD	0.90	20	0.90	20				
F.13.1.2	[D-1] Directory Listings/T(N)hours	PBD	0.07	26	0.07	26				
F.13.1.3	[D-1] Directory Assistance/T(N)hours	PBD	3.92	25	2.94	25				
	% Update Accuracy									
F.13.2.1	[D-2] LIDB/T(N)%	>= 95%	100.00%	84						YES
F.13.2.2	[D-2] Directory Listings/T(N)%	>= 95%	100.00%	76						YES
F.13.2.3	[D-2] Directory Assistance/T(N)%	>= 95%	100.00%	76						YES
	% NXXs / LRNs Loaded by LERG Effective Date									
F.13.3	[D-3] T(N)%	100%		46						YES
	General - Network Outage Notification									
	Mean Time to Notify CLEC of Major Network Outages									
F.14.1	[M&R-7] TN (minutes)	Parity w Retail	102	3	4,013	3				NO

BellSouth Monthly State Summary

Tennessee, June 2001

% Completions w/o Notice or < 24 hours (Resale)	
A.2.24.1.1	P-6
A.2.24.1.2	P-6
A.2.24.2.1	P-6
A.2.24.2.2	P-6
A.2.24.3.1	P-6
A.2.24.3.2	P-6
A.2.24.4.1	P-6
A.2.24.4.2	P-6
A.2.24.5.1	P-6
A.2.24.5.2	P-6
A.2.24.6.1	P-6
A.2.24.6.2	P-6

% Completions w/o Notice or < 24 hours (LINE)	
B.2.32.1.1	P-6
B.2.32.1.2	P-6
B.2.32.2.1	P-6
B.2.32.2.2	P-6
B.2.32.3.1	P-6
B.2.32.3.2	P-6
B.2.32.4.1	P-6
B.2.32.4.2	P-6
B.2.32.5.1	P-6
B.2.32.5.2	P-6
B.2.32.6.1	P-6
B.2.32.6.2	P-6
B.2.32.7.1	P-6
B.2.32.7.2	P-6
B.2.32.8.1	P-6
B.2.32.8.2	P-6
B.2.32.9.1	P-6
B.2.32.9.2	P-6
B.2.32.10.1	P-6
B.2.32.10.2	P-6
B.2.32.11.1	P-6
B.2.32.11.2	P-6
B.2.32.12.1	P-6
B.2.32.12.2	P-6
B.2.32.13.1	P-6
B.2.32.13.2	P-6
B.2.32.14.1	P-6
B.2.32.14.2	P-6
B.2.32.15.1	P-6
B.2.32.15.2	P-6
B.2.32.16.1	P-6
B.2.32.16.2	P-6
B.2.32.17.1	P-6

Benchmark / Analog	BST Measure	BST Volume	CLEC Measure	CLEC Volume	Standard Deviation	Standard Error	ZScore	Equity
					100.00%	475		
					100.00%	10,254		
					100.00%	79		
					100.00%	236		
					100.00%	7		
					100.00%	60		
					100.00%	7		
					100.00%	107		
					100.00%	1,159		
					100.00%	1		
					100.00%	715		
					100.00%	89		
					100.00%	61		
					100.00%	67		
					100.00%	123		
					100.00%	1		
					100.00%	108		
					100.00%	1,875		
					100.00%	1		
					100.00%	13		

B.2.32.17.2	P-6	LNP (Standalone)/Non-Dispatch/TN (%)		Diagnostic
B.2.32.18.1	P-6	Digital Loop < DS1/Dispatch/TN (%)		Diagnostic
B.2.32.18.2	P-6	Digital Loop < DS1/Non-Dispatch/TN (%)		Diagnostic
B.2.32.19.1	P-6	Digital Loop >= DS1/Dispatch/TN (%)		Diagnostic
B.2.32.19.2	P-6	Digital Loop >= DS1/Non-Dispatch/TN (%)		Diagnostic
% Completions w/o Notice or < 24 hours (LT)				
C.2.10.1	P-6	Local Interconnection Trunks/Dispatch/TN (%)		Diagnostic
C.2.10.2	P-6	Local Interconnection Trunks/Non-Dispatch/TN (%)		Diagnostic
% New Business Requests Processed within 30 Business Days				
F.11.1	BFR-1	Region (%)	>= 90% w in 30 bus days	100.00%
		% Quotes Provided within X Business Days		4
F.11.2.1	BFR-2A	Region (%)	>= 90% w in 10 bus days	100.00%
F.11.2.2	BFR-2B	Region (%)	>= 90% w in 30 bus days	0.00%
F.11.2.3	BFR-2C	Region (%)	>= 90% w in 60 bus days	2

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (SUMMARY)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

	ACHIEVED FLOW-THROUGH %	ADJUSTED FLOW- THROUGH %
CLEC AGGREGATE		
REGION ALL SERVICES	72.88%	88.16%
BST AGGREGATE		
REGION		
- RETAIL RESIDENCE	94.40%	
- RETAIL BUSINESS**	TBD	

****NOTE: BellSouth is reinstituting the reporting of business retail flow through as directed by the Georgia Public Service Commission. BellSouth currently has no way to measure flow through for the Regional Operating System (ROS) interface used by business retail. BellSouth retail reports capture all business service requests submitted from all sources, including manually. BellSouth has initiated the development of an accurate report and will reflect this measure as soon as its development is complete**

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH										
Company Info		Mechanized Interface Used					LESOG					Errors					CLEC Error Excluded Calculation					
Name	RESH / OCN	LENS	EDI	TAG	Total LSR's	Manual Fallout	Total Mech LSR's	Auto Clarification	Pending Supps (Z Status)	LSR's	Validated	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Achieved Flowthrough	Issued SO's	Base Calculation					
#1	0	115	0	115	32	28	0	55	22	6	16	33	46.48%	60.00%	84.62%	73.17%	73.17%	78.95%				
#2	52	0	0	52	3	8	0	41	11	8	3	30	73.17%	73.17%	11.32%	25.82%	25.82%	62.44%				
#3	0	3454	0	3454	570	666	0	2218	1967	151	1816	251	76.64%	76.64%	72.73%	72.73%	72.73%	84.52%				
#4	976	0	0	976	71	86	16	803	219	107	112	584	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%				
#5	12	0	0	12	0	2	0	10	0	0	0	10	10	10	10	10	10	100.00%				
#6	0	19	0	19	5	2	0	12	6	6	6	6	6	6	6	6	6	35.29%	50.00%	50.00%		
#7	0	21	0	21	5	2	0	14	6	3	3	3	8	8	8	8	8	50.00%	57.14%	72.73%		
#8	30	0	0	30	2	3	7	18	11	10	10	1	7	7	7	7	7	36.84%	38.89%	41.18%		
#9	915	0	0	915	107	112	0	696	38	28	28	10	658	658	658	658	658	82.98%	94.54%	95.92%		
#10	164	0	0	164	28	8	2	126	60	51	51	9	66	66	66	66	66	45.52%	52.38%	56.41%		
#11	1797	0	0	1797	228	182	3	134	190	161	161	29	1194	1194	1194	1194	1194	75.43%	86.27%	88.12%		
#12	2	0	0	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%		
#13	0	0	0	13	13	2	1	0	10	4	3	1	6	6	6	6	6	54.55%	60.00%	66.67%		
#14	2	0	0	2	0	0	0	0	2	0	0	0	0	0	0	0	0	100.00%	100.00%	100.00%		
#15	0	0	0	17	17	13	3	0	1	1	1	0	0	0	0	0	0	0.00%	0.00%	0.00%		
#16	2259	0	0	2259	290	286	36	1648	374	265	265	119	1274	1274	1274	1274	1274	70.00%	77.31%	83.32%		
#17	359	0	0	359	19	28	1	311	25	23	23	2	286	286	286	286	286	87.20%	91.98%	92.56%		
#18	286	0	0	286	36	32	3	215	72	66	66	6	143	143	143	143	143	58.37%	66.51%	68.42%		
#19	1382	0	0	1382	136	49	5	1192	76	62	62	14	1116	1116	1116	1116	1116	84.93%	93.62%	94.74%		
#20	0	0	0	6	6	0	2	0	4	3	3	0	1	1	1	1	1	25.00%	25.00%	25.00%		
#21	38	0	0	38	15	7	2	14	9	8	8	1	5	5	5	5	5	17.83%	35.71%	35.46%		
#22	0	0	0	2	2	0	0	0	2	2	1	1	0	0	0	0	0	0.00%	0.00%	0.00%		
#23	0	0	0	1477	1477	209	253	8	1007	427	349	78	580	580	580	580	580	50.97%	57.60%	62.43%		
#24	197	0	0	197	37	15	2	143	9	8	8	1	134	134	134	134	134	74.86%	93.71%	94.37%		
#25	17	0	0	17	2	3	1	11	2	2	2	0	9	9	9	9	9	69.23%	81.82%	81.82%		
#26	76	0	0	76	5	15	2	54	31	28	28	3	23	23	23	23	23	41.07%	42.59%	45.10%		
#27	0	0	0	900	900	130	133	1	636	256	219	219	37	380	380	380	380	380	52.13%	59.75%	63.44%	
#28	0	0	0	18	18	1	1	0	16	16	7	9	0	0	0	0	0	0.00%	0.00%	0.00%		
#29	0	0	0	23	23	4	2	13	6	2	2	4	7	7	7	7	7	53.85%	53.85%	77.78%		
#30	100	0	0	100	10	2	2	78	31	24	24	7	47	47	47	47	47	58.02%	60.26%	66.20%		
#31	54	0	0	54	13	4	0	37	2	2	2	0	35	35	35	35	35	70.00%	94.59%	94.59%		
#32	0	0	0	1287	1287	281	246	11	749	345	242	242	103	404	404	404	404	404	43.58%	53.94%	62.54%	
#33	0	212	0	212	163	23	0	26	20	19	19	1	6	6	6	6	6	3.19%	23.08%	24.00%		
#34	914	0	0	914	499	114	2	299	110	80	80	30	189	189	189	189	189	24.61%	63.21%	70.26%		

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
 REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
 Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING												FLOW THROUGH					
		LESOG						CLEC						Base Calculation			CLEC Error Excluded Calculation		
		Company Info		Mechanized Interface Used				Manual Rejects		Pending Supps (Z Status)		LSR's		Total System Fallout		BST Caused Fallout		Achieved Throughput	
Resh / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Fallout	Auto Clarification	LSP Status	Total	Rejects	Pending	Supps (Z Status)	LSP's	Total	System Fallout	BST Caused Fallout	Issued SO's	Achieved Throughput	Base Calculation	CLEC Error Excluded Calculation
#35	2	0	0	2	0	0	0	0	0	0	0	496	222	179	43	1	50.00%	50.00%	50.00%
#36	0	0	780	780	120	155	9	11	327	136	115	21	191	274	47.82%	55.24%	60.49%	60.49%	
#37	0	0	554	554	112	104	0	0	221	20	15	5	201	79.13%	45.69%	58.41%	62.42%	62.42%	
#38	273	0	0	273	38	14	0	0	0	5	1	1	0	4	66.67%	80.00%	90.95%	93.06%	93.06%
#39	6	0	0	6	1	0	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#40	524	0	0	524	130	25	0	0	369	15	12	3	354	71.37%	95.93%	95.93%	96.72%	96.72%	
#41	479	0	0	479	51	48	9	9	371	108	79	29	263	66.92%	70.89%	70.89%	76.90%	76.90%	
#42	0	198	0	198	25	16	8	8	149	69	53	16	80	50.63%	53.69%	53.69%	60.15%	60.15%	
#43	4	0	0	4	3	0	0	0	1	1	1	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#44	17	0	0	17	1	1	0	0	15	1	1	1	0	14	87.50%	93.33%	93.33%	93.33%	93.33%
#45	0	32	32	10	2	0	0	0	20	4	4	0	0	16	53.33%	80.00%	80.00%	80.00%	80.00%
#46	65	0	0	65	8	13	0	0	44	5	3	2	39	78.00%	88.64%	88.64%	92.86%	92.86%	
#47	333	0	0	333	24	29	1	1	279	30	20	10	249	84.98%	89.25%	89.25%	92.57%	92.57%	
#48	704	0	0	704	175	57	6	6	466	144	121	23	322	52.10%	69.10%	72.69%	72.69%	72.69%	
#49	2090	0	0	2090	440	164	7	7	1479	403	324	79	1076	58.48%	72.75%	72.75%	76.86%	76.86%	
#50	52	0	0	52	12	4	1	1	35	9	6	3	26	59.09%	74.29%	74.29%	81.25%	81.25%	
#51	65	0	0	65	15	12	0	0	38	9	9	0	29	54.72%	76.32%	76.32%	76.32%	76.32%	
#52	571	0	0	571	55	6	0	0	510	4	4	0	506	89.55%	99.22%	99.22%	99.22%	99.22%	
#53	48	0	0	48	0	0	0	1	47	47	46	1	0	0.00%	0.00%	0.00%	0.00%	0.00%	
#54	514	0	0	514	28	25	1	1	460	28	26	2	432	88.89%	93.91%	93.91%	94.32%	94.32%	
#55	36	0	0	36	3	1	1	1	31	4	2	2	27	84.39%	87.10%	87.10%	93.10%	93.10%	
#56	0	0	29	29	11	5	1	1	12	9	1	8	3	20.00%	25.00%	25.00%	75.00%	75.00%	
#57	711	0	0	711	93	115	7	7	496	84	63	21	412	72.54%	83.06%	86.74%	86.74%	86.74%	
#58	0	0	11	11	0	2	2	2	7	7	3	4	0	0.00%	0.00%	0.00%	0.00%	0.00%	
#59	112	0	0	112	8	10	9	9	85	79	54	25	6	8.82%	7.06%	10.00%	56.25%	58.59%	
#60	26	0	0	26	3	2	5	5	16	13	4	9	3	30.00%	18.75%	42.86%	80.71%	84.07%	
#61	1083	0	0	1083	162	88	5	5	828	79	64	15	749	76.82%	90.46%	90.46%	92.13%	92.13%	
#62	29	0	0	29	4	4	0	0	21	2	2	0	19	76.00%	90.48%	90.48%	90.48%	90.48%	
#63	0	0	581	581	93	87	1	1	400	175	159	16	225	47.17%	56.25%	58.59%	60.49%	60.49%	
#64	520	0	0	520	48	43	4	4	425	82	65	17	343	75.22%	80.71%	84.07%	84.07%	84.07%	
#65	2076	0	0	2076	276	131	9	9	1660	142	121	21	1518	79.27%	91.45%	91.45%	92.62%	92.62%	
#66	1548	0	0	1548	67	249	6	6	1226	253	156	97	973	81.35%	79.36%	86.18%	86.18%	86.18%	
#67	184	0	0	184	13	15	0	0	156	8	7	1	148	88.10%	94.87%	95.48%	95.48%	95.48%	
#68	0	0	2	2	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	
#69	105	0	0	105	30	18	0	0	57	16	10	6	41	50.62%	71.93%	80.39%	80.39%	80.39%	

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ORDERING

REPORT: PERCENT FLOWTHROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH									
		Mechanized Interface Used					LESOG					CLEC					CLEC Error Excluded Calculation				
Name	Company Info	RESH / OCN		LENS	EDI	TAG	Total LSR's	Manual	Rejects	Pending	Supps (Z Status)	LST's	Total	System	BST	Cause	Achieved	Base	Calculation		
		Total	5	0	5	3	0	0	0	2	1	1	1	1	4	4	1	0	20.00%	50.00%	50.00%
#70		0	3	0	0	3	0	3	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#71		2	0	0	2	0	0	0	0	2	1	1	1	0	0	1	1	50.00%	50.00%	50.00%	50.00%
#72		0	4	0	4	0	4	0	2	0	0	0	0	0	0	0	2	100.00%	100.00%	100.00%	100.00%
#73		0	0	33	33	7	12	2	12	2	12	4	4	4	4	0	8	42.11%	66.67%	66.67%	66.67%
#74		0	0	0	44	4	11	2	27	9	8	1	1	1	1	1	18	60.00%	66.67%	66.67%	69.33%
#75		0	83	0	83	9	10	0	64	23	13	10	10	10	10	10	41	65.08%	64.06%	64.06%	75.93%
#76		74	0	0	74	12	10	4	48	18	16	2	2	2	2	2	30	51.72%	62.50%	62.50%	65.22%
#77		72	0	0	72	14	5	0	53	15	14	1	1	1	1	1	38	57.58%	71.70%	71.70%	73.08%
#78		0	0	13	13	2	6	0	5	3	3	0	0	0	0	0	2	28.57%	40.00%	40.00%	40.00%
#79		11	0	0	11	3	4	0	4	1	1	0	0	0	0	0	3	42.86%	75.00%	75.00%	75.00%
#80		86	0	0	86	18	13	5	50	16	13	3	3	3	3	3	34	52.31%	68.00%	68.00%	72.34%
#81		793	0	0	793	130	50	6	607	55	39	16	16	16	16	16	552	76.56%	90.94%	90.94%	93.40%
#82		0	0	105	105	11	6	0	88	34	28	6	6	6	6	6	54	58.06%	61.36%	61.36%	65.85%
#83		129	0	0	129	18	6	1	104	20	17	3	3	3	3	3	84	70.59%	80.77%	80.77%	83.17%
#84		2	0	0	2	0	2	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#85		0	0	2	2	1	0	0	1	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#86		31	0	0	31	4	1	1	25	12	1	11	11	11	11	11	13	72.22%	52.00%	52.00%	92.86%
#87		0	0	1	1	0	0	0	1	1	1	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#88		27	0	0	27	1	5	1	20	3	3	0	0	0	0	0	17	80.95%	85.00%	85.00%	85.00%
#89		11	0	0	11	1	1	0	9	0	0	0	0	0	0	0	9	90.00%	100.00%	100.00%	100.00%
#90		38	0	0	38	6	2	0	235	19	15	4	4	4	4	4	22	68.75%	73.33%	73.33%	91.51%
#91		294	0	0	294	32	27	0	2	7	5	3	2	2	2	2	2	25.00%	28.57%	40.00%	40.00%
#92		14	0	0	14	3	2	0	0	2	2	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#93		0	0	2	2	0	0	0	0	2	2	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#94		195	0	0	195	42	8	5	140	50	33	17	17	17	17	17	90	54.55%	64.29%	64.29%	73.17%
#95		0	0	241	241	35	26	1	179	94	67	27	27	27	27	27	85	45.45%	47.49%	47.49%	55.92%
#96		235	0	0	235	17	26	7	185	35	24	11	11	11	11	11	150	78.53%	81.08%	86.21%	86.21%
#97		90	0	0	90	35	12	1	42	16	13	3	3	3	3	3	26	35.14%	61.90%	66.67%	66.67%
#98		93	0	0	93	10	0	0	83	6	6	0	0	0	0	0	77	82.80%	92.77%	92.77%	92.77%
#99		9	0	0	9	0	0	0	0	9	3	2	1	1	1	1	6	75.00%	66.67%	66.67%	75.00%
#100		0	6	0	6	2	0	0	0	4	4	1	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#101		3	0	0	3	2	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#102		228	0	0	228	30	15	0	183	10	6	4	4	4	4	4	173	82.78%	94.54%	94.54%	96.65%
#103		26	0	0	26	3	3	0	20	5	3	2	2	2	2	2	15	71.43%	75.00%	75.00%	83.33%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH									
		Mechanized Interface Used					LESOG					Errors					CLEC Error Excluded Calculation				
		Name	RESH / OCN	LENS	EDI	TAG	Total	Manual Rejects	Total	Manual Supps	Auto Clarification	Validated	Total	System Fallout	BST Caused	Cause	Total	Achieved	Flowthrough	Base Calculation	
							(Z Status)	(Z Status)	LSR's	LSR's	LSR's	LSR's	LSR's	LSR's	LSR's	LSR's	LSR's	LSR's	LSR's	LSR's	
#105		14	0	0	0	0	14	0	0	0	0	14	2	2	2	12	85.71%	85.71%	85.71%	85.71%	
#106		611	0	0	0	0	611	83	30	2	496	48	39	9	448	78.60%	90.32%	91.99%	91.99%		
#107		0	24	0	24	11	0	2	11	11	10	1	0	0	0	0.00%	0.00%	0.00%	0.00%		
#108		332	0	0	0	0	332	35	18	0	279	9	7	2	270	86.54%	96.77%	97.47%	97.47%		
#109		0	0	604	604	27	74	0	503	19	14	5	484	92.19%	96.22%	97.19%	97.19%				
#110		468	0	0	468	49	66	2	351	26	21	5	325	82.28%	92.59%	93.93%	93.93%				
#111		172	0	0	172	25	74	1	72	9	5	4	63	67.74%	87.50%	92.65%	92.65%				
#112		97	0	0	97	9	0	0	88	2	2	0	86	88.66%	97.73%	97.73%	97.73%				
#113		4	0	0	4	0	1	0	3	3	3	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%		
#114		1000	0	0	1000	103	88	2	807	43	32	11	764	84.98%	94.67%	95.98%	95.98%				
#115		293	0	0	293	41	8	0	244	23	20	3	221	78.33%	90.57%	91.70%	91.70%				
#116		163	0	0	163	37	8	2	116	18	16	2	98	64.90%	84.48%	85.96%	85.96%				
#117		0	0	28	28	7	2	0	19	3	2	1	16	64.00%	84.21%	88.89%	88.89%				
#118		36	0	0	36	1	3	0	32	3	2	1	29	90.63%	90.63%	93.55%	93.55%				
#119		0	0	777	777	11	63	0	703	25	16	9	678	96.17%	96.44%	97.69%	97.69%				
#120		449	0	0	449	124	85	2	238	36	27	9	202	57.22%	84.87%	88.21%	88.21%				
#121		212	0	0	212	28	13	1	170	15	15	0	155	78.28%	91.18%	91.18%	91.18%				
#122		440	0	0	440	47	24	0	369	10	5	5	359	87.35%	97.29%	98.63%	98.63%				
#123		686	0	0	686	17	115	0	554	65	58	7	489	86.70%	88.27%	89.40%	89.40%				
#124		0	0	39	39	12	0	2	26	9	9	0	16	43.24%	64.00%	64.00%	64.00%				
#125		0	114	0	114	31	9	0	74	27	20	7	47	47.98%	63.51%	70.15%	70.15%				
#126		49	0	0	49	9	3	0	37	12	4	8	25	65.79%	67.57%	86.21%	86.21%				
#127		72	0	0	72	10	2	2	58	17	17	0	41	60.29%	70.69%	70.69%	70.69%				
#128		6	0	0	6	0	2	0	4	1	1	0	3	75.00%	75.00%	75.00%	75.00%				
#129		250	0	0	250	48	29	0	173	36	32	4	137	63.13%	79.19%	81.07%	81.07%				
#130		31	0	0	31	2	2	0	27	4	3	1	23	82.14%	85.19%	88.46%	88.46%				
#131		33	0	0	33	3	2	0	28	5	2	3	23	82.14%	82.14%	92.00%	92.00%				
#132		2	0	0	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%	50.00%				
#133		0	58	0	58	14	7	0	37	10	3	7	27	61.36%	72.97%	90.00%	90.00%				
#134		26	0	0	26	7	4	0	15	6	0	6	9	56.25%	60.00%	100.00%	100.00%				
#135		135	0	0	135	11	1	0	123	2	2	0	121	90.30%	98.37%	98.37%	98.37%				
#136		237	0	0	237	56	9	0	172	8	8	0	164	71.93%	95.35%	95.35%	95.35%				
#137		1561	0	0	1561	132	47	0	132	72	58	14	1310	87.33%	94.79%	95.76%	95.76%				
#138		0	0	882	882	143	13	78	648	489	433	56	159	21.63%	24.54%	26.86%	26.86%				
#139		880	0	0	880	82	28	1	769	71	66	5	698	82.51%	90.77%	91.36%	91.36%				

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH									
		Mechanized Interface Used					LESOG					CLEC					CLEC Error Excluded Calculation				
		RESH / OCN		LENS	EDI	TAG	Total Mech LSR's	Manual Total	Rejects	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Achieved Flowthrough	Issued SO's	Base Calculation			
#140		1646	0	0	1646	133	95	5	1413	79	53	26	1334	87.76%	94.41%	96.18%					
#141		3805	0	0	3805	536	193	37	3039	201	162	39	2838	80.26%	93.39%	94.60%					
#142		86	0	0	86	6	4	1	75	12	9	3	63	80.77%	84.00%	87.50%					
#143		41	0	0	41	4	9	0	28	2	2	0	26	81.25%	92.86%	92.86%					
#144		0	25	0	25	0	6	3	16	16	0	16	0	0.00%	0.00%	0.00%	0.00%				
#145		4	0	0	4	0	2	0	2	2	1	1	0	0.00%	0.00%	0.00%	0.00%				
#146		34	0	0	34	5	4	2	23	11	5	6	12	54.55%	52.17%	70.59%					
#147		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%				
#148		33	0	0	33	16	6	0	11	7	6	1	4	15.38%	36.36%	40.00%					
#149		213	0	0	213	16	9	0	188	8	8	0	180	88.22%	95.74%	95.74%					
#150		196	0	0	196	19	20	0	157	9	7	2	148	85.00%	94.27%	95.48%					
#151		2	0	0	2	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%				
#152		506	0	0	506	66	41	3	396	26	15	11	370	82.04%	93.43%	96.10%					
#153		83	0	0	83	1	1	0	81	6	3	3	75	94.90%	92.55%	96.15%					
#154		106	0	0	106	21	11	0	74	20	13	7	54	61.36%	72.97%	80.60%					
#155		0	0	397	397	70	30	4	293	70	61	9	223	62.99%	76.11%	78.52%					
#156		966	0	0	966	138	74	10	744	131	105	26	613	71.61%	82.39%	85.38%					
#157		1	0	0	1	0	0	0	1	1	1	0	0	0.00%	0.00%	0.00%	0.00%				
#158		0	0	13	13	0	0	0	0	13	8	3	5	5	62.50%	38.46%	62.50%				
#159		230	0	0	230	28	21	0	181	6	3	3	175	84.99%	96.65%	98.31%					
#160		424	0	0	424	203	30	8	183	89	68	21	94	25.75%	51.37%	58.02%					
#161		55	0	0	55	1	1	0	53	5	5	0	48	88.85%	90.57%	90.57%					
#162		0	0	5	5	0	1	0	4	1	1	0	3	75.00%	75.00%	75.00%					
#163		38	0	0	38	2	1	1	34	4	4	0	30	83.33%	88.24%	88.24%					
#164		23	0	0	23	3	3	0	17	9	8	1	8	42.11%	47.08%	50.00%					
#165		1441	0	0	1441	215	109	1	1116	57	44	13	1059	80.33%	94.38%	96.01%					
#166		214	0	0	214	42	23	5	144	49	40	9	95	53.67%	65.97%	70.37%					
#167		14	0	0	14	2	1	0	11	2	0	2	9	81.82%	81.82%	100.00%					
#168		111	0	0	111	23	6	4	78	38	31	7	40	42.55%	51.28%	56.34%					
#169		0	0	15	15	0	0	0	15	13	11	2	2	15.38%	13.33%	15.38%					
#170		0	0	381	381	20	162	1	198	31	16	15	167	82.27%	84.34%	91.26%					
#171		952	0	0	952	65	61	0	826	19	16	3	807	90.88%	97.70%	98.06%					
#172		780	0	0	780	84	129	1	566	68	60	8	498	77.57%	87.99%	89.25%					
#173		75	0	0	75	20	10	2	43	14	13	1	29	46.77%	67.44%	69.05%					
#174		128	0	0	128	18	15	0	95	11	9	2	84	75.66%	88.42%	90.32%					

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPE		LSR PROCESSING										FLOWTHROUGH											
		LESOG					Errors					CTEC					CLEC Error Excluded Calculation						
Name	RESH / OCN	Mechanized Interface Used			Manual Total		Rejected		Pending Supps (Z Status)		LSR's	Validated		Total		BST Caused Fallout		Caused Fallout		Achieved Flowthrough		Base Calculation	
		LENS	EDI	TAG	Total	Mech LSRs	Manual Fallout	Auto Clarification	System Fallout	BST Caused Fallout		Total	System Fallou	Total	System Fallou	BST Caused Fallout	Caused Fallout	Achieved Flowthrough	Base Calculation	Flowthrough	Base Calculation		
#175		7236	0	0	7236	486	572	10	6168	401	311	90	5767	87.86%	93.50%	94.88%	93.50%	86.97%	86.97%	87.28%			
#176		342	0	0	342	30	27	1	284	37	36	1	247	78.91%	86.97%	92.41%	92.41%	92.59%	92.59%	92.59%			
#177		1272	0	0	1272	112	76	3	1081	82	80	2	999	83.88%	94.12%	100.00%	100.00%	100.00%	100.00%	100.00%			
#178	0	0	117	117	6	15	0	96	0	0	0	0	96	94.12%	94.12%	94.57%	94.57%	95.60%	95.60%	95.60%			
#179	213	0	0	213	95	26	0	92	5	4	1	87	46.77%	46.77%	44.83%	44.83%	50.00%	50.00%	50.00%				
#180	45	0	0	45	13	3	0	29	16	13	3	13	33.33%	33.33%	44.83%	44.83%	50.00%	50.00%	50.00%				
#181	32	0	0	32	2	4	0	26	2	2	0	24	24	85.71%	85.71%	92.31%	92.31%	92.31%	92.31%	92.31%			
#182	203	0	0	203	32	2	1	168	8	6	2	160	80.81%	80.81%	95.24%	95.24%	96.39%	96.39%	96.39%				
#183	414	0	0	414	103	61	0	250	26	13	13	224	65.88%	65.88%	89.60%	89.60%	94.51%	94.51%	94.51%				
#184	1	0	0	1	0	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%			
#185	147	0	0	147	25	9	0	113	6	6	0	107	77.54%	77.54%	94.69%	94.69%	94.69%	94.69%	94.69%				
#186	0	0	9999	9999	5052	1320	96	3531	1548	1105	443	1983	24.36%	24.36%	56.16%	56.16%	64.22%	64.22%	64.22%				
#187	8434	0	0	8434	447	820	38	7129	735	565	170	6394	86.34%	86.34%	89.69%	89.69%	91.88%	91.88%	91.88%				
#188	2069	0	0	2069	281	105	6	1677	163	107	56	1514	79.60%	79.60%	90.28%	90.28%	93.40%	93.40%	93.40%				
#189	77	0	0	77	2	20	2	53	39	22	17	14	14	36.84%	36.84%	26.42%	26.42%	38.89%	38.89%	38.89%			
#190	4	0	0	4	0	0	0	0	4	4	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%			
#191	43	0	0	43	2	7	0	34	14	7	7	20	68.97%	68.97%	58.82%	58.82%	74.07%	74.07%	74.07%				
#192	2403	0	0	2403	145	250	2	2006	137	77	60	1869	89.38%	89.38%	93.17%	93.17%	96.04%	96.04%	96.04%				
#193	1215	0	0	1215	86	76	1	1033	59	44	15	994	88.43%	88.43%	94.40%	94.40%	95.76%	95.76%	95.76%				
#194	96	0	0	96	5	16	0	76	4	4	0	71	88.73%	88.73%	94.67%	94.67%	94.67%	94.67%	94.67%				
#195	1	0	0	1	0	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%			
#196	120	0	0	120	52	9	1	58	18	16	2	40	40	37.04%	37.04%	68.97%	68.97%	71.43%	71.43%	71.43%			
#197	10	0	0	10	3	1	0	6	3	3	0	3	3	33.33%	33.33%	50.00%	50.00%	50.00%	50.00%	50.00%			
#198	2	0	0	2	0	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%			
#199	0	0	3	3	0	3	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%			
#200	65	0	0	65	19	15	0	31	12	12	0	19	38.00%	38.00%	61.29%	61.29%	66.67%	66.67%	66.67%				
#201	1	0	0	1	0	0	0	0	1	1	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%			
#202	2	0	0	2	2	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%			
#203	4	0	0	4	0	0	0	0	4	2	2	0	2	2	2	40.00%	40.00%	40.00%	40.00%	40.00%			
#204	0	0	23	7	8	0	0	8	4	2	2	4	4	30.77%	30.77%	50.00%	50.00%	66.67%	66.67%	66.67%			
#205	195	0	0	195	30	34	1	130	26	23	3	104	66.24%	66.24%	80.00%	80.00%	81.89%	81.89%	81.89%				
#206	74	0	0	74	7	5	0	62	5	4	1	57	83.82%	83.82%	91.94%	91.94%	93.44%	93.44%	93.44%				
#207	10	0	0	10	0	0	2	3	5	3	0	2	2	2	2	100.00%	100.00%	100.00%	100.00%	100.00%			
#208	0	0	2	2	0	0	0	0	0	0	0	0	0	0	0	0	100.00%	100.00%	100.00%	100.00%	100.00%		
#209	30	0	0	30	0	0	2	0	28	23	12	11	5	29.41%	29.41%	29.41%	29.41%	29.41%	29.41%	29.41%			

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH									
		Mechanized Interface Used					Manual					Errors					CLEC				
		RESH / OCN	Name	LENS	EDI	TAG	Total LSR's	Mechanical Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total	BST Caused Fallout	Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation			
#210		0	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#211		4	0	0	4	0	0	0	0	4	1	206	81	70	11	125	43.55%	75.00%	100.00%	100.00%	100.00%
#212		333	0	0	333	92	34	1	22	4	9	1	8	7	4	3	1	11.11%	60.68%	64.10%	20.00%
#213		22	0	0	22	0	4	0	0	1	0	1	1	0	0	1	0	0.00%	0.00%	12.50%	20.00%
#214		0	0	2	2	0	1	0	1	0	1	0	1	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#215		5	0	0	5	0	0	0	0	5	3	2	2	1	2	2	50.00%	40.00%	50.00%	50.00%	50.00%
#216		1	0	0	1	0	0	0	0	1	1	1	1	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#217		135	0	0	135	35	16	2	82	44	35	9	9	38	35.19%	46.34%	52.05%	52.05%	52.05%	13.79%	11.76%
#218		47	0	0	47	9	4	0	34	30	25	5	4	4	10.53%	11.76%	13.79%	13.79%	13.79%	13.79%	13.79%
#219		96	0	0	96	24	5	0	67	5	4	1	1	1	1	62	68.89%	92.54%	93.94%	93.94%	93.94%
#220		7	0	0	7	0	1	0	0	6	0	0	0	0	0	6	100.00%	100.00%	100.00%	100.00%	100.00%
#221		4	0	0	4	1	0	0	0	3	0	0	0	0	0	3	75.00%	100.00%	100.00%	100.00%	100.00%
#222		0	0	982	982	126	58	15	783	82	42	40	40	40	40	701	80.67%	89.53%	94.35%	94.35%	94.35%
#223		422	0	0	422	19	30	0	373	8	7	1	1	1	1	365	93.35%	97.86%	98.12%	98.12%	98.12%
#224		65	0	0	65	2	9	3	51	16	12	4	4	4	4	35	71.43%	68.63%	74.47%	74.47%	74.47%
#225		13	0	0	13	1	2	0	10	4	2	2	2	2	2	6	66.67%	60.00%	75.00%	75.00%	75.00%
#226		692	0	0	692	23	38	3	628	21	18	3	3	3	3	607	93.67%	96.66%	97.12%	97.12%	97.12%
#227		0	0	4028	4028	116	68	15	3829	277	221	56	56	56	56	3552	91.33%	92.77%	94.14%	94.14%	94.14%
#228		8803	0	0	8803	658	388	5	7732	278	243	35	35	35	35	7474	89.24%	96.41%	98.85%	98.85%	98.85%
#229		9	0	0	9	2	3	1	3	1	2	2	2	2	2	0	1	20.00%	33.33%	33.33%	33.33%
#230		0	0	3341	3341	56	454	29	2892	1182	822	360	360	360	360	1620	64.85%	57.82%	66.34%	66.34%	66.34%
#231		61	0	61	26	11	0	24	4	4	4	0	0	0	0	20	40.00%	83.33%	83.33%	83.33%	83.33%
#232		0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#233		30	0	0	30	14	9	0	7	3	2	2	1	1	1	4	20.00%	57.14%	66.67%	66.67%	66.67%
#234		24	0	0	24	13	4	1	6	5	5	5	5	5	5	1	5.26%	16.67%	16.67%	16.67%	16.67%
#235		779	0	0	779	87	31	7	654	43	34	9	9	9	9	611	83.47%	93.43%	94.73%	94.73%	94.73%
#236		45	0	0	45	6	7	0	32	8	8	0	0	0	0	24	63.16%	75.00%	75.00%	75.00%	75.00%
#237		735	0	0	735	65	16	1	653	17	15	2	2	2	2	636	88.83%	97.40%	97.70%	97.70%	97.70%
#238		389	0	0	389	49	23	1	316	16	10	6	6	6	6	300	83.57%	94.94%	96.77%	96.77%	96.77%
#239		831	0	0	831	58	62	0	711	46	36	10	10	10	10	665	87.62%	93.53%	94.86%	94.86%	94.86%
#240		242	0	0	242	22	39	9	172	75	52	23	23	23	23	97	56.73%	56.40%	65.10%	65.10%	65.10%
#241		238	0	0	238	8	10	1	219	39	28	11	11	11	11	180	83.33%	82.19%	86.54%	86.54%	86.54%
#242		914	0	0	914	95	79	8	732	93	82	11	11	11	11	639	78.31%	87.30%	88.63%	88.63%	88.63%
#243		177	0	0	177	18	46	2	111	69	53	16	16	16	16	42	37.17%	37.84%	44.21%	44.21%	44.21%
#244		49	0	0	49	0	2	2	45	44	28	16	16	16	16	1	3.45%	2.22%	3.45%	3.45%	3.45%

8/29/2001

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ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH									
		Mechanized Interface Used					LESOG					CLEC					Base Calculation				
Name	Company Info	RESH / OCN	LENS	EDI	TAG	Total LSR's	Manual Fallout	Rejects	Pending	Auto Clarification	Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Achieved Flowthrough	Issued SO's	Base Calculation	CLEC Error Excluded Calculation		
#245		0	0	929	929	20	95	1	813	11	8	3	802	96.63%	98.65%	99.01%					
#246		82	0	0	82	1	8	1	72	8	6	2	64	90.14%	88.89%	91.43%					
#247		338	0	0	338	47	13	1	277	27	15	12	250	80.13%	90.25%	94.34%					
#248		65	0	0	65	34	9	0	22	15	11	4	7	13.46%	31.82%	38.89%					
#249		24305	0	0	24305	7393	349	108	13395	4104	3046	1058	9291	47.09%	69.36%	75.31%					
#250		373	0	0	373	31	24	0	318	22	19	3	296	85.55%	93.08%	93.97%					
#251		0	0	251	251	46	59	1	145	33	28	4	112	59.89%	77.24%	79.43%					
#252		392	0	0	392	55	82	1	254	81	70	11	173	58.05%	68.11%	71.19%					
#253		0	0	23	23	2	2	0	19	3	2	1	16	80.00%	84.21%	88.89%					
#254		69	0	0	69	13	13	0	43	4	4	0	39	69.64%	90.70%	90.70%					
#255		0	0	7	7	1	1	0	5	1	1	0	4	66.67%	80.00%	80.00%					
#256		26	0	0	26	8	3	0	16	7	6	1	8	36.39%	53.33%	57.14%					
#257		38	0	0	38	3	6	0	29	1	1	0	28	87.50%	96.55%	96.55%					
#258		24	0	0	24	1	7	2	14	7	6	1	7	50.00%	53.85%	53.85%					
#259		283	0	0	283	21	3	0	259	7	6	1	252	90.32%	97.30%	97.67%					
#260		30	0	0	30	2	0	0	28	0	0	0	28	93.33%	100.00%	100.00%					
#261		124	0	0	124	26	6	3	89	27	22	5	62	56.36%	69.66%	73.81%					
#262		3070	0	0	3070	226	358	2	244	103	90	13	2381	88.28%	95.85%	96.36%					
#263		0	520	0	520	384	56	0	80	48	30	18	32	7.17%	40.00%	51.61%					
#264		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%					
#265		12	0	0	12	0	6	0	6	5	3	2	1	25.00%	16.67%	25.00%					
#266		0	0	14	14	7	3	0	4	1	1	0	3	27.22%	75.00%	75.00%					
#267		36	0	0	36	8	2	1	25	6	4	2	19	61.29%	76.00%	82.61%					
#268		0	1458	0	1458	74	103	0	1281	172	144	28	1109	83.57%	86.57%	88.51%					
#269		2	0	0	2	0	0	0	2	2	1	1	0	0.00%	0.00%	0.00%					
#270		0	0	61	61	30	7	0	24	14	11	3	10	19.56%	41.67%	47.62%					
#271		49	0	0	49	6	4	0	39	14	13	1	25	56.82%	64.10%	65.79%					
#272		0	18760	0	18760	3338	2248	7	13167	1960	1518	442	11207	69.77%	85.11%	88.07%					
#273		10	0	0	10	2	4	0	4	3	0	3	1	33.33%	25.00%	100.00%					
#274		6	0	6	0	3	0	3	0	3	1	1	2	66.67%	66.67%	66.67%					
#275		2	0	0	2	1	1	0	0	0	0	0	0	0.00%	0.00%	0.00%					
#276		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%					
#277		3	0	0	3	1	0	0	3	1	1	0	2	66.67%	66.67%	66.67%					
#278		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%					
#279		4	0	0	4	0	0	0	4	0	0	0	4	100.00%	100.00%	100.00%					

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ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH									
		Mechanized Interface Used					LESOG					CLEC					CLEC Error Excluded Calculation				
Name	Company Info	RESH / OCN		LENS	EDI	TAG	Total LSR's	Mechanical Failout	Manual Clarification	Auto	Rejects	Pending Supps	(Z Status)	LSR's	Total System Fallout	BST Caused Fallout	Cause SO's	Achieved Flowthrough	Base Calculation		
		0	399	0	399	0	249	121	2	27	2	27	2	27	7	20	0	0.00%	0.00%		
#280		0	1	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0.00%	0.00%		
#281		17	0	0	0	17	4	0	1	12	5	3	3	2	7	7	50.00%	58.33%	70.00%		
#282		0	607	0	607	393	91	4	114	44	31	13	13	70	70	14.03%	61.40%	69.31%	50.00%		
#283		26	0	0	26	2	3	0	21	14	7	7	7	7	7	43.75%	33.33%	85.07%	75.00%		
#284		65	0	0	65	0	5	3	57	21	12	9	16	12	9	36	75.00%	63.16%	100.00%	100.00%	
#285		0	4	4	0	0	1	3	0	0	0	0	0	0	0	3	0.00%	0.00%	0.00%	0.00%	
#286		0	14	0	14	11	0	0	0	3	3	1	1	2	0	0	0.00%	0.00%	0.00%	0.00%	
#287		108	0	0	108	23	8	3	74	17	10	7	7	7	7	57	63.33%	77.03%	76.92%	92%	
#288		0	6225	0	6225	746	1222	3	4254	159	66	93	93	4095	4095	83.45%	96.26%	98.41%	94.94%	94.94%	
#289		204	0	0	204	14	10	0	180	11	9	2	2	169	169	88.02%	93.89%	93.89%	62.50%	62.50%	
#290		52	0	0	52	30	5	1	16	6	3	3	3	10	10	10	23.26%	23.26%	92%	92%	
#291		0	9087	0	9087	1496	1807	5	5779	301	134	167	167	5478	5478	77.07%	94.79%	97.61%	95.24%	95.24%	
#292		346	0	0	346	23	27	0	296	16	14	2	2	280	280	88.33%	94.59%	94.59%	100.00%	100.00%	
#293		0	13	0	13	4	0	0	9	2	0	2	2	2	2	7	7	77.78%	77.78%	100.00%	100.00%
#294		0	267	0	267	101	53	7	106	45	34	11	11	61	61	31.12%	57.55%	64.21%	66.89%	66.89%	
#295		0	0	0	0	792	167	105	15	505	204	149	149	55	301	301	48.78%	59.60%	87.59%	87.59%	
#296		1	0	0	1	0	0	0	0	1	1	1	1	0	0	0	0.00%	0.00%	0.00%	0.00%	
#297		8	0	0	8	5	1	0	0	2	2	2	2	0	0	0	0.00%	0.00%	0.00%	0.00%	
#298		2	0	0	2	2	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	
#299		2	0	0	2	2	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	
#300		7	0	0	7	1	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	
#301		482	0	0	482	31	32	4	415	62	50	50	50	12	353	353	81.34%	85.05%	85.05%	85.51%	
#302		184	0	0	184	28	13	1	142	24	20	4	20	4	118	118	71.08%	83.10%	83.10%	44.98%	62.99%
#303		848	0	0	848	69	84	10	685	377	181	196	196	308	308	55.20%	33.33%	33.33%	14.29%	14.29%	
#304		0	9257	0	9257	164	1781	3	7399	1844	1227	617	617	5465	5465	79.71%	74.77%	81.66%	90.27%	90.27%	
#305		25	0	0	25	0	5	4	16	14	12	2	2	2	2	2	14.29%	12.50%	12.50%	100.00%	100.00%
#306		2	0	0	2	0	0	0	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#307		279	0	0	279	13	38	0	228	24	22	22	22	2	204	204	85.36%	89.41%	89.41%	58.82%	58.82%
#308		35	0	0	35	5	7	1	22	12	7	5	5	10	10	45.45%	45.45%	45.45%	93.33%	93.33%	
#309		0	0	0	25	25	2	4	0	19	5	1	1	4	14	82.35%	73.68%	73.68%	28.57%	35.29%	
#310		35	0	0	35	14	0	0	21	15	11	4	4	6	6	19.35%	28.57%	28.57%	14.29%	14.29%	
#311		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#312		26	0	0	36	36	0	5	1	30	11	8	3	3	19	19	70.37%	63.33%	63.33%	41.67%	45.45%
#313		491	0	0	491	18	27	0	446	18	14	4	4	4	428	428	93.04%	95.96%	95.96%	96.83%	96.83%

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ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH					
		Mechanized Interface Used					LESOG					CLEC Error Excluded Calculation					
Company Info	Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Rejects	Total Supps (Z Status)	Auto Clarification	Validated	Total Errors	BST Caused Fallout	System Caused Fallout	CFC Caused Fallout	Achieved Flowthrough	Base Calculation	
#315		36	0	0	36	4	4	0	28	2	2	0	26	81.23%	92.86%	92.86%	
#316		5	0	0	5	2	1	0	0	2	0	0	0	2	50.00%	100.00%	100.00%
#317		0	0	8	8	0	1	0	7	4	4	0	0	3	42.86%	42.86%	42.86%
#318		18	0	0	18	5	3	1	9	4	1	3	5	45.45%	55.56%	83.33%	83.33%
#319		10	0	0	10	0	0	0	10	2	2	0	8	80.00%	80.00%	80.00%	80.00%
#320		916	0	0	916	216	174	6	520	243	190	53	277	40.56%	53.21%	59.31%	59.31%
#321		2	0	0	2	0	1	0	1	1	1	0	0	0	0.00%	0.00%	0.00%
#322		11	0	0	11	0	1	0	10	0	0	0	10	100.00%	100.00%	100.00%	100.00%
#323		0	0	1	1	0	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#324		3386	0	0	3386	427	224	8	2727	224	193	31	2503	80.15%	91.79%	92.84%	92.84%
#325		0	0	3412	3412	520	321	55	2516	339	220	119	2177	74.63%	86.53%	90.82%	90.82%
#326		15101	0	0	15101	385	979	10	13727	253	198	55	13474	95.85%	98.16%	98.55%	98.55%
#327		0	56	0	56	2	4	0	50	11	6	5	39	82.98%	78.00%	86.67%	86.67%
#328		170	0	0	170	2	6	0	162	5	4	1	157	96.32%	96.91%	97.52%	97.52%
#329		494	0	0	494	19	73	2	400	5	4	1	395	94.50%	98.75%	99.00%	99.00%
#330		0	0	3	3	0	2	0	1	0	0	0	1	100.00%	100.00%	100.00%	100.00%
#331		190	0	0	190	35	23	1	131	27	23	4	104	64.20%	79.39%	81.89%	81.89%
#332		0	2192	0	2192	1076	260	11	845	235	129	106	610	33.61%	72.19%	82.54%	82.54%
#333		221	0	0	221	75	20	1	125	53	44	9	72	37.70%	57.60%	62.07%	62.07%
#334		0	0	33	33	10	10	0	13	2	2	0	11	47.83%	84.62%	84.62%	84.62%
#335		59	0	0	59	17	12	1	29	13	8	5	16	39.02%	55.17%	66.67%	66.67%
#336		0	9555	9555	277	701	16	8561	245	191	54	8316	94.67%	97.14%	97.75%	97.75%	
#337		3146	0	0	3146	408	211	19	2508	261	202	59	2247	78.65%	89.59%	91.75%	91.75%
#338		706	0	0	706	75	63	13	555	146	124	22	409	67.27%	73.69%	76.74%	76.74%
#339		135	0	0	135	19	7	2	107	42	31	11	65	56.52%	60.75%	67.71%	67.71%
#340		0	417	0	417	210	74	6	127	80	58	22	47	14.92%	37.01%	44.76%	44.76%
#341		615	0	0	615	99	38	5	473	120	100	20	353	63.95%	74.63%	77.92%	77.92%
#342		1061	0	0	1061	222	165	14	660	305	241	64	355	43.40%	53.79%	59.56%	59.56%
#343		45	0	0	45	27	3	0	15	12	10	2	3	7.56%	20.00%	23.08%	23.08%
#344		1038	0	0	1038	147	39	1	851	46	30	16	805	81.98%	94.59%	96.41%	96.41%
#345		345	0	0	345	7	28	0	310	15	10	5	295	94.55%	95.16%	96.72%	96.72%
#346		0	105	0	105	5	2	0	98	5	0	93	1186	95.41%	98.10%	98.42%	98.42%
#347		374	0	0	374	52	44	5	273	85	68	17	188	61.04%	68.86%	73.44%	73.44%
#348		41	0	0	41	4	5	0	32	12	12	0	20	55.56%	62.50%	62.50%	62.50%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAILED)
 REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
 Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH							
		Mechanized Interface Used			LESOG			Errors			CTEC			Achieved Flowthrough			Base Calculation		
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Total	Rejects	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	Caused Fallout	Issued SO's					
#350		15	0	0	15	6	1	0	0	8	5	4	1	3	37.50%	37.50%	42.86%	42.86%	
#351		129	0	0	129	7	14	1	107	9	8	1	1	98	86.73%	91.59%	91.59%	92.45%	
#352		1007	0	0	1007	88	57	1	861	82	74	8	779	82.78%	90.48%	90.48%	91.32%		
#353		13	0	0	13	0	1	0	12	1	1	0	11	91.67%	91.67%	91.67%	91.67%		
#354		0	0	1527	1527	167	99	5	126	148	118	30	1108	79.54%	88.22%	88.22%	90.38%		
#355		83	0	0	83	4	5	0	74	4	4	0	70	89.74%	94.59%	94.59%	94.59%		
#356		0	2447	0	2447	854	195	1	1387	212	162	50	1185	53.84%	84.82%	84.82%	87.91%		
#357		3176	0	0	3176	440	173	17	2546	581	485	86	1965	67.76%	77.18%	77.18%	79.88%		
#358		24	0	0	24	2	9	3	10	8	5	3	2	22.22%	20.00%	20.00%	28.57%		
#359		0	1787	1787	35	330	4	1418	14	12	2	1404	96.78%	99.01%	99.01%	99.15%			
#360		49	0	0	49	1	5	0	43	6	6	0	37	84.09%	86.05%	86.05%	86.05%		
#361		4	0	0	4	0	0	0	4	3	3	0	1	25.00%	25.00%	25.00%	25.00%		
#362		579	0	0	579	39	38	1	501	23	16	7	478	89.68%	95.41%	95.41%	96.76%		
#363		100	0	0	100	22	11	0	67	29	25	4	38	44.71%	56.72%	60.32%	60.32%		
#364		0	169	0	169	115	11	3	40	21	18	3	19	12.50%	47.50%	51.35%	51.35%		
#365		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%		
#366		0	0	13	13	6	0	0	7	1	1	0	6	46.15%	85.71%	85.71%	85.71%		
#367		12	0	0	12	0	2	2	2	8	4	2	4	66.67%	50.00%	66.67%	66.67%		
#368		0	0	485	485	10	76	0	399	4	3	1	395	96.81%	99.00%	99.00%	99.25%		
#369		21	0	0	21	2	1	0	18	4	4	0	14	70.00%	77.78%	77.78%	77.78%		
#370		67	0	0	67	8	10	3	46	10	8	2	36	69.29%	78.28%	81.82%	81.82%		
#371		2418	0	0	2418	487	266	9	1656	688	566	122	968	47.98%	58.45%	63.10%	63.10%		
#372		463	0	0	463	82	24	1	356	34	31	3	322	74.02%	90.45%	91.22%	91.22%		
#373		53	0	0	53	12	2	0	39	0	0	0	39	76.47%	100.00%	100.00%	100.00%		
#374		136	0	0	136	2	24	4	106	39	12	27	67	82.72%	63.21%	84.81%	84.81%		
#375		323	0	0	323	22	10	1	290	17	13	4	273	88.64%	94.14%	95.45%	95.45%		
#376		46	0	0	46	7	0	0	39	8	7	1	31	68.89%	79.49%	81.58%	81.58%		
#377		0	0	58	58	2	8	0	48	3	3	0	45	90.00%	93.75%	93.75%	93.75%		
#378		101	0	0	101	16	3	0	82	3	3	0	79	80.61%	96.34%	96.34%	96.34%		
#379		0	4162	0	4162	547	804	2	2809	115	57	58	2894	81.69%	95.91%	95.91%	97.93%		
#380		170	0	0	170	8	5	0	157	28	27	1	129	78.66%	82.11%	82.69%	82.69%		
#381		0	0	186	186	62	49	6	69	49	41	8	20	16.26%	28.59%	32.79%	32.79%		
#382		9347	0	0	9347	1178	641	106	7422	1853	1646	217	5559	66.31%	74.90%	77.15%	77.15%		
#383		0	272	0	272	165	54	0	53	19	15	4	34	15.89%	64.15%	64.15%	69.39%		
#384		26	0	0	26	4	7	0	15	5	4	1	10	55.56%	66.67%	71.43%	71.43%		

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH										
		Mechanized Interface Used					LESOG					CLEC					CLEC Error Excluded Calculation					
Name	Company Info	RESH / OCN		LENS	EDI	TAG	Total LSR's	Manual Fallout	Auto Clarification	Rejects	Permitting Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation				
#385		786	0	0	786	146	83	9	548	194	164	30	354	53.3%	64.60%	68.34%	64.60%	64.60%	64.60%	64.60%	64.60%	
#386		95	0	0	95	4	7	0	84	1	1	0	83	94.32%	98.81%	98.81%	98.81%	98.81%	98.81%	98.81%	98.81%	
#387		71	0	0	71	6	12	2	51	22	18	4	29	54.72%	56.86%	61.70%	56.86%	56.86%	56.86%	56.86%	56.86%	
#388		0	94	0	94	56	3	1	34	13	12	1	21	23.60%	61.76%	63.64%	61.76%	61.76%	61.76%	61.76%	61.76%	
#389		10	0	0	10	1	2	0	7	1	0	1	6	85.71%	85.71%	10.00%	85.71%	85.71%	85.71%	85.71%	85.71%	
#390		0	0	0	43	6	5	1	31	9	7	2	22	62.86%	70.97%	75.86%	62.86%	62.86%	62.86%	62.86%	62.86%	
#391		448	0	0	448	37	15	1	395	31	25	6	384	85.45%	92.15%	93.57%	85.45%	85.45%	92.15%	92.15%	92.15%	
#392		3028	0	0	3028	518	230	7	2273	297	224	73	1976	72.70%	86.93%	89.32%	72.70%	72.70%	86.93%	86.93%	86.93%	
#393		1	0	0	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#394		40	0	0	40	5	11	0	24	9	8	1	15	53.57%	62.50%	65.22%	53.57%	53.57%	62.50%	62.50%	62.50%	
#395		94	0	0	94	2	8	0	84	28	23	5	56	69.14%	66.67%	70.89%	66.67%	66.67%	66.67%	66.67%	66.67%	
#396		27110	0	0	27110	3811	2479	63	20757	1618	1364	254	19139	78.72%	92.21%	93.35%	78.72%	78.72%	92.21%	92.21%	92.21%	
#397		13	0	0	13	2	0	0	11	4	3	1	7	58.33%	63.64%	70.00%	58.33%	58.33%	63.64%	63.64%	63.64%	
#398		82	0	0	82	51	10	0	21	6	3	3	15	21.74%	71.43%	83.33%	21.74%	21.74%	71.43%	71.43%	71.43%	
#399		549	0	0	549	55	62	4	428	44	34	10	384	81.18%	89.72%	91.87%	81.18%	81.18%	89.72%	89.72%	91.87%	
#400		387	0	0	387	44	22	2	319	61	37	24	258	76.11%	80.88%	87.46%	76.11%	76.11%	80.88%	80.88%	87.46%	
#401		160	0	0	160	15	14	0	131	2	2	0	129	88.38%	98.47%	98.47%	88.38%	88.38%	98.47%	98.47%	98.47%	
#402		168	0	0	168	39	10	2	117	47	36	11	70	48.28%	59.83%	66.04%	48.28%	48.28%	59.83%	59.83%	66.04%	
#403		60	0	0	60	0	3	0	57	1	1	0	56	98.25%	98.25%	98.25%	98.25%	98.25%	98.25%	98.25%	98.25%	
#404		272	0	0	272	9	26	0	237	12	9	3	225	92.59%	94.94%	96.15%	92.59%	92.59%	94.94%	94.94%	96.15%	
#405		122	0	0	122	3	7	0	112	6	5	1	106	92.98%	94.64%	95.50%	92.98%	92.98%	94.64%	94.64%	95.50%	
#406		167	0	0	167	17	11	3	136	19	18	1	117	76.97%	86.03%	86.67%	76.97%	76.97%	86.03%	86.03%	86.67%	
#407		89	0	0	89	5	11	0	73	4	3	1	69	89.6%	94.52%	95.83%	89.6%	89.6%	94.52%	94.52%	95.83%	
#408		0	2	0	2	0	0	0	2	2	1	1	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#409		523	0	0	523	77	59	20	367	156	125	31	211	51.00%	57.49%	62.80%	51.00%	51.00%	57.49%	57.49%	62.80%	
#410		0	560	0	560	109	61	2	388	128	101	27	260	55.32%	67.01%	72.02%	55.32%	55.32%	67.01%	67.01%	72.02%	
#411		664	0	0	664	108	110	8	438	146	125	21	292	55.62%	66.67%	70.02%	55.62%	55.62%	66.67%	66.67%	70.02%	
#412		0	2	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#413		2229	0	0	2229	116	177	6	1930	153	136	17	1777	87.58%	92.07%	92.89%	87.58%	87.58%	92.07%	92.07%	92.89%	
#414		400	0	0	400	50	17	1	332	29	21	8	303	81.02%	91.27%	93.52%	81.02%	81.02%	91.27%	91.27%	93.52%	
#415		18	0	0	18	1	8	1	8	3	0	5	5	55.56%	62.50%	62.50%	55.56%	55.56%	62.50%	62.50%	62.50%	
#416		701	0	0	701	43	42	1	615	27	18	9	588	90.6%	95.61%	97.03%	90.6%	90.6%	95.61%	95.61%	97.03%	
#417		1036	0	0	1036	131	134	8	763	183	167	16	580	66.06%	76.02%	77.64%	66.06%	66.06%	76.02%	76.02%	77.64%	
#418		0	137	0	137	33	41	2	61	17	11	6	44	50.00%	72.13%	80.00%	50.00%	50.00%	72.13%	72.13%	80.00%	
#419		0	0	21	21	9	8	0	4	0	0	4	4	30.77%	100.00%	100.00%	30.77%	30.77%	100.00%	100.00%	100.00%	

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ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPE		LSR PROCESSING										FLOWTHROUGH					
Company Info		Mechanized Interface Used					Manual Processing					CLEC Error Excluded Calculation					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Fallout	Auto Clarification	Total Supps (Z Status)	LSR's	Validated	Errors	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Achieved Flowthrough	Base Calculation	
#420		42	0	0	42	4	9	0	29	5	5	0	0	24	72.73%	82.76%	82.76%
#421		123	0	0	123	22	12	2	87	27	27	0	0	60	55.05%	68.97%	68.97%
#422		0	152	0	152	52	20	3	77	43	38	5	34	27.42%	44.16%	47.22%	44.16%
#423		1385	0	0	1385	222	166	35	962	528	448	80	434	39.31%	45.11%	49.21%	45.11%
#424		0	8	0	8	0	1	0	7	2	2	0	5	71.43%	71.43%	71.43%	71.43%
#425		2075	0	0	2075	221	218	25	1611	847	696	151	764	45.45%	47.42%	52.33%	47.42%
#426		367	0	0	367	27	26	0	314	19	17	2	295	87.02%	93.95%	94.55%	94.55%
#427		85	0	0	85	13	12	2	58	26	24	2	32	46.38%	55.17%	57.14%	57.14%
#428		3281	0	0	3281	325	254	9	2633	164	112	52	2529	85.27%	93.91%	95.76%	95.76%
#429		226	0	0	226	147	22	7	50	29	21	8	21	11.11%	42.00%	56.00%	42.00%
#430		207	0	0	207	3	1	4	199	39	36	3	160	80.40%	80.40%	80.40%	80.40%
#431		0	0	1	1	0	1	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%
#432		7	0	0	7	1	0	0	6	1	0	1	5	83.33%	83.33%	100.00%	100.00%
#433		1397	0	0	1397	125	91	8	1173	111	81	30	1052	83.75%	90.54%	92.91%	92.91%
#434		48	0	0	48	2	6	0	40	12	9	3	28	71.79%	70.00%	75.68%	70.00%
#435		13	0	0	13	7	0	0	6	6	4	2	0	0.00%	0.00%	0.00%	0.00%
#436		7	0	0	7	1	0	0	6	1	1	0	5	71.43%	83.33%	83.33%	83.33%
#437		1221	0	0	1221	173	48	7	993	173	154	19	820	71.49%	82.58%	84.19%	84.19%
#438		608	0	0	608	134	114	15	345	197	150	47	148	34.26%	42.90%	49.66%	49.66%
<i>LENS Subtotal</i>		215175	0	0	215175	29256	19129	1058	165732	23344	18359	4985	142388	74.94%	85.91%	88.58%	88.58%
<i>EDI Subtotal</i>		0	61313	0	61313	10966	9731	68	40548	7619	4045	3574	32929	68.63%	81.21%	89.06%	89.06%
<i>TAG Subtotal</i>		0	0	47859	47859	8042	5284	387	34146	6472	4860	1612	27674	68.20%	81.05%	85.06%	85.06%
TOTAL INTERFACES		215176	61313	47859	324347	48264	34144	1613	240426	37436	27264	10171	202991	72.85%	84.43%	88.16%	88.16%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (RESIDENCE DETAIL)
 REPORT PERIOD: 06/01/2001 - 06/30/2001

AGGREGATE ORDER TYPES	Company Info	LSR PROCESSING										FLOWTHROUGH							
		Mechanized Interface Used					Validated					Errors				Achieved Flowthrough			
		RESH / OCN	NAME	LENS	EDI	TAG	Total Mech LSR's	Manual Fallout	Auto Clarification	Total Supps (Z Status)	LSRs	System Fallout	BST Caused Fallout	CLEC Caused Fallout	ISSUED SO's	7	10	7	17
#1		0	8	0	8	0	34	1	0	0	0	29	12	0	0	12	17	94.44%	58.62%
#2		0	34	0	0	0	34	1	4	0	0	2	1	1	0	1	1	25.00%	50.00%
#3		8	0	0	8	0	2	4	0	0	0	3	2	1	1	1	1	33.33%	33.33%
#4		0	4	0	4	1	0	0	0	0	0	3	2	1	1	1	1	33.33%	33.33%
#5		914	0	0	914	107	112	0	0	695	38	28	10	637	82.95%	94.53%	94.53%	95.91%	
#6		164	0	0	164	28	8	2	126	60	51	9	66	45.52%	52.38%	56.41%	56.41%		
#7		458	0	0	458	56	66	2	334	53	45	8	281	73.56%	84.13%	86.20%	86.20%		
#8		2	0	0	2	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	
#9		29	0	0	29	2	7	0	20	14	11	11	3	6	31.58%	30.00%	30.00%	35.29%	
#10		358	0	0	358	19	28	1	310	25	23	23	2	285	87.16%	91.94%	91.94%	92.53%	
#11		267	0	0	267	36	26	3	202	61	56	5	141	60.52%	69.80%	71.57%	71.57%		
#12		1382	0	0	1382	136	49	5	1192	76	62	14	1116	84.93%	93.62%	94.74%	94.74%		
#13		0	0	4	4	0	2	0	2	2	2	0	0	0	0.00%	0.00%	0.00%	0.00%	
#14		196	0	0	196	37	15	2	142	9	8	1	133	74.72%	93.68%	94.33%	94.33%		
#15		17	0	0	17	2	3	1	11	2	2	2	0	9	69.22%	81.82%	81.82%	81.82%	
#16		54	0	0	54	13	4	0	37	2	2	2	0	35	70.00%	94.59%	94.59%	94.59%	
#17		0	8	0	8	0	5	0	3	0	0	0	0	3	100.00%	100.00%	100.00%	100.00%	
#18		2	0	0	2	0	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%	50.00%	
#19		273	0	0	273	38	14	0	221	20	15	5	201	79.13%	90.95%	93.06%	93.06%		
#20		524	0	0	524	130	25	0	369	15	12	3	354	71.37%	95.93%	96.72%	96.72%		
#21		17	0	0	17	1	1	0	15	1	1	0	14	87.50%	93.33%	93.33%	93.33%		
#22		333	0	0	333	24	29	1	279	30	20	10	249	84.98%	89.25%	92.57%	92.57%		
#23		4	0	0	4	1	3	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	
#24		17	0	0	17	0	11	1	5	3	2	1	2	50.00%	40.00%	50.00%	50.00%		
#25		51	0	0	51	12	4	1	34	9	6	3	25	58.14%	73.53%	80.65%	80.65%		
#26		65	0	0	65	15	12	0	38	9	9	0	29	54.72%	76.32%	76.32%	76.32%		
#27		571	0	0	571	55	6	0	510	4	4	0	506	89.56%	99.22%	99.22%	99.22%		
#28		513	0	0	513	28	25	1	459	28	26	2	431	88.87%	93.90%	94.31%	94.31%		
#29		36	0	0	36	3	1	1	31	4	2	2	27	84.38%	87.10%	93.10%	93.10%		
#30		0	0	14	14	0	3	1	10	8	1	7	2	66.67%	20.00%	66.67%	66.67%		
#31		673	0	0	673	90	102	5	476	77	58	19	399	72.94%	83.82%	87.31%	87.31%		
#32		17	0	0	17	3	4	0	10	9	6	3	1	10.00%	10.00%	14.29%	14.29%		
#33		2	0	0	2	0	0	2	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%		
#34		1079	0	0	1079	162	85	5	827	79	64	15	748	76.80%	90.45%	92.12%	92.12%		

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (RESIDENCE DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

AGGREGATE ORDER TYPES		Company Info										LSR Processing										Flowthrough										
		Mechanized Interface Used					LESOG					Validated					Errors					Flowthrough					CLEC Error Excluded Calculation					
Name	RESH / OCN	LENS	EDI	TAG	Total LSR's		Manual		Rejects		Auto Clarification		Pending Supps (Z Status)		LSR's		Total System Fallout		BST Caused Fallout		Caused Fallout		Issued SO's		Achieved Flowthrough		Base Calculation					
					Total	Mech	Total	Manual	Total	Mech	Total	Auto	Total	Supps	Total	System	Total	System	Total	BST	Caused	Caused	Issued	Achieved	Flowthrough	Base	Calculation					
#35		29	0	0	29	4	4	0	0	4	0	0	0	0	21	2	2	2	0	0	0	19	76.00%	90.48%	90.48%	90.48%	0.00%	0.00%				
#36		6	0	0	6	0	0	0	0	4	0	0	0	0	2	2	2	2	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%		
#37		2066	0	0	2066	273	130	9	1654	140	119	21	1514	79.43%	91.54%	92.71%	92.71%	92.71%	92.71%	92.71%	92.71%	92.71%	92.71%	92.71%	92.71%	92.71%	92.71%	92.71%	92.71%	92.71%	92.71%	
#38		1548	0	0	1548	67	249	6	1226	253	156	97	973	81.35%	79.36%	86.18%	86.18%	86.18%	86.18%	86.18%	86.18%	86.18%	86.18%	86.18%	86.18%	86.18%	86.18%	86.18%	86.18%	86.18%	86.18%	
#39		182	0	0	182	13	15	0	154	8	7	1	146	87.95%	94.81%	95.42%	95.42%	95.42%	95.42%	95.42%	95.42%	95.42%	95.42%	95.42%	95.42%	95.42%	95.42%	95.42%	95.42%	95.42%	95.42%	
#40		3	0	0	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#41		3	0	0	3	0	0	0	0	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#42		1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	
#43		66	0	0	66	6	6	0	54	18	10	8	36	69.23%	66.67%	78.26%	78.26%	78.26%	78.26%	78.26%	78.26%	78.26%	78.26%	78.26%	78.26%	78.26%	78.26%	78.26%	78.26%	78.26%	78.26%	
#44		2	0	0	2	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#45		49	0	0	49	13	5	1	30	6	5	1	24	57.14%	80.00%	82.76%	82.76%	82.76%	82.76%	82.76%	82.76%	82.76%	82.76%	82.76%	82.76%	82.76%	82.76%	82.76%	82.76%	82.76%	82.76%	
#46		793	0	0	793	130	50	6	607	55	39	16	552	76.56%	90.94%	93.40%	93.40%	93.40%	93.40%	93.40%	93.40%	93.40%	93.40%	93.40%	93.40%	93.40%	93.40%	93.40%	93.40%	93.40%	93.40%	
#47		2	0	0	2	0	0	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#48		11	0	0	11	1	1	0	9	0	0	0	0	0	0	0	0	0	0	0	9	90.00%	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%		
#49		281	0	0	281	31	21	0	229	18	14	4	211	82.42%	92.14%	93.78%	93.78%	93.78%	93.78%	93.78%	93.78%	93.78%	93.78%	93.78%	93.78%	93.78%	93.78%	93.78%	93.78%	93.78%	93.78%	
#50		13	0	0	13	3	2	2	6	4	2	2	2	2	2	2	2	2	2	2	2	2	28.57%	33.33%	50.00%	50.00%	50.00%	50.00%	50.00%	50.00%	50.00%	50.00%
#51		3	0	0	3	2	0	0	1	1	1	0	1	0	1	0	1	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#52		4	0	0	4	1	0	0	0	3	1	0	0	1	0	1	0	1	0	1	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#53		83	0	0	83	35	11	1	36	14	12	2	22	31.88%	61.11%	64.71%	64.71%	64.71%	64.71%	64.71%	64.71%	64.71%	64.71%	64.71%	64.71%	64.71%	64.71%	64.71%	64.71%	64.71%	64.71%	
#54		93	0	0	93	10	0	0	83	6	6	0	77	82.80%	92.77%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%
#55		228	0	0	228	30	15	0	183	10	6	4	173	82.73%	94.54%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	96.65%	
#56		26	0	0	26	3	3	0	20	5	3	2	15	71.43%	75.00%	83.33%	83.33%	83.33%	83.33%	83.33%	83.33%	83.33%	83.33%	83.33%	83.33%	83.33%	83.33%	83.33%	83.33%	83.33%	83.33%	
#57		611	0	0	611	83	30	2	496	48	39	9	448	78.60%	90.32%	91.99%	91.99%	91.99%	91.99%	91.99%	91.99%	91.99%	91.99%	91.99%	91.99%	91.99%	91.99%	91.99%	91.99%	91.99%	91.99%	
#58		332	0	0	332	35	18	0	279	9	7	2	270	86.54%	96.77%	97.47%	97.47%	97.47%	97.47%	97.47%	97.47%	97.47%	97.47%	97.47%	97.47%	97.47%	97.47%	97.47%	97.47%	97.47%	97.47%	
#59		0	0	604	27	74	0	503	19	14	5	484	92.15%	96.22%	97.19%	97.19%	97.19%	97.19%	97.19%	97.19%	97.19%	97.19%	97.19%	97.19%	97.19%	97.19%	97.19%	97.19%	97.19%	97.19%		
#60		468	0	0	468	49	66	2	351	26	21	5	325	82.28%	92.59%	93.93%	93.93%	93.93%	93.93%	93.93%	93.93%	93.93%	93.93%	93.93%	93.93%	93.93%	93.93%	93.93%	93.93%	93.93%	93.93%	
#61		172	0	0	172	25	74	1	72	9	5	4	63	67.74%	87.50%	92.65%	92.65%	92.65%	92.65%	92.65%	92.65%	92.65%	92.65%	92.65%	92.65%	92.65%	92.65%	92.65%	92.65%	92.65%	92.65%	
#62		97	0	0	97	9	0	0	88	2	2	0	86	88.66%	98.66%	99.73%	99.73%	99.73%	99.73%	99.73%	99.73%	99.73%	99.73%	99.73%	99.73%	99.73%	99.73%	99.73%	99.73%	99.73%	99.73%	
#63		998	0	0	998	103	88	2	805	43	32	11	762	84.98%	94.56%	95.97%	95.97%	95.97%	95.97%	95.97%	95.97%	95.97%	95.97%	95.97%	95.97%	95.97%	95.97%	95.97%	95.97%	95.97%	95.97%	
#64		293	0	0	293	41	8	0	244	23	20	3	221	78.37%	90.57%	91.70%	91.70%	91.70%	91.70%	91.70%	91.70%	91.70%	91.70%	91.70%	91.70%	91.70%	91.70%	91.70%	91.70%	91.70%	91.70%	
#65		163	0	0	163	37	8	2	116	18	16	2	98	64.90%	84.48%	85.96%	85.96%	85.96%	85.96%	85.96%	85.96%	85.96%	85.96%	85.96%	85.96%	85.96%	85.96%	85.96%	85.96%	85.96%	85.96%	
#66		0	0	777	777	11	63	0	703	25	16	9	678	96.17%	96.44%	97.69%	97.69%	97.69%	97.69%	97.69%	97.69%	97.69%	97.69%	97.69%	97.69%	97.69%	97.69%	97.69%	97.69%	97.69%	97.69%	
#67		449	0	0	449	124	85	2	238	36	27	9	202	57.22%	84.87%	88.21%	88.21%	88.21%	88.21%	88.21%	88.21%	88.21%	88.21%	88.21%	88.21%	88.21%	88.21%	88.21%	88.21%	88.21%	88.21%	
#68		212	0	0	212	28	13	1	170	15	15	0	155	78.28%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	91.18%	

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (RESIDENCE DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3

Attachment 2A

AGGREGATE ORDER TYPES	Company Info	LSR PROCESSING										FLOWTHROUGH									
		Mechanized Interface Used					LESOG					Errors					CLEC Error Excluded Calculation				
		RESH / OCN	LENS	EDI	TAG	Total LSR's	Manual Total	Mech Manual	LSR's	Pending Supps	Auto Clarification	LSR's	Total System	BST Caused	CLEC Caused	BST Caused	LSR's	Achieved Flowthrough	Base Calculation		
#69		440	0	0	440	47	24	0	369	10	5	5	5	5	5	359	87.35%	97.29%	98.63%		
#70		686	0	0	686	17	115	0	554	65	58	58	7	7	7	489	86.70%	88.27%	89.40%		
#71		3	0	0	3	2	0	0	1	0	0	0	0	0	0	1	33.33%	100.00%	100.00%		
#72		72	0	0	72	10	2	2	58	17	17	17	0	0	0	41	60.29%	70.69%	70.69%		
#73		214	0	0	214	36	20	0	159	22	18	18	4	4	4	137	72.11%	86.16%	88.39%		
#74		31	0	0	31	2	2	0	27	4	3	3	1	1	1	23	82.14%	85.19%	88.46%		
#75		30	0	0	30	2	2	0	26	4	2	2	2	2	2	22	84.62%	84.62%	91.67%		
#76		0	7	0	7	0	0	0	5	4	0	0	4	4	4	1	33.33%	20.00%	100.00%		
#77		131	0	0	131	10	1	0	120	2	2	2	0	0	0	118	90.77%	98.33%	98.33%		
#78		237	0	0	237	56	9	0	172	8	8	8	0	0	0	164	71.93%	95.35%	95.35%		
#79		1561	0	0	1561	132	47	0	132	72	58	58	14	14	14	1310	87.33%	94.79%	95.76%		
#80		874	0	0	874	80	28	1	765	71	66	66	5	5	5	694	82.62%	90.72%	91.32%		
#81		1639	0	0	1639	132	95	4	1408	77	53	53	24	24	24	1331	87.80%	94.53%	96.17%		
#82		3805	0	0	3805	536	193	37	3039	201	162	162	39	39	39	2838	80.26%	93.39%	94.60%		
#83		86	0	0	86	6	4	1	75	12	9	9	3	3	3	63	80.77%	84.00%	87.50%		
#84		41	0	0	41	4	9	0	28	2	2	2	0	0	0	26	81.25%	92.86%	92.86%		
#85		6	0	0	6	0	4	0	2	0	0	0	0	0	0	2	100.00%	100.00%	100.00%		
#86		213	0	0	213	16	9	0	188	8	8	8	0	0	0	180	88.24%	95.74%	95.74%		
#87		178	0	0	178	18	17	0	143	6	4	4	2	2	2	137	86.16%	95.80%	97.16%		
#88		2	0	0	2	2	2	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%		
#89		506	0	0	506	66	41	3	396	26	15	15	11	11	11	370	82.04%	93.43%	96.10%		
#90		83	0	0	83	1	1	0	81	6	3	3	3	3	3	75	94.94%	92.58%	96.15%		
#91		3	0	0	3	0	0	2	0	1	0	0	0	0	0	1	100.00%	100.00%	100.00%		
#92		224	0	0	224	28	21	0	175	4	3	3	1	1	1	171	84.63%	97.71%	98.28%		
#93		6	0	0	6	2	2	0	2	2	2	2	0	0	0	0	0.00%	0.00%	0.00%		
#94		43	0	0	43	1	1	0	41	1	1	1	0	0	0	40	95.24%	97.55%	97.56%		
#95		38	0	0	38	2	1	1	34	4	4	4	0	0	0	30	83.33%	88.24%	88.24%		
#96		1441	0	0	1441	215	109	1	1116	57	44	44	13	13	13	1059	80.35%	94.89%	96.01%		
#97		41	0	0	41	5	5	0	31	13	9	9	4	4	4	18	56.25%	58.06%	66.67%		
#98		0	0	381	381	20	162	1	198	31	16	16	15	15	15	167	82.27%	84.34%	91.26%		
#99		948	0	0	948	64	60	0	824	17	14	14	3	3	3	807	91.19%	97.94%	98.29%		
#100		777	0	0	777	83	129	1	564	68	60	60	6	6	6	496	77.62%	87.94%	89.21%		
#101		31	0	0	31	7	2	0	22	2	1	1	1	1	1	20	71.43%	90.91%	95.24%		
#102		128	0	0	128	18	15	0	95	11	9	9	2	2	2	84	75.68%	88.42%	90.32%		

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (RESIDENCE DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH									
		Mechanized Interface Used					LESOG					Errors					CLEC				
Name	RESH / OCN	LENS	EDI	TAG	Total	Mech	Manual	Auto	Pending	Validated	Total	System	BST	Cause	Achieved	Base	Flowthrough	Calculation	CLEC Error Excluded Calculation		
		LSR's	LSR's	LSR's	7236	0	0	7236	486	572	10	6168	401	311	90	5767	87.86%	93.50%	94.88%		
#103		321	0	0	0	321	25	21	0	275	33	32	1	1	242	80.94%	88.00%	88.32%			
#104		1272	0	0	0	1272	112	76	3	1081	82	80	2	999	83.88%	92.41%	92.59%				
#105		0	0	0	117	117	6	15	0	96	0	0	0	96	94.12%	100.00%	100.00%				
#106		213	0	0	213	95	26	0	92	5	4	1	87	46.77%	94.57%	95.60%					
#107		32	0	0	32	2	4	0	26	2	2	0	0	24	85.71%	92.31%	92.31%				
#108		203	0	0	203	32	2	1	168	8	6	2	160	80.81%	95.24%	96.39%					
#109		406	0	0	406	99	61	0	246	25	12	13	221	66.57%	89.84%	94.85%					
#110		1	0	0	1	0	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%				
#111		147	0	0	147	25	9	0	113	6	6	0	0	107	77.54%	94.69%	94.69%				
#112		0	0	0	17	17	3	6	2	6	6	2	4	0	0.00%	0.00%	0.00%				
#113		78	0	0	78	4	68	0	6	1	1	0	0	5	50.00%	83.33%	83.33%				
#114		2049	0	0	2049	281	101	6	1661	157	102	55	1504	79.70%	90.55%	93.65%					
#115		2403	0	0	2403	145	250	2	206	137	77	60	1869	89.38%	93.17%	96.04%					
#116		1215	0	0	1215	86	75	1	1053	59	44	15	994	88.43%	94.40%	95.76%					
#117		94	0	0	94	5	15	0	74	4	4	0	0	0	0.00%	0.00%	0.00%				
#118		1	0	0	1	1	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%				
#119		10	0	0	10	3	1	0	6	3	3	3	0	3	33.33%	50.00%	50.00%				
#120		2	0	0	2	0	2	0	0	0	0	0	0	0	0.00%	0.00%	0.00%				
#121		40	0	0	40	8	12	0	20	8	8	0	12	42.86%	60.00%	60.00%					
#122		74	0	0	74	7	5	0	62	5	4	1	57	83.88%	91.94%	93.44%					
#123		8	0	0	8	0	2	3	3	3	3	0	0	0	0.00%	0.00%	0.00%				
#124		297	0	0	297	84	33	1	179	69	60	9	110	43.33%	61.45%	64.71%					
#125		96	0	0	96	24	5	0	67	5	4	1	62	68.89%	92.54%	93.94%					
#126		22	0	0	22	4	9	1	8	7	4	3	1	11.11%	12.50%	20.00%					
#127		4	0	0	4	1	0	0	1	1	1	0	0	0	0.00%	0.00%	0.00%				
#128		10	0	0	10	0	3	0	7	6	5	1	1	16.67%	14.29%	16.67%					
#129		96	0	0	96	24	5	0	67	5	4	1	62	68.89%	92.54%	93.94%					
#130		4	0	0	4	1	0	0	3	0	0	0	3	75.00%	100.00%	100.00%					
#131		0	0	0	5	5	3	1	0	1	0	1	0	0	0.00%	0.00%	0.00%				
#132		21	0	0	21	4	16	0	1	1	0	0	0	0	0.00%	0.00%	0.00%				
#133		692	0	0	692	23	38	3	628	21	18	3	607	93.67%	96.33%	97.12%					
#134		0	0	4027	4027	116	68	15	3828	277	221	56	3551	91.33%	92.76%	94.14%					
#135		8802	0	0	8802	658	388	5	7751	278	243	35	7473	89.24%	96.41%	96.85%					
#136		0	0	3319	3319	54	448	29	2788	1172	813	359	1616	65.08%	57.96%	66.53%					

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (RESIDENCE DETAIL)

REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3

Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH						
		Mechanized Interface Used					LESOG					Errors			Achieved Flowthrough			
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Failout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Failout	BST Caused Failout	CLEC Caused Failout	Issued SO's	Achieved Flowthrough	Base Calculation			
#137		765	0	0	765	85	29	7	644	41	33	8	603	83.63%	93.63%	94.81%		
#138		2	0	0	2	1	0	0	1	1	1	0	0	0.00%	0.00%	0.00%	0.00%	
#139		735	0	0	735	65	16	1	633	17	15	2	636	88.83%	97.40%	97.70%		
#140		389	0	0	389	49	23	1	316	16	10	6	300	83.57%	94.94%	96.77%		
#141		831	0	0	831	58	62	0	711	46	36	10	665	87.62%	93.53%	94.86%		
#142		27	0	0	27	1	7	2	17	4	0	4	13	92.86%	76.47%	100.00%		
#143		238	0	0	238	8	10	1	219	39	28	11	180	83.33%	82.19%	86.54%		
#144		876	0	0	876	91	69	8	768	79	71	8	629	79.52%	88.84%	89.86%		
#145		11	0	0	11	0	4	0	7	4	3	1	3	50.00%	42.86%	50.00%		
#146		0	0	929	929	20	95	1	813	11	8	3	802	96.63%	98.65%	99.01%		
#147		82	0	0	82	1	8	1	72	8	6	2	64	90.14%	88.89%	91.43%		
#148		338	0	0	338	47	13	1	277	27	15	12	250	80.13%	90.25%	94.34%		
#149		22766	0	0	22766	7149	3128	91	12398	3568	2642	926	8830	47.42%	71.22%	76.97%		
#150		373	0	0	373	31	24	0	318	22	19	3	286	85.55%	93.08%	93.97%		
#151		0	0	219	219	43	42	1	133	24	22	2	109	62.64%	81.95%	83.21%		
#152		230	0	0	230	18	44	1	167	33	30	3	134	73.63%	80.24%	81.71%		
#153		69	0	0	69	13	13	0	43	4	4	0	39	69.64%	90.70%	90.70%		
#154		0	0	2	2	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%		
#155		18	0	0	18	4	3	0	11	5	4	1	6	42.86%	54.55%	60.00%		
#156		38	0	0	38	3	6	0	29	1	1	0	28	87.50%	96.55%	96.55%		
#157		3	0	0	3	0	1	0	2	0	0	0	2	100.00%	100.00%	100.00%		
#158		283	0	0	283	21	3	0	259	7	6	1	252	90.32%	97.30%	97.67%		
#159		30	0	0	30	2	0	0	28	0	0	0	28	93.33%	100.00%	100.00%		
#160		63	0	0	63	6	1	1	55	13	10	3	42	72.41%	76.36%	80.77%		
#161		3064	0	0	3064	226	358	2	2479	102	89	13	2377	88.33%	95.89%	96.39%		
#162		1	0	0	1	0	0	0	0	1	0	0	1	100.00%	100.00%	100.00%		
#163		0	1458	0	1458	74	103	0	1281	172	144	28	1109	83.57%	86.57%	88.51%		
#164		0	0	10	10	5	1	0	4	1	1	0	3	33.33%	75.00%	75.00%		
#165		8	0	0	8	2	2	0	4	3	2	1	1	20.00%	25.00%	33.33%		
#166		0	95	0	95	7	3	0	85	1	1	0	84	91.30%	98.82%	98.82%		
#167		1	0	0	1	0	0	0	0	0	0	1	1	100.00%	100.00%	100.00%		
#168		4	0	0	4	1	0	0	0	3	0	0	3	75.00%	100.00%	100.00%		
#169		41	0	0	41	0	4	0	37	1	0	1	36	100.00%	97.50%	100.00%		
#170		0	6225	0	6225	746	1222	3	4254	159	66	93	4095	83.45%	96.26%	98.41%		

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (RESIDENCE DETAIL)
 REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
 Attachment 2A

AGGREGATE ORDER TYPES	Company Info	LSR PROCESSING										FLOWTHROUGH						
		Mechanized Interface Used					LESOG					Errors			Achieved Flowthrough			
		RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Total	Rejects	Validated	Pending Supps (Z Status)	LSR's	Total System Failure	BST Caused Failure	CLEC Caused Failure	Issued SO's	2	168	88.02%
#171		204	0	0	204	14	10	0	180	11	9	2	167	5478	77.07%	94.79%	94.94%	
#172		0	9087	0	1496	1807	5	5779	301	134	2	280	88.33%	94.59%	94.59%	97.61%		
#173		346	0	0	346	23	27	0	296	16	14	51	56.67%	69.86%	69.86%	95.24%		
#174		0	0	127	127	24	26	4	73	22	15	7	7	172	74.78%	71.97%	77.21%	
#175		304	0	0	304	19	41	5	299	67	39	28	2	89	83.96%	93.68%	95.70%	
#176		2	0	0	2	1	0	0	1	1	1	0	0	0	0	0.00%	0.00%	0.00%
#177		433	0	0	433	27	24	3	379	43	36	7	7	336	84.21%	88.65%	90.32%	
#178		118	0	0	118	13	10	0	95	6	4	2	2	89	83.96%	93.68%	95.70%	
#179		271	0	0	271	6	20	2	243	100	54	46	46	143	70.44%	58.85%	72.55%	
#180		0	9257	0	9257	164	1781	3	7309	1844	1227	617	5465	79.71%	74.77%	81.66%		
#181		268	0	0	268	13	30	0	225	21	20	1	1	204	86.08%	90.67%	91.07%	
#182		34	0	0	34	4	7	1	22	12	7	5	10	10	47.62%	45.45%	58.82%	
#183		18	0	0	18	9	0	0	9	9	9	0	0	0	0.00%	0.00%	0.00%	
#184		6	0	0	6	1	2	0	3	0	0	0	0	3	75.00%	100.00%	100.00%	
#185		491	0	0	491	18	27	0	446	18	14	4	4	428	93.04%	95.96%	96.83%	
#186		36	0	0	36	4	4	0	28	2	2	0	26	26	81.25%	92.86%	92.86%	
#187		8	0	0	8	0	3	0	5	2	1	1	3	3	75.00%	60.00%	75.00%	
#188		10	0	0	10	0	0	0	10	2	2	0	8	8	80.00%	80.00%	80.00%	
#189		159	0	0	159	51	22	0	86	26	13	13	60	60	48.39%	69.77%	82.19%	
#90		11	0	0	11	0	1	0	10	0	0	0	10	10	100.00%	100.00%	100.00%	
#91		0	0	1	1	0	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#92		3384	0	0	3384	427	224	8	2725	224	193	31	2501	80.13%	91.75%	92.84%		
#93		0	0	3355	3355	505	317	54	2479	313	209	104	2166	75.2%	87.37%	91.20%		
#94		15096	0	0	15096	383	977	10	13726	253	198	55	13473	95.87%	98.16%	98.55%		
#195		0	56	0	56	2	4	0	50	11	6	5	5	39	82.98%	78.00%	86.67%	
#196		170	0	0	170	2	6	0	162	5	4	1	1	157	96.32%	96.91%	97.52%	
#197		494	0	0	494	19	73	2	400	5	4	1	1	395	94.50%	98.75%	99.00%	
#198		0	0	1	1	0	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#199		129	0	0	128	15	18	1	95	15	13	2	2	80	74.07%	84.21%	86.02%	
#200		0	152	0	152	22	33	1	98	36	15	21	21	60	61.86%	62.50%	80.00%	
#201		2	0	0	2	0	2	0	0	2	0	0	2	2	100.00%	100.00%	100.00%	
#202		0	0	9555	9555	277	701	16	8561	245	191	54	8316	94.67%	97.14%	97.75%		
#203		3146	0	0	3146	408	211	19	2508	261	202	59	2247	78.65%	89.59%	91.75%		
#204		335	0	0	335	46	27	3	259	44	35	9	215	72.64%	83.01%	86.00%		

ORDERING

REPORT: PERCENT FLOWTHROUGH SERVICE REQUESTS (RESIDENCE DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH											
		Mechanized Interface Used					Manual					Validated					Errors					CLEC Error Excluded Calculation	
		RESH / OCN	LENS	EDI	TAG	Total LSR's	Total Mech LSR's	Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSP's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Achieved Flowthrough	Base Calculation	Issued SO's	16	59.26%	69.57%	69.57%	0.00%	
#205		29	0	0	0	29	4	2	0	0	0	23	7	7	0	0	0	0	0.00%	0.00%	0.00%		
#206		1	0	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%		
#207		125	0	0	125	16	13	2	94	31	22	9	63	63	63	63	63	63	62.38%	67.02%	74.12%		
#208		32	0	0	32	20	2	0	10	7	5	2	3	3	3	3	3	3	30.00%	30.00%	37.50%		
#209		1036	0	0	1036	147	39	1	849	46	30	16	803	803	803	803	803	803	81.94%	94.58%	96.40%		
#210		345	0	0	345	7	28	0	310	15	10	5	295	295	295	295	295	295	94.55%	95.16%	96.72%		
#211		0	0	1370	1370	38	122	1	1209	23	19	4	1186	1186	1186	1186	1186	1186	95.41%	98.10%	98.42%		
#212		105	0	0	105	5	2	0	98	5	5	0	93	93	93	93	93	93	90.29%	94.90%	94.90%		
#213		230	0	0	230	18	17	2	193	30	28	2	163	163	163	163	163	163	77.99%	84.46%	85.34%		
#214		23	0	0	23	2	3	0	18	3	3	0	15	15	15	15	15	15	75.00%	83.33%	83.33%		
#215		129	0	0	129	7	14	1	107	9	8	1	98	98	98	98	98	98	86.73%	91.59%	92.45%		
#216		1007	0	0	1007	88	57	1	861	82	74	8	779	779	779	779	779	779	82.78%	90.48%	91.32%		
#217		13	0	0	13	0	1	0	12	1	1	0	11	11	11	11	11	11	91.67%	91.67%	91.67%		
#218		0	0	1527	1527	167	99	5	126	148	118	30	108	108	108	108	108	108	79.54%	88.22%	90.38%		
#219		83	0	0	83	4	5	0	74	4	4	0	70	70	70	70	70	70	89.74%	94.59%	94.59%		
#220		0	2447	0	2447	854	195	1	1387	212	162	50	1185	1185	1185	1185	1185	1185	53.84%	84.82%	87.97%		
#221		3176	0	0	3176	440	173	17	256	581	495	86	1965	1965	1965	1965	1965	1965	67.76%	77.18%	79.88%		
#222		2	0	0	2	0	1	0	1	0	0	0	0	0	0	0	0	0	100.00%	100.00%	100.00%		
#223		0	0	1787	1787	35	330	4	1418	14	12	2	1404	1404	1404	1404	1404	1404	96.76%	99.01%	99.15%		
#224		49	0	0	49	1	5	0	43	6	6	0	37	37	37	37	37	37	84.06%	86.05%	86.05%		
#225		4	0	0	4	0	0	0	0	4	3	3	0	0	0	0	0	0	25.00%	25.00%	25.00%		
#226		574	0	0	574	36	38	1	499	23	16	7	476	476	476	476	476	476	90.15%	95.39%	96.75%		
#227		1	0	0	1	0	0	0	1	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%		
#228		0	0	6	6	2	0	0	0	4	0	0	0	0	0	0	0	0	66.67%	100.00%	100.00%		
#229		0	0	485	485	10	76	0	399	4	3	1	395	395	395	395	395	395	96.8%	99.00%	99.25%		
#230		21	0	0	21	2	1	0	18	4	4	0	14	14	14	14	14	14	70.00%	77.78%	77.78%		
#231		59	0	0	59	8	8	1	42	7	6	1	35	35	35	35	35	35	71.43%	83.33%	85.37%		
#232		463	0	0	463	82	24	1	356	34	31	3	322	322	322	322	322	322	74.02%	90.45%	91.22%		
#233		53	0	0	53	12	2	0	39	0	0	0	39	39	39	39	39	39	76.4%	100.00%	100.00%		
#234		323	0	0	323	22	10	1	290	17	13	4	273	273	273	273	273	273	88.64%	94.14%	95.45%		
#235		8	0	0	8	0	0	0	0	8	2	1	6	6	6	6	6	6	85.71%	75.00%	85.71%		
#236		0	0	58	58	2	8	0	48	3	3	0	45	45	45	45	45	45	90.00%	93.75%	93.75%		
#237		101	0	0	101	16	3	0	82	3	3	0	79	79	79	79	79	79	80.61%	96.34%	96.34%		
#238		0	4162	0	4162	547	804	2	2809	115	57	58	2694	2694	2694	2694	2694	2694	81.69%	95.91%	97.93%		

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (RESIDENCE DETAIL)
 REPORT PERIOD: 06/01/2001 - 06/30/2001

AGGREGATE ORDER TYPES	Company Info	LSR PROCESSING										FLOWTHROUGH					
		Mechanized Interface Used					LESOG					Errors			Achieved Flowthrough		
		RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Total	Rejects	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Failure	BST Caused Failure	CLEC Caused Failure	Issued SO's	Achieved Flowthrough	Base Calculation
#239		170	0	0	170	8	5	0	157	28	27	1	129	78.66%	82.17%	82.68%	
#240		287	0	0	287	46	38	4	199	36	31	5	163	67.92%	81.91%	84.03%	
#241		26	0	0	26	4	7	0	15	5	4	1	10	55.56%	66.67%	71.43%	
#242		95	0	0	95	4	7	0	84	1	1	0	83	94.32%	98.81%	98.81%	
#243		10	0	0	10	1	2	0	7	1	0	1	6	85.71%	85.71%	100.00%	
#244		448	0	0	448	15	37	1	385	31	25	6	364	85.45%	92.15%	93.51%	
#245		3028	0	0	3028	518	230	7	2273	297	224	73	1976	72.70%	86.93%	89.82%	
#246		27110	0	0	27110	3811	2479	63	20757	1618	1364	254	19139	78.72%	92.21%	93.35%	
#247		50	0	0	50	32	7	0	11	1	0	1	10	23.81%	90.91%	100.00%	
#248		548	0	0	548	54	62	4	428	44	34	10	384	81.36%	89.72%	91.87%	
#249		387	0	0	387	44	22	2	319	61	37	24	258	76.11%	80.88%	87.46%	
#250		160	0	0	160	15	14	0	131	2	2	0	129	88.36%	98.47%	98.47%	
#251		60	0	0	60	0	3	0	57	1	1	0	56	98.25%	98.25%	98.25%	
#252		272	0	0	272	9	26	0	237	12	9	3	225	92.59%	94.94%	96.15%	
#253		122	0	0	122	3	7	0	112	6	5	1	106	92.98%	94.64%	95.50%	
#254		167	0	0	167	17	11	3	136	19	18	1	117	76.97%	86.03%	86.67%	
#255		89	0	0	89	5	11	0	73	4	3	1	69	89.61%	94.52%	95.83%	
#256		30	0	0	30	3	1	1	25	3	2	1	22	81.48%	88.00%	91.67%	
#257		0	2	0	2	0	2	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#258		2194	0	0	2194	113	169	6	1906	148	135	13	1758	87.64%	92.24%	92.87%	
#259		400	0	0	400	50	17	1	332	29	21	8	303	81.02%	91.27%	93.52%	
#260		18	0	0	18	1	8	1	8	3	3	0	5	55.56%	62.50%	62.50%	
#261		693	0	0	693	38	42	1	612	25	17	8	587	91.43%	95.92%	97.19%	
#262		1031	0	0	1031	131	134	8	758	179	166	13	579	66.10%	76.39%	77.72%	
#263		56	0	0	56	5	7	0	44	8	8	0	36	73.47%	81.82%	81.82%	
#264		0	12	0	12	8	1	0	3	1	1	0	2	18.18%	66.67%	66.67%	
#265		145	0	0	145	12	9	0	124	21	19	2	103	76.87%	83.06%	84.43%	
#266		5	0	0	5	0	3	0	2	2	2	0	0	0.00%	0.00%	0.00%	
#267		367	0	0	367	27	26	0	314	19	17	2	295	87.02%	93.95%	94.53%	
#268		49	0	0	49	11	7	2	29	11	10	1	18	46.15%	62.07%	64.29%	
#269		3281	0	0	3261	322	254	9	2676	159	110	49	2517	85.35%	94.06%	95.81%	
#270		42	0	0	42	2	12	6	22	10	8	2	12	54.55%	54.55%	60.00%	
#271		1	0	0	1	0	0	0	1	0	0	1	1	100.00%	100.00%	100.00%	
#272		1395	0	0	1395	125	91	8	1171	109	80	29	1062	83.82%	90.69%	92.98%	

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (RESIDENCE DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH					
		Mechanized Interface Used					Validated					Errors					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLIC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation	CLIC Error Excluded Calculation	
#273		36	0	0	36	1	5	0	30	8	5	3	22	78.57%	73.33%	81.48%	
#274		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%	
#275		1221	0	0	1221	173	48	7	993	173	154	19	820	71.49%	82.58%	84.19%	
LENS Subtotal	166308	0	0	166308	21917	14141	489	129761	12701	9975	2726	117060	78.59%	90.21%	92.15%		
EDI Subtotal	0	33014	0	33014	3925	5964	15	23110	2870	1814	1056	20240	77.91%	87.58%	91.77%		
TAG Subtotal	0	0	28697	28697	1368	2661	134	24534	2350	1687	663	22184	87.90%	90.42%	92.93%		
TOTAL INTERFACES	166308	33014	28697	223019	27210	22766	638	177405	17921	13476	4445	16944	79.67%	89.90%	92.21%		

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (BUSINESS DETAIL)

REPORT PERIOD: 06/01/2001 - 06/30/2001

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH									
		Mechanized Interface Used					LESOG					Errors					CLEC				
Name	RESH / OCN	Manual	Total	Rejects	Validated	Pending	Auto	Supps	(Z Status)	LSR's	Total	System	BST Caused	CLEC	Achieved	Flowthrough	Base	Calculation	CLEC Error Excluded Calculation		
		Total	Total	Total	Total	Total	Total	Total	Total	Total	Total	Total	Total	Total	Total	Total	Total	Total	Total	Total	
#1		1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	100.00%	100.00%	100.00%	
#2		1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	100.00%	100.00%	100.00%	
#3		1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	100.00%	100.00%	100.00%	
#4		19	0	0	19	0	6	0	0	13	14	10	1	2	2	16.67%	15.38%	16.67%	15.38%	16.67%	
#5		0	0	2	2	0	0	0	0	2	1	1	0	0	0	1	0	50.00%	50.00%	50.00%	
#6		5	0	0	5	0	1	1	1	3	2	2	0	0	0	1	1	33.33%	33.33%	33.33%	
#7		0	0	10	10	2	0	0	8	3	1	2	2	5	5	62.50%	62.50%	62.50%	62.50%	62.50%	
#8		1	0	0	1	0	0	0	1	0	0	0	0	0	0	1	100.00%	100.00%	100.00%	100.00%	100.00%
#9		76	0	0	76	5	15	2	54	31	28	3	23	3	23	41.07%	42.59%	45.10%	42.59%	45.10%	
#10		0	0	4	4	0	3	0	1	0	0	0	0	0	0	1	100.00%	100.00%	100.00%	100.00%	100.00%
#11		0	0	10	10	0	1	0	9	6	4	2	2	3	3	42.86%	33.33%	42.86%	33.33%	42.86%	
#12		0	11	0	11	0	5	0	6	3	3	0	3	0	3	50.00%	50.00%	50.00%	50.00%	50.00%	
#13		0	0	2	2	0	0	0	0	2	1	1	0	1	1	50.00%	50.00%	50.00%	50.00%	50.00%	
#14		0	0	3	3	0	0	0	3	1	1	1	0	0	2	66.67%	66.67%	66.67%	66.67%	66.67%	
#15		6	0	6	1	0	0	0	5	1	1	0	0	4	4	66.67%	66.67%	66.67%	66.67%	66.67%	
#16		5	0	0	5	2	0	0	3	2	2	0	1	1	4	66.67%	80.00%	80.00%	80.00%	80.00%	
#17		2	0	0	2	0	0	1	0	1	0	0	0	1	1	20.00%	33.33%	33.33%	33.33%	33.33%	
#18		12	0	0	12	1	6	0	5	1	1	1	1	0	2	66.67%	66.67%	66.67%	66.67%	66.67%	
#19		57	0	0	57	8	14	0	36	14	8	6	6	6	21	56.78%	60.00%	72.41%	60.00%	72.41%	
#20		1	0	0	1	0	0	0	1	0	0	0	0	0	1	1	100.00%	100.00%	100.00%	100.00%	100.00%
#21		1	0	0	1	0	0	0	1	0	0	0	0	0	1	1	100.00%	100.00%	100.00%	100.00%	100.00%
#22		0	0	15	15	11	2	0	2	0	1	0	0	1	1	1	8.33%	50.00%	50.00%	50.00%	50.00%
#23		34	0	0	34	3	12	1	18	5	3	2	13	13	13	68.42%	72.22%	81.25%	72.22%	81.25%	
#24		2	0	0	2	0	1	0	1	0	1	0	0	1	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#25		24	0	0	24	3	0	5	16	13	4	9	3	3	3	30.00%	18.75%	42.86%	18.75%	42.86%	
#26		4	0	4	0	4	0	3	0	1	0	0	0	1	1	100.00%	100.00%	100.00%	100.00%	100.00%	
#27		0	0	2	2	1	0	0	0	1	0	0	0	0	1	1	50.00%	100.00%	100.00%	100.00%	100.00%
#28		28	0	0	28	4	5	0	19	7	4	3	12	12	12	60.00%	63.16%	75.00%	63.16%	75.00%	
#29		10	0	0	10	3	1	0	6	2	2	0	4	4	4	44.44%	66.67%	66.67%	66.67%	66.67%	
#30		2	0	0	2	0	0	0	0	2	0	0	0	2	2	100.00%	100.00%	100.00%	100.00%	100.00%	
#31		4	0	0	4	1	3	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#32		1	0	0	1	0	0	0	0	1	1	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#33		2	0	0	2	0	0	1	1	1	1	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#34		17	0	0	17	3	4	0	10	5	3	2	5	5	5	45.45%	50.00%	62.50%	50.00%	62.50%	

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (BUSINESS DETAIL)

REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3

Attachment 2A

AGGREGATE ORDER TYPES	Company Info	LSR PROCESSING										FLOWTHROUGH								
		Mechanized Interface Used				LESOG				Validated				Errors				Achieved Flowthrough		
		RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Failout	Auto Clarification	LSR's	Total Supps (Z Status)	System Failout	BST Caused Failout	CLEC Caused Failout	Issued SO's	Achieved Flowthrough	2	66.67%	66.67%	66.67%	
#35		6	0	0	0	6	0	2	1	3	1	1	1	0	0	2	66.67%	66.67%	66.67%	
#36		2	0	0	0	2	0	0	0	2	1	1	0	0	1	1	50.00%	50.00%	50.00%	
#37		37	0	0	0	37	5	8	4	20	10	8	2	10	10	10	43.48%	50.00%	55.55%	
#38		2	0	0	2	0	0	0	0	2	1	1	0	0	1	1	50.00%	50.00%	50.00%	
#39		13	0	0	13	1	6	0	6	1	1	0	0	5	5	71.43%	83.33%	83.33%		
#40		1	0	0	1	0	0	0	0	1	1	1	0	0	0	0	0.00%	0.00%	0.00%	
#41		5	0	0	5	0	0	0	0	5	1	1	0	0	4	4	80.00%	80.00%	80.00%	
#42		0	0	1	1	0	0	0	0	1	1	1	0	0	0	0	0.00%	0.00%	0.00%	
#43		9	0	0	9	0	2	0	7	3	2	1	1	4	4	66.67%	57.14%	66.67%		
#44		7	0	0	7	0	1	0	6	2	2	1	1	4	4	80.00%	66.67%	80.00%		
#45		2	0	0	2	0	0	0	2	0	0	0	0	0	2	2	100.00%	100.00%	100.00%	
#46		1	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	
#47		3	0	0	3	1	0	0	0	2	1	0	1	1	1	1	50.00%	50.00%	50.00%	
#48		4	0	0	4	1	0	0	0	3	0	0	0	0	0	3	75.00%	100.00%	100.00%	
#49		6	0	0	6	2	0	0	0	4	0	0	0	0	4	4	66.67%	100.00%	100.00%	
#50		7	0	0	7	1	0	1	5	2	0	0	0	2	3	75.00%	60.00%	60.00%		
#51		27	0	0	27	16	2	0	9	7	6	1	1	2	3	75.00%	22.22%	25.00%		
#52		18	0	0	18	1	3	0	14	3	3	0	11	11	11	11	73.33%	78.57%	78.57%	
#53		10	0	0	10	3	1	0	6	3	3	0	3	3	3	3	33.33%	50.00%	50.00%	
#54		0	0	1	1	0	0	0	0	1	0	0	0	0	1	1	100.00%	100.00%	100.00%	
#55		35	0	0	35	0	4	1	30	12	7	5	18	18	18	18	72.00%	60.00%	72.00%	
#56		1	0	0	1	0	0	0	0	1	1	1	0	0	0	0	0.00%	0.00%	0.00%	
#57		6	0	0	6	0	0	0	0	6	2	0	2	4	4	4	50.00%	57.14%	57.14%	
#58		12	0	0	12	0	0	0	0	12	4	4	0	0	8	8	66.67%	81.83%	81.83%	
#59		23	0	0	23	3	3	0	17	9	8	1	8	8	8	8	42.11%	47.06%	50.00%	
#60		9	0	0	9	1	1	0	7	3	3	0	4	4	4	4	0.00%	0.00%	0.00%	
#61		14	0	0	14	2	1	0	11	2	0	0	2	9	9	9	50.00%	81.83%	100.00%	
#62		70	0	0	70	18	1	4	47	25	22	3	22	22	22	22	35.48%	46.81%	50.00%	
#63		4	0	0	4	1	1	0	0	2	2	2	0	0	0	0	0.00%	0.00%	0.00%	
#64		3	0	0	3	1	0	0	0	2	0	0	0	2	2	2	66.67%	100.00%	100.00%	
#65		44	0	0	44	13	13	8	2	21	12	0	9	9	9	9	26.41%	42.86%	42.86%	
#66		21	0	0	21	5	6	1	9	4	4	0	5	5	5	5	35.71%	55.56%	55.56%	
#67		45	0	0	45	13	3	0	29	16	13	3	13	13	13	13	33.33%	44.83%	50.00%	
#68		8	0	0	8	4	4	0	0	4	1	1	0	3	3	3	37.50%	75.00%	75.00%	

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ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (BUSINESS DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH										
		Mechanized Interface Used			LESOG			Validated				Errors		CLEC		Base Calculation		CLEC Error Excluded Calculation				
Name	Company Info	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total Mech LSR's	Manual Clarification	Auto Clarification	Rejects	Pending Supps	System Fallout	BST Caused Fallout	CLEC Caused Fallout	Achieved Flowthrough	Issued SO's	Flowthrough	0.00%	75.00%	67.92%	0.00%	0.00%
#69		0	0	11	11	10	0	0	0	0	48	12	11	1	1	0	0	0.00%	75.00%	67.92%	0.00%	0.00%
#70		62	0	0	62	6	8	0	0	16	6	5	1	10	1	36	62.50%	62.50%	66.67%	66.67%	66.67%	
#71		20	0	0	20	0	4	0	4	1	25	11	11	0	14	1	10	56.00%	56.00%	56.00%	56.00%	56.00%
#72		32	0	0	32	2	4	1	4	0	26	6	5	1	20	2	20	74.07%	76.92%	80.00%	80.00%	80.00%
#73		28	0	0	28	2	0	0	0	0	0	0	0	0	0	1	1	100.00%	100.00%	100.00%	100.00%	100.00%
#74		2	0	0	2	0	1	0	1	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#75		4	0	0	4	0	2	1	1	1	1	1	1	0	0	0	0	0.00%	0.00%	63.64%	63.64%	63.64%
#76		25	0	0	25	11	3	0	0	11	4	4	0	0	7	7	31.82%	63.64%	55.56%	60.00%	60.00%	
#77		36	0	0	36	8	1	0	27	12	10	2	15	15	15	15	45.45%	45.45%	45.45%	45.45%	45.45%	
#78		33	0	0	33	7	1	0	25	22	19	3	3	3	3	3	10.34%	12.00%	13.64%	13.64%	13.64%	
#79		0	0	1	1	0	0	0	0	1	0	0	0	0	1	1	1	100.00%	100.00%	100.00%	100.00%	100.00%
#80		1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	1	100.00%	100.00%	100.00%	100.00%	100.00%
#81		9	0	0	9	2	3	1	3	2	2	2	0	0	1	1	1	20.00%	33.33%	33.33%	33.33%	33.33%
#82		0	0	22	22	2	6	0	14	10	9	1	4	4	4	4	26.67%	28.57%	30.77%	30.77%	30.77%	
#83		0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#84		18	0	0	18	7	5	0	6	6	2	2	0	4	4	4	4	30.77%	66.67%	66.67%	66.67%	66.67%
#85		24	0	0	24	13	4	1	6	5	5	5	0	1	1	1	5.26%	16.67%	16.67%	16.67%	16.67%	
#86		14	0	0	14	2	2	0	10	2	1	1	1	1	8	8	72.73%	80.00%	88.89%	88.89%	88.89%	
#87		43	0	0	43	5	7	0	31	7	7	0	0	0	24	24	66.67%	77.42%	77.42%	77.42%	77.42%	
#88		215	0	0	215	21	32	7	156	71	52	19	19	19	84	84	53.50%	54.19%	61.76%	61.76%	61.76%	
#89		38	0	0	38	4	10	0	24	14	11	3	3	3	10	10	40.00%	41.67%	47.62%	47.62%	47.62%	
#90		155	0	0	155	18	40	2	95	57	48	9	9	9	38	38	36.54%	40.00%	44.19%	44.19%	44.19%	
#91		4	0	0	4	0	2	0	2	0	1	1	0	1	1	1	50.00%	50.00%	50.00%	50.00%	50.00%	
#92		1	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	
#93		1499	0	0	1499	226	273	17	983	522	393	129	129	129	461	461	42.66%	46.30%	53.98%	53.98%	53.98%	
#94		0	0	32	32	3	17	0	12	9	7	2	2	2	3	3	23.08%	25.00%	30.00%	30.00%	30.00%	
#95		117	0	0	117	22	19	0	76	37	33	4	39	39	41.48%	51.32%	54.17%	71.43%	83.33%	83.33%	83.33%	
#96		0	0	5	5	1	1	0	3	0	0	0	0	0	3	3	75.00%	100.00%	100.00%	100.00%	100.00%	
#97		8	0	0	8	4	0	0	0	4	2	2	2	2	2	2	2	25.00%	50.00%	50.00%	50.00%	50.00%
#98		11	0	0	11	0	2	2	2	7	2	1	1	1	5	5	83.33%	71.43%	83.33%	83.33%	83.33%	
#99		61	0	0	61	20	5	2	34	14	12	2	2	2	20	20	38.46%	58.82%	62.50%	62.50%	62.50%	
#100		6	0	0	6	1	0	0	5	1	1	0	0	0	4	4	66.67%	80.00%	80.00%	80.00%	80.00%	
#101		0	0	7	7	0	3	0	4	1	1	0	0	0	3	3	75.00%	75.00%	75.00%	75.00%	75.00%	
#102		34	0	0	34	8	2	0	24	6	4	2	2	2	18	18	60.00%	75.00%	81.82%	81.82%	81.82%	

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (BUSINESS DETAIL)
 REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
 Attachment 2A

AGGREGATE ORDER TYPES	Company Info	LSR PROCESSING										FLOWTHROUGH						
		Mechanized Interface Used					LESOG					CLEC				Base Calculation		
		RESH / OCN	LENS	EDI	TAG	Total LSR's	Total Mech LSR's	Manual Fallout	Manual Z Status	Auto Clarification	Pending Supps	LSR's	System Fallout	BST Caused Fallout	CLEC Caused Fallout	Achieved Flowthrough	Issued SO's	CLEC Error Excluded Calculation
#103		0	0	51	51	25	6	0	20	35	11	11	0	3	7	16.67%	35.00%	41.18%
#104		41	0	0	41	4	2	0	0	3	1	1	0	2	24	61.54%	68.57%	68.57%
#105		6	0	0	6	0	3	0	0	3	0	0	0	0	2	66.67%	68.67%	66.67%
#106		2	0	0	2	1	1	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#107		3	0	0	3	0	0	0	0	3	1	1	0	2	2	66.67%	66.67%	66.67%
#108		1	0	0	1	1	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#109		4	0	0	4	0	0	0	0	4	0	0	0	4	4	100.00%	100.00%	100.00%
#110		1	0	0	1	0	1	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#111		13	0	0	13	3	0	0	1	9	5	3	2	4	4	40.00%	44.44%	57.14%
#112		26	0	0	26	2	3	0	21	14	7	7	7	7	7	43.75%	33.33%	50.00%
#113		0	14	0	14	11	0	0	3	3	1	2	0	0	0	0.00%	0.00%	0.00%
#114		52	0	0	52	30	5	1	16	6	3	3	3	10	10	23.26%	62.50%	76.92%
#115		0	0	136	136	75	25	3	33	23	19	4	10	10	10	9.62%	30.30%	34.48%
#116		384	0	0	384	93	23	10	258	129	102	27	27	129	129	39.81%	50.00%	55.84%
#117		1	0	0	1	0	0	0	1	1	1	0	0	0	0	0.00%	0.00%	0.00%
#118		6	0	0	6	4	1	0	1	1	1	0	0	0	0	0.00%	0.00%	0.00%
#119		2	0	0	2	2	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#120		2	0	0	2	2	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#121		49	0	0	49	4	8	1	36	19	14	5	17	17	17	48.57%	47.22%	54.84%
#122		58	0	0	58	12	3	0	43	14	12	2	2	29	29	54.72%	67.44%	70.73%
#123		577	0	0	577	63	64	8	442	277	127	150	165	165	165	46.48%	37.33%	56.51%
#124		7	0	0	7	1	0	0	6	4	4	0	0	2	2	28.57%	33.33%	33.33%
#125		4	0	0	4	0	1	0	3	1	1	0	0	2	2	66.67%	66.67%	66.67%
#126		2	0	0	2	0	0	0	0	2	0	0	0	2	2	100.00%	100.00%	100.00%
#127		1	0	0	1	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#128		0	11	11	2	4	0	5	1	0	0	1	4	4	4	66.67%	80.00%	100.00%
#129		17	0	0	17	5	0	0	12	6	2	4	6	6	6	46.15%	50.00%	75.00%
#130		15	0	0	15	8	0	0	7	6	5	1	1	1	1	7.14%	14.29%	16.67%
#131		3	0	0	3	0	1	0	2	0	0	0	0	2	2	100.00%	100.00%	100.00%
#132		10	0	0	10	5	0	1	4	2	0	2	2	2	2	28.57%	50.00%	100.00%
#133		571	0	0	571	117	45	5	404	187	151	36	217	217	217	44.74%	53.71%	58.97%
#134		2	0	0	2	0	0	0	0	0	0	0	2	2	2	100.00%	100.00%	100.00%
#135		0	0	3	3	0	0	0	0	0	0	0	3	3	3	100.00%	100.00%	100.00%
#136		61	0	0	61	20	5	0	36	12	10	2	24	24	24	44.44%	66.67%	70.59%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (BUSINESS DETAIL)
 REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
 Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH				
		Mechanized Interface Used					LESOG					CLEC			Base Calculation	
Name	RESH / OCN	LENS	EDI	TAG	Total LSR's	Mechanical Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	Caused Fallout	Achieved Flowthrough	Issued SO's	Flowthrough	CLEC Error Excluded Calculation
#137		0	504	0	504	238	88	6	172	59	41	18	113	28.83%	65.70%	73.38%
#138		78	0	0	78	19	8	0	51	16	13	3	35	52.24%	66.63%	72.92%
#139		0	0	33	33	10	10	0	13	2	2	0	11	47.83%	84.82%	84.62%
#140		57	0	0	57	16	11	1	29	13	8	5	16	40.00%	55.17%	66.67%
#141		367	0	0	367	29	36	8	294	102	89	13	192	61.94%	65.31%	68.33%
#142		106	0	0	106	15	5	2	84	35	24	11	49	55.88%	58.33%	67.12%
#143		22	0	0	22	5	9	1	7	6	5	1	1	9.09%	14.29%	16.67%
#144		861	0	0	861	202	103	10	546	254	201	53	292	42.01%	53.48%	59.23%
#145		13	0	0	13	7	1	0	5	5	5	0	0	0.00%	0.00%	0.00%
#146		2	0	0	2	0	0	0	2	0	0	0	0	100.00%	100.00%	100.00%
#147		104	0	0	104	30	10	2	62	37	29	8	25	29.76%	40.32%	46.30%
#148		18	0	0	18	2	2	0	14	9	9	0	5	31.25%	35.71%	35.71%
#149		15	0	0	15	6	1	0	8	5	4	1	3	23.08%	37.50%	42.86%
#150		22	0	0	22	2	8	3	9	8	5	3	1	12.50%	11.11%	16.67%
#151		5	0	0	5	3	0	0	2	0	0	0	2	40.00%	100.00%	100.00%
#152		99	0	0	99	22	11	0	66	28	24	4	38	45.24%	57.55%	61.29%
#153		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#154		0	0	7	7	4	0	0	3	1	1	0	2	28.57%	66.67%	66.67%
#155		8	0	0	8	0	2	2	4	3	2	1	1	33.33%	25.00%	33.33%
#156		1	0	0	1	0	0	0	1	0	0	0	1	100.00%	100.00%	100.00%
#157		3	0	0	3	1	1	0	1	0	0	0	1	50.00%	100.00%	100.00%
#158		38	0	0	38	7	0	0	31	6	6	0	25	65.79%	80.65%	80.65%
#159		0	0	92	92	15	30	5	42	40	35	5	2	3.85%	4.76%	5.41%
#160		871	0	0	871	262	113	9	487	239	213	26	248	34.30%	50.92%	53.80%
#161		53	0	0	53	6	3	1	43	16	13	3	27	58.70%	62.79%	67.50%
#162		43	0	0	43	6	5	1	31	9	7	2	22	62.88%	70.91%	75.86%
#163		40	0	0	40	5	11	0	24	9	8	1	15	53.57%	62.50%	65.22%
#164		94	0	0	94	2	8	0	84	28	23	5	56	69.14%	66.67%	70.89%
#165		13	0	0	13	2	0	0	11	4	3	1	7	58.33%	63.64%	70.00%
#166		29	0	0	29	18	2	0	9	4	2	2	5	20.00%	55.56%	71.43%
#167		1	0	0	1	1	0	0	0	0	0	0	0	0.00%	0.00%	0.00%
#168		167	0	0	167	0	0	0	187	39	10	2	35	48.61%	60.34%	66.67%
#169		0	2	0	2	0	0	0	0	2	2	1	0	0.00%	0.00%	0.00%
#170		474	0	0	474	69	55	13	337	148	120	28	189	50.00%	56.08%	61.17%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (BUSINESS DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES	Company Info	LSR PROCESSING										FLOWTHROUGH									
		LESOG					Errors					CLEC					Base Calculation				
		Mechanized Interface Used		Manual		Rejects	Validated		Pending Supps (Z Status)		LSR's	System Fallout		BST Caused Fallout	Caused Fallout		Achieved Flowthrough	Issued SO's			
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total	Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Achieved Flowthrough	Issued SO's	Achieved Flowthrough	Base Calculation	Base Calculation	Base Calculation	Base Calculation	Base Calculation
#171	0	1	0	1	0	0	0	0	0	1	23	18	14	1	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#172	58	0	0	58	0	58	23	11	1	1	23	5	1	4	19	5	11.90%	21.14%	26.32%	26.32%	26.32%
#173	35	0	0	35	0	35	3	8	0	0	24	5	1	4	19	82.61%	79.17%	95.00%	95.00%	95.00%	
#174	8	0	0	8	0	8	5	0	0	0	3	2	1	1	1	1	14.29%	33.33%	50.00%	50.00%	50.00%
#175	5	0	0	5	0	5	0	0	0	0	5	4	1	3	1	1	50.00%	20.00%	50.00%	50.00%	50.00%
#176	0	0	0	6	6	6	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#177	1	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%
#178	67	0	0	67	17	5	2	43	19	19	19	0	0	0	24	40.00%	55.81%	55.81%	55.81%	55.81%	
#179	0	140	0	140	44	19	3	74	42	42	37	5	32	32	32	28.32%	43.24%	46.38%	46.38%	46.38%	
#180	1139	0	0	1139	204	125	21	789	459	459	387	72	330	330	330	35.83%	41.83%	46.03%	46.03%	46.03%	
#181	44	0	0	44	4	18	2	20	18	18	18	0	2	2	2	8.33%	10.00%	10.00%	10.00%	10.00%	
#182	35	0	0	35	2	5	0	28	14	14	13	1	14	14	14	48.28%	50.00%	50.00%	50.00%	51.85%	
#183	20	0	0	20	3	0	0	17	5	2	3	2	3	12	12	70.58%	70.58%	70.58%	70.58%	85.71%	
#184	184	0	0	184	145	10	1	28	19	19	13	6	9	9	9	5.39%	32.14%	40.91%	40.91%	40.91%	
#185	6	0	0	6	3	0	0	3	1	1	1	0	0	2	2	33.33%	66.67%	66.67%	66.67%	66.67%	
#186	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	
#187	6	0	0	6	1	0	0	0	5	1	0	1	4	4	4	80.00%	80.00%	100.00%	100.00%	100.00%	
#188	2	0	0	2	0	0	0	0	2	2	1	1	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	
#189	12	0	0	12	1	1	0	10	4	4	4	0	6	6	6	54.55%	60.00%	60.00%	60.00%	60.00%	
#190	13	0	0	13	7	0	0	6	6	6	4	2	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	
#191	6	0	0	6	1	0	0	0	5	1	1	0	4	4	4	66.67%	80.00%	80.00%	80.00%	80.00%	
#192	11	0	0	11	8	0	0	3	3	3	3	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	
LENS Subtotal	10449	0	0	10449	2097	1323	166	6863	3354	2604	750	3509	42.74%	51.13%	57.40%	42.74%	51.13%	57.40%	51.13%	57.40%	
EDI Subtotal	0	672	0	672	293	112	9	258	110	83	27	148	28.24%	57.35%	64.07%	28.24%	57.35%	64.07%	28.24%	57.35%	
TAG Subtotal	0	0	469	469	115	8	184	115	94	21	69	21	21	21.23%	37.50%	42.33%	21.23%	37.50%	42.33%	21.23%	
TOTAL INTERFACES	10449	672	469	11590	2552	1550	183	7305	3579	2781	798	3726	3726	41.13%	51.01%	57.26%	41.13%	51.01%	57.26%	41.13%	51.01%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (UNE DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH					
		LESOG					Errors					CLEC					CLEC Error Excluded Calculation
Name	Company Info	Mechanized Interface Used					Validated		Pending		Total System		BST Caused		Achieved		Base Calculation
		RESH / OCN	LENS	EDI	TAG	Total LSR's	Manual	Rejects	Auto Clarification	Sups (Z Status)	LSR's	System Fallout	BST Caused Fallout	Caused Fallout	Issued SO's	Achieved Flowthrough	
#1		0	107	0	107	31	28	0	48	22	6	16	26	41.27%	54.17%	81.25%	
#2		52	0	0	52	3	8	0	41	11	8	3	30	73.17%	73.17%	78.95%	
#3		0	3420	0	3420	569	662	0	2189	1955	151	1804	234	24.53%	10.69%	60.78%	
#4		967	0	0	967	69	82	16	800	218	106	112	582	76.88%	72.75%	84.59%	
#5		12	0	0	12	0	2	0	10	0	0	0	10	100.00%	100.00%	100.00%	
#6		0	19	0	19	5	2	0	12	6	6	0	6	35.29%	50.00%	50.00%	
#7		0	17	0	17	4	2	0	11	4	2	2	7	53.85%	63.64%	77.78%	
#8		30	0	0	30	2	3	0	7	18	11	10	1	36.84%	38.89%	41.18%	
#9		1339	0	0	1339	172	116	1	1050	137	116	21	913	76.02%	86.95%	88.73%	
#10		0	0	13	13	2	1	0	10	4	3	1	6	54.55%	60.00%	66.67%	
#11		2	0	0	2	0	0	0	2	0	0	0	2	100.00%	100.00%	100.00%	
#12		0	0	17	17	13	3	0	1	1	1	0	0	0.00%	0.00%	0.00%	
#13		2230	0	0	2230	288	278	36	1628	360	244	116	1268	70.44%	77.89%	83.86%	
#14		33	0	0	33	15	6	1	11	11	7	6	1	4	16.00%	36.36%	40.00%
#15		0	0	2	2	0	0	0	0	2	2	1	1	0	0.00%	0.00%	0.00%
#16		0	0	1467	1467	207	253	8	999	424	348	76	575	50.88%	57.56%	62.30%	
#17		0	0	896	896	130	130	1	635	256	219	37	379	52.06%	59.69%	63.38%	
#18		0	0	18	18	1	1	0	16	7	9	0	0	0.00%	0.00%	0.00%	
#19		0	0	23	23	4	4	2	13	6	2	4	7	53.85%	53.85%	77.78%	
#20		100	0	0	100	10	10	2	78	31	24	7	47	58.02%	60.26%	66.20%	
#21		0	0	1277	1277	281	245	11	740	339	238	101	401	43.59%	54.19%	62.75%	
#22		0	193	0	193	163	13	0	17	17	16	1	0	0.00%	0.00%	0.00%	
#23		914	0	0	914	499	114	2	299	110	80	30	189	24.61%	63.21%	70.26%	
#24		0	0	778	778	120	155	9	494	221	178	43	273	47.81%	55.26%	60.53%	
#25		0	0	551	551	112	104	11	324	135	114	21	189	45.54%	58.33%	62.38%	
#26		474	0	0	474	49	48	9	368	106	77	29	262	67.55%	71.20%	77.29%	
#27		0	198	0	198	25	16	8	149	69	53	16	80	50.63%	53.69%	60.15%	
#28		4	0	0	4	3	0	0	1	1	1	0	0	0.00%	0.00%	0.00%	
#29		0	0	32	32	10	2	0	20	4	4	0	16	53.33%	80.00%	80.00%	
#30		63	0	0	63	8	12	0	43	5	3	2	38	77.55%	88.37%	92.68%	
#31		688	0	0	688	173	48	6	461	143	120	23	318	52.05%	68.38%	72.60%	
#32		2016	0	0	2016	432	139	6	1439	386	314	72	1033	58.55%	73.18%	77.03%	
#33		48	0	0	48	0	0	1	47	46	1	0	0	0.00%	0.00%	0.00%	
#34		4	0	0	4	0	1	1	2	2	2	0	0	0.00%	0.00%	0.00%	

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (LINE DETAIL)
 REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
 Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH										
		LESOG					Validated					Errors					CLEC					
Name	RESH / OCN	Mechanized Interface Used		Total Mech LSR's	Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total		System Fallout		BST Caused Fallout		Caused Fallout		Achieved Flowthrough		Base Calculation		CLEC Error Excluded Calculation	
		LENS	EDI						Total	7	Total	7	Total	7	Achieved Flowthrough	0	0.00%	0.00%	0.00%	0.00%	0.00%	
#35	0	0	11	11	0	2	2	2	9	74	69	48	21	5	8.62%	6.76%	9.44%	9.44%	56.14%	56.14%	58.49%	
#36	93	0	0	93	5	5	5	9	399	175	159	16	224	47.16%	47.16%	81.93%	81.93%	84.87%	84.87%	84.87%		
#37	0	0	579	92	87	1	1	0	404	73	59	14	331	76.27%	76.27%	100.00%	100.00%	100.00%	100.00%	100.00%		
#38	486	0	0	486	44	34	4	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%		
#39	0	0	2	2	0	2	0	0	57	16	10	6	41	53.25%	53.25%	71.93%	71.93%	80.39%	80.39%	80.39%		
#40	98	0	0	98	26	15	0	0	2	1	1	0	1	20.00%	20.00%	50.00%	50.00%	50.00%	50.00%	50.00%		
#41	0	5	0	5	3	0	0	0	2	0	0	0	0	2	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	
#42	0	4	0	4	0	2	0	0	2	0	0	0	0	0	42.11%	42.11%	66.67%	66.67%	66.67%	66.67%	66.67%	
#43	0	0	33	33	7	12	2	2	12	4	4	0	0	8	18	18	69.23%	69.23%	72.00%	72.00%	72.00%	
#44	42	0	0	42	4	10	2	2	26	8	7	1	1	18	62.07%	62.07%	63.54%	63.54%	66.67%	66.67%	66.67%	
#45	66	0	0	66	12	8	2	2	44	16	14	2	2	28	51.85%	51.85%	72.55%	72.55%	74.00%	74.00%	74.00%	
#46	70	0	0	70	14	5	0	0	51	14	13	1	1	37	57.81%	57.81%	81.31%	81.31%	83.84%	83.84%	83.84%	
#47	0	0	13	13	2	6	0	0	5	3	3	0	0	2	28.57%	28.57%	40.00%	40.00%	40.00%	40.00%	40.00%	
#48	11	0	0	11	3	4	0	0	4	1	1	0	0	3	42.86%	42.86%	75.00%	75.00%	75.00%	75.00%	75.00%	
#49	0	0	105	105	11	6	0	0	88	34	28	6	54	58.06%	58.06%	61.36%	61.36%	65.85%	65.85%	66.67%		
#50	127	0	0	127	18	6	1	1	102	19	16	3	83	70.94%	70.94%	81.31%	81.31%	83.84%	83.84%	83.84%		
#51	0	0	2	2	1	0	0	0	1	1	0	1	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#52	31	0	0	31	4	1	1	1	25	12	1	1	11	13	72.22%	72.22%	52.00%	52.00%	52.00%	52.00%	52.00%	
#53	0	0	1	1	0	0	0	0	1	1	1	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#54	27	0	0	27	1	5	1	1	20	3	3	0	0	17	80.95%	80.95%	85.00%	85.00%	85.00%	85.00%	85.00%	
#55	0	0	2	2	0	0	0	0	0	2	2	2	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#56	35	0	0	35	4	2	0	0	29	7	4	3	22	73.33%	73.33%	75.86%	75.86%	84.62%	84.62%	84.62%		
#57	186	0	0	186	41	8	5	5	132	48	32	16	84	53.50%	53.50%	63.64%	63.64%	72.41%	72.41%	72.41%		
#58	0	0	240	240	35	26	1	1	178	93	66	27	85	45.70%	45.70%	47.75%	47.75%	56.29%	56.29%	56.29%		
#59	226	0	0	226	17	24	7	7	178	32	22	10	146	78.92%	78.92%	82.02%	82.02%	86.90%	86.90%	86.90%		
#60	9	0	9	0	0	0	0	0	9	3	2	1	6	75.00%	75.00%	66.67%	66.67%	75.00%	75.00%	75.00%		
#61	0	6	0	6	2	0	0	0	4	4	1	3	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%		
#62	3	0	0	3	2	0	0	0	1	1	0	1	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%		
#63	14	0	0	14	0	0	0	0	14	2	2	0	0	12	85.71%	85.71%	85.71%	85.71%	85.71%	85.71%	85.71%	
#64	0	24	0	24	11	0	2	1	11	10	1	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#65	4	0	0	4	0	0	1	0	3	3	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%		
#66	0	0	28	28	7	2	0	0	19	3	2	1	16	64.00%	64.00%	84.21%	84.21%	88.89%	88.89%	88.89%		
#67	35	0	0	35	1	2	0	0	32	3	2	1	29	90.63%	90.63%	90.33%	90.33%	93.55%	93.55%	93.55%		
#68	0	114	0	114	31	9	0	0	74	27	20	7	47	47.98%	47.98%	63.51%	63.51%	70.15%	70.15%	70.15%		

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (LINE DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH										
		Mechanized Interface Used					Validated					Errors					CLEC Error Excluded Calculation					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Failout	Auto Clarification	Total	Pending Supps (Z Status)	LSR's	Total System Fallout	BST Caused Fallout	CLEC Caused Fallout	Issued SO's	Achieved Flowthrough	Base Calculation						
#69	0	0	39	39	12	0	2	25	9	9	0	0	0	16	43.24%	64.00%	64.00%	66.67%	85.71%			
#70	46	0	0	46	7	3	0	36	12	4	4	8	24	68.57%	66.57%	66.57%	75.00%	75.00%	75.00%	75.00%		
#71	6	0	0	6	0	2	0	4	1	1	0	0	3	75.00%	75.00%	75.00%	0.00%	0.00%	0.00%	0.00%		
#72	36	0	0	36	13	9	0	14	14	14	0	0	0	0	0.00%	0.00%	0.00%	50.00%	50.00%	50.00%	50.00%	
#73	2	0	0	2	0	0	0	0	2	1	1	0	1	50.00%	50.00%	50.00%	81.25%	89.66%	89.66%	89.66%		
#74	0	51	0	51	12	7	0	32	6	3	3	3	26	63.41%	63.41%	63.41%	0.00%	0.00%	0.00%	0.00%		
#75	26	0	0	882	882	143	13	78	648	489	433	56	159	21.63%	21.63%	21.63%	60.00%	100.00%	100.00%	100.00%		
#76	0	25	0	25	0	6	3	16	16	0	0	6	9	56.25%	56.25%	56.25%	24.54%	24.54%	24.54%	24.54%		
#77	0	4	0	4	0	2	0	2	2	0	2	1	1	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#78	34	0	0	34	5	4	2	23	11	5	6	6	12	54.55%	54.55%	54.55%	52.17%	70.59%	70.59%	70.59%		
#79	1	0	0	1	0	1	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	
#80	96	0	0	96	18	10	0	68	17	10	7	7	51	64.56%	64.56%	64.56%	75.00%	83.61%	83.61%	83.61%		
#81	0	0	396	396	70	30	4	292	70	61	9	9	222	62.88%	62.88%	62.88%	76.03%	78.45%	78.45%	78.45%		
#82	928	0	0	928	138	68	9	713	119	98	21	21	594	71.57%	71.57%	71.57%	83.31%	85.84%	85.84%	85.84%		
#83	0	0	13	13	0	0	0	13	8	3	5	5	5	62.50%	62.50%	62.50%	38.48%	62.50%	62.50%	62.50%		
#84	418	0	0	418	201	28	8	181	87	66	21	21	94	26.04%	26.04%	26.04%	51.93%	58.75%	58.75%	58.75%		
#85	0	5	5	0	1	0	4	1	1	1	0	0	3	75.00%	75.00%	75.00%	75.00%	75.00%	75.00%	75.00%		
#86	205	0	0	205	41	22	5	137	46	37	9	9	91	53.88%	53.88%	53.88%	66.42%	71.09%	71.09%	71.09%		
#87	0	15	15	0	0	0	0	15	13	11	2	2	2	15.38%	15.38%	15.38%	13.33%	15.38%	15.38%	15.38%		
#88	0	9971	9971	5039	1314	94	3524	1541	1102	439	193	193	24.41%	24.41%	24.41%	56.27%	64.28%	64.28%	64.28%			
#89	0	8294	0	8294	437	744	38	7075	722	553	169	169	6353	86.55%	86.55%	86.55%	89.80%	91.99%	91.99%	91.99%		
#90	45	0	0	45	0	16	1	28	28	11	17	0	1	0.00%	0.00%	0.00%	100.00%	100.00%	100.00%	100.00%		
#91	115	0	0	115	51	7	0	57	17	15	2	2	40	37.74%	37.74%	37.74%	70.18%	72.73%	72.73%	72.73%		
#92	4	0	0	4	0	0	0	0	4	0	4	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%		
#93	15	0	0	15	0	7	0	8	8	2	6	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%		
#94	1	0	0	1	0	0	0	1	0	0	0	0	1	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%		
#95	115	0	0	115	51	7	0	57	17	15	2	2	40	37.74%	37.74%	37.74%	70.18%	72.73%	72.73%	72.73%		
#96	0	3	3	0	3	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	50.00%	50.00%	50.00%	50.00%		
#97	1	0	0	1	0	0	0	0	1	1	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%		
#98	2	0	0	2	2	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%		
#99	4	0	0	4	0	0	0	0	4	0	2	0	2	50.00%	50.00%	50.00%	30.77%	66.67%	66.67%	66.67%		
#100	0	0	23	23	7	8	0	8	4	2	2	4	2	4	66.24%	66.24%	66.24%	80.00%	81.89%	81.89%	100.00%	
#101	195	0	0	195	30	34	1	130	26	23	3	104	104	104	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	
#102	2	0	0	2	0	0	0	0	2	0	0	0	0	2	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	100.00%	

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REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (LINE DETAIL)
 REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
 Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH														
		LSOG					Validated					Errors					CLEC									
Name	Company Info	Mechanized Interface Used			Manual		Rejects		Pending		Supps (Z Status)		LSR's		Total System Fallout		BST Caused Fallout		Caused Fallout		Achieved Flowthrough		Base Calculation		CLEC Error Excluded Calculation	
		RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Total System Fallout	Total System Fallout	Total BST Caused Fallout	Total BST Caused Fallout	Total Caused Fallout	Total Caused Fallout	Total Achieved Flowthrough	Total Achieved Flowthrough	Total Base Calculation	Total Base Calculation	Total CLEC Error Excluded Calculation	Total CLEC Error Excluded Calculation								
#103		0	0	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	2	100.00%	100.00%	100.00%	100.00%		
#104		30	0	0	30	0	0	2	0	0	28	0	0	0	23	12	11	5	5	29.41%	17.85%	29.41%	29.41%			
#105		0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%			
#106		4	0	0	4	0	0	0	0	0	4	1	0	1	1	1	3	3	3	100.00%	75.00%	100.00%	100.00%			
#107		0	0	2	2	0	1	0	1	0	1	1	0	1	1	0	0	0	0	0.00%	0.00%	0.00%	0.00%			
#108		5	0	0	5	0	0	0	0	0	5	3	2	1	1	2	1	2	2	50.00%	40.00%	50.00%	50.00%			
#109		135	0	0	135	35	16	2	82	44	35	9	9	38	35	35	35	35	35	35.19%	46.34%	52.05%	52.05%			
#110		4	0	0	4	2	0	0	0	0	2	2	1	1	1	0	0	0	0	0.00%	0.00%	0.00%	0.00%			
#111		7	0	0	7	0	1	0	0	6	0	0	0	0	0	0	0	0	6	100.00%	100.00%	100.00%	100.00%			
#112		0	0	977	977	123	57	15	782	81	42	39	39	701	81	42	39	39	701	80.98%	89.64%	94.35%	94.35%			
#113		401	0	0	401	15	14	0	372	7	6	1	1	365	7	6	1	1	365	94.56%	98.12%	98.38%	98.38%			
#114		65	0	0	65	2	9	3	51	16	12	4	4	35	16	12	4	4	35	71.43%	68.53%	74.47%	74.47%			
#115		13	0	0	13	1	2	0	10	4	2	2	2	6	4	2	2	2	6	66.67%	60.00%	75.00%	75.00%			
#116		0	61	0	61	26	11	0	24	4	4	4	4	0	20	4	4	4	20	40.00%	83.33%	83.33%	83.33%			
#117		12	0	0	12	7	4	0	1	1	0	1	0	1	0	1	0	1	0	0.00%	0.00%	0.00%	0.00%			
#118		11	0	0	11	0	2	0	9	8	2	6	6	1	1	6	1	1	1	33.33%	11.11%	33.33%	33.33%			
#119		45	0	0	45	0	0	0	2	43	43	27	16	0	0	16	0	0	0	0.00%	0.00%	0.00%	0.00%			
#120		64	0	0	64	34	8	0	22	15	11	4	4	7	15	11	4	4	7	13.46%	31.82%	38.89%	38.89%			
#121		40	0	0	40	18	8	0	14	14	11	3	3	0	0	11	3	0	0	0.00%	0.00%	0.00%	0.00%			
#122		45	0	0	45	15	19	0	11	11	7	4	0	0	0	7	4	0	0	0.00%	0.00%	0.00%	0.00%			
#123		0	0	23	23	2	2	0	19	3	2	1	1	16	2	1	1	1	16	80.00%	84.21%	88.89%	88.89%			
#124		10	0	0	10	1	4	0	5	5	5	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%			
#125		0	520	0	384	56	0	80	48	30	30	18	18	32	30	30	18	18	32	7.17%	40.00%	51.61%	51.61%			
#126		12	0	0	12	0	6	0	6	5	3	2	1	1	1	0	1	0	1	25.00%	16.67%	25.00%	25.00%			
#127		0	0	7	7	7	0	0	0	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%			
#128		2	0	0	2	0	0	1	1	0	0	0	0	0	0	1	0	1	1	100.00%	100.00%	100.00%	100.00%			
#129		2	0	0	2	0	0	0	2	2	2	1	1	0	0	1	0	0	0	0.00%	0.00%	0.00%	0.00%			
#130		0	18665	0	18665	3331	2245	7	13082	1959	1517	442	442	11123	1959	1517	442	442	11123	69.64%	85.03%	88.00%	88.00%			
#131		10	0	0	10	2	4	0	4	3	0	3	0	3	0	3	0	3	1	33.33%	25.00%	100.00%	100.00%			
#132		0	399	0	399	249	121	2	27	7	20	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%			
#133		0	607	0	607	398	91	4	114	44	31	13	13	70	13	44	44	31	13	14.03%	61.40%	69.31%	69.31%			
#134		65	0	0	65	0	65	0	5	3	57	21	12	9	36	57	21	12	9	75.00%	63.16%	75.00%	75.00%			
#135		0	0	4	4	4	0	0	0	1	3	0	0	3	0	3	0	3	100.00%	100.00%	100.00%	100.00%				
#136		67	0	0	67	23	4	3	37	16	10	6	21	21	38.89%	38.89%	38.89%	38.89%	38.89%	56.76%	67.74%	67.74%	67.74%			

8/29/2001

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ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (UNE DETAIL)
 REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
 Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH										
		Mechanized Interface Used					LESOG					Errors					CLEC Error Excluded Calculation					
Name	RESH / OCN	LENS	EDI	TAG	Total Mech LSR's	Manual Fallout	Auto Clarification	Pending Supps (Z Status)	LSR's	Total	System Fallout	BST Caused Fallout	CLEC Caused Fallout	Achieved SO's	Flowthrough	Base Calculation						
#137	0	0	13	0	4	2	0	0	0	9	2	0	2	7	100.0%	77.78%	100.00%	0.00%	0.00%	0.00%	0.00%	
#138	0	0	4	4	2	2	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#139	104	0	0	104	55	41	0	8	8	8	8	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#140	8	0	0	8	3	0	1	4	4	4	4	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#141	21	0	0	21	0	4	4	13	13	11	11	2	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#142	11	0	0	11	0	8	0	3	3	2	1	1	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#143	0	0	14	14	0	0	0	14	4	4	1	3	3	10	10	90.9%	71.43%	90.91%	90.91%	90.91%	90.91%	90.91%
#144	0	0	36	36	0	5	1	30	11	8	3	19	3	19	19	70.3%	63.33%	63.33%	63.33%	63.33%	63.33%	63.33%
#145	5	0	0	5	1	0	2	2	2	1	1	0	1	1	1	33.33%	50.00%	50.00%	50.00%	50.00%	50.00%	50.00%
#146	2	0	0	2	2	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#147	0	0	8	0	1	0	7	4	4	4	4	0	0	3	3	42.86%	42.86%	42.86%	42.86%	42.86%	42.86%	42.86%
#148	186	0	0	186	48	107	1	30	30	26	26	4	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#149	2	0	0	2	0	1	0	1	1	1	1	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#150	0	0	54	54	15	4	1	34	26	11	15	15	8	8	8	23.53%	23.53%	23.53%	23.53%	23.53%	23.53%	23.53%
#151	5	0	0	5	2	2	0	0	1	0	0	0	0	1	1	33.33%	100.0%	100.0%	100.0%	100.0%	100.0%	100.0%
#152	0	0	2	2	0	1	0	1	0	0	0	0	0	1	1	100.0%	100.0%	100.0%	100.0%	100.0%	100.0%	100.0%
#153	0	1536	0	1536	816	139	4	577	140	73	67	67	437	437	437	32.98%	75.74%	85.69%	85.69%	85.69%	85.69%	85.69%
#154	141	0	0	141	56	12	1	72	37	31	6	6	35	35	35	28.63%	48.61%	53.03%	53.03%	53.03%	53.03%	53.03%
#155	2	0	2	1	1	0	0	0	0	0	0	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#156	4	0	4	0	4	0	0	2	2	0	0	0	0	2	2	100.0%	100.0%	100.0%	100.0%	100.0%	100.0%	100.0%
#157	0	417	0	417	210	74	6	127	80	58	22	22	47	47	47	14.92%	37.01%	44.76%	44.76%	44.76%	44.76%	44.76%
#158	592	0	0	592	94	28	4	466	114	95	19	19	352	352	352	65.06%	75.54%	78.75%	78.75%	78.75%	78.75%	78.75%
#159	75	0	0	75	4	49	2	20	20	18	2	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#160	40	0	0	40	4	17	1	18	18	11	7	0	0	0	0	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%	0.00%
#161	0	169	0	169	115	11	3	40	21	18	3	19	12.50%	12.50%	12.50%	47.50%	51.35%	51.35%	51.35%	51.35%	51.35%	51.35%
#162	12	0	0	12	0	2	2	8	4	2	2	2	4	4	4	66.67%	50.90%	66.67%	66.67%	66.67%	66.67%	66.67%
#163	2417	0	0	2417	487	266	9	1655	688	566	122	122	967	967	967	47.87%	58.43%	63.08%	63.08%	63.08%	63.08%	63.08%
#164	133	0	0	133	1	23	4	105	39	12	27	27	66	66	66	83.54%	62.86%	84.62%	84.62%	84.62%	84.62%	84.62%
#165	0	0	94	94	47	19	1	27	9	6	3	3	18	18	18	25.35%	66.67%	75.00%	75.00%	75.00%	75.00%	75.00%
#166	8189	0	0	8189	870	490	93	1588	1402	186	5148	5148	69.38%	69.38%	69.38%	76.43%	78.60%	78.60%	78.60%	78.60%	78.60%	78.60%
#167	0	272	0	272	165	54	0	53	19	15	4	34	34	34	34	15.89%	64.15%	69.39%	69.39%	69.39%	69.39%	69.39%
#168	786	0	0	786	146	83	9	548	194	164	30	30	354	354	354	53.31%	64.50%	68.34%	68.34%	68.34%	68.34%	68.34%
#169	18	0	0	18	0	9	1	8	6	5	1	2	2	2	2	28.57%	25.00%	28.57%	28.57%	28.57%	28.57%	28.57%
#170	0	94	0	94	56	3	1	34	13	12	1	21	21	21	21	23.60%	61.76%	63.64%	63.64%	63.64%	63.64%	63.64%

ORDERING

REPORT: PERCENT FLOW THROUGH SERVICE REQUESTS (UNE DETAIL)
REPORT PERIOD: 06/01/2001 - 06/30/2001Supplemental Exhibit DAC-3
Attachment 2A

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH					
		LESOG					Errors					CLEC		Base Calculation		CLEC Error Excluded Calculation	
Name	RESH / OCN	LENS	EDI	TAG	Mechanized Interface Used			LSR's	Total Mech LSR's	Manual Total	Rejects	Validated	Pending Supps (Z Status)	Total System Fallout	BST Caused Fallout	Issued SO's	Achieved Flowthrough
					Total	Mech	Auto Clarification										
#171		1	0	1	0	0	1		0	0	0	0	0	0	0	0.00%	0.00%
#172		3	0	0	0	0	3		1	0	0	1	1	1	0	0.00%	0.00%
#173		1	0	0	0	0	1		0	0	0	1	1	1	0	0.00%	0.00%
#174	19	0	0	19	5	5	3		6	5	5	5	3	2	0	0.00%	0.00%
#175	0	559	0	559	109	61	2		387	127	101	26	26	260	260	55.32%	67.18%
#176	606	0	0	606	85	99	7		415	128	111	17	17	287	287	59.42%	69.16%
#177	0	137	0	137	33	41	2		61	17	11	6	6	44	44	50.00%	72.13%
#178	0	0	15	15	9	2	0		4	0	0	0	0	4	4	30.77%	100.00%
#179	41	0	0	41	4	8	0		29	5	5	0	0	24	24	72.73%	82.76%
#180	101	0	0	101	6	32	14		49	48	42	6	6	1	1	2.04%	2.04%
#181	0	8	0	8	0	1	0		7	2	2	0	0	5	5	71.43%	71.43%
#182	2026	0	0	2026	217	197	23		1569	827	676	151	151	762	762	46.04%	47.95%
#183	1	0	0	1	0	0	0		0	1	1	0	0	0	0	0.00%	0.00%
#184	201	0	0	201	0	1	4		196	38	35	3	3	158	158	81.87%	80.61%
#185	597	0	0	597	126	114	15		342	194	147	47	47	148	148	35.15%	43.27%
LENS Subtotal		38418	0	38418	5242	3665	403		29108	7289	5780	1509	1509	21819	21819	66.44%	74.96%
EDI Subtotal		0	27627	0	27627	6748	3655		17180	4639	2148	2491	2491	12541	12541	58.50%	73.00%
TAG Subtotal		0	0	18693	18693	6512	2508		9428	4007	3079	928	928	5421	5421	36.11%	57.50%
TOTAL INTERFACES		38418	27627	18693	84738	18693	18502	9828	692	155716	155716	11007	4928	39781	39781	57.41%	78.33%

ORDERING

REPORT: PERCENT LNP FLOW THROUGH SERVICE REQUESTS (SUMMARY)
REPORT PERIOD: 06/01/2001 - 06/30/2001

Supplemental Exhibit DAC-3
Attachment 2A

	ACHIEVED FLOW- THROUGH %	ADJUSTED FLOW- THROUGH %
CLEC AGGREGATE		
REGION ALL SERVICES	54.30%	91.83%

ORDERING

REPORT: PERCENT LNP FLOW THROUGH SERVICE REQUESTS (AGGREGATE DETAIL)

Supplemental Exhibit DAC-3

Attachment 2A

REPORT PERIOD: 06/01/2001 - 06/30/2001

AGGREGATE ORDER TYPES		LSR PROCESSING										FLOWTHROUGH				
Company Info		Mechanized Interface Used					LSR's					CLEC				
Name	RESH / OCN	EDI	TAG	Total	Manual Total	Rejects	Validated	Total	System Fallout	Caused Fallout	Caused Fallout	Achieved Flowthrough	Base Calculation	CLEC Error Excluded Calculation		
#1		322	0	322	167	11	144	83	65	18	61	20.82%	42.36%	48.41%		
#2		621	0	621	274	57	290	55	31	24	235	43.52%	81.03%	88.35%		
#3		0	31	31	17	3	11	10	1	9	1	5.26%	9.09%	50.00%		
#4		4	0	4	1	0	3	0	0	0	3	75.00%	100.00%	100.00%		
#5		1	0	1	0	0	1	0	0	0	1	100.00%	100.00%	100.00%		
#6		0	1588	1588	633	124	831	302	149	153	529	40.35%	63.66%	78.02%		
#7		677	0	677	548	55	74	32	20	12	42	6.89%	56.76%	67.74%		
#8		0	41	41	22	5	14	8	4	4	6	18.75%	42.86%	60.00%		
#9		0	1496	1496	1418	78	0	0	0	0	0	0.00%	0.00%	0.00%		
#10		174	0	174	97	7	70	28	16	12	42	27.10%	60.00%	72.41%		
#11		9	0	9	3	2	4	3	2	1	1	16.67%	25.00%	33.33%		
#12		0	85	85	27	8	50	11	9	2	39	52.00%	78.00%	81.25%		
#13		2046	0	2046	735	156	1155	241	129	112	914	51.41%	79.13%	87.63%		
#14		0	217	217	112	23	82	17	11	6	65	34.57%	79.27%	85.53%		
#15		2746	0	2746	308	29	2409	103	44	59	2306	86.76%	95.72%	98.13%		
#16		94	0	94	68	4	22	7	0	7	15	18.07%	68.18%	100.00%		
#17		0	2	2	0	2	0	0	0	0	0	0.00%	0.00%	0.00%		
#18		152	0	152	145	0	7	5	0	5	2	1.36%	28.57%	100.00%		
#19		4239	0	4239	519	83	3637	230	66	164	3407	85.35%	93.68%	98.10%		
#20		0	83	83	40	5	38	13	3	10	25	36.76%	65.79%	89.29%		
#21		7	0	7	3	0	4	4	3	1	0	0.00%	0.00%	0.00%		
#22		1013	0	1013	636	45	332	116	41	75	216	24.19%	65.06%	84.05%		
#23		0	18	18	7	0	11	8	6	2	3	18.75%	27.27%	33.33%		
#24		362	0	362	150	17	195	83	64	19	112	34.36%	57.44%	63.64%		
#25		13	0	13	1	1	2	10	0	0	10	90.91%	100.00%	100.00%		
#26		370	0	370	189	9	172	76	59	17	96	27.91%	55.81%	61.94%		

Trunk Group Performance - Aggregate

Tennessee		Average blocking percentage by hour																							
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24
BellSouth	0.0166	0.0071	0.0011	0.0017	0.0000	0.0008	2.0138	0.1060	0.0354	0.2558	0.0235	0.1039	0.2420	0.8236	0.1510	0.0421	0.0654	0.0387	0.0352	0.0089	0.0138	0.0790	0.4188	0.0707	
CLEC	0.0802	0.1196	0.1087	0.0603	0.0024	0.0036	0.0258	0.0821	0.0392	0.0219	0.0093	0.0344	0.0499	0.0129	0.0520	0.0210	0.0407	0.0252	0.0114	0.0095	0.1095	0.1170	0.0479	0.0561	
Difference	-0.0636	-0.1125	-0.1086	-0.0024	-0.0586	-0.0028	1.9880	0.0238	-0.0037	0.2338	0.0142	0.0694	0.1922	0.8106	0.0980	0.0210	0.0246	0.0135	0.0238	-0.0006	-0.0957	-0.0379	0.3708	0.0146	
BellSouth	0.0084	0.0030	0.0006	0.0000	0.0006	0.0068	0.4921	0.0417	0.3249	0.1087	0.0699	0.0585	0.0587	0.1708	0.0415	0.0353	0.0112	0.0723	0.0544	0.0006	0.0096	0.0317	0.0862	0.0576	
CLEC	0.0000	0.0003	0.0894	0.0000	0.0000	0.0004	0.0356	0.0697	0.0445	0.0143	0.0122	0.0148	0.0171	0.0088	0.0273	0.0238	0.0409	0.0501	0.0031	0.0020	0.1757	0.2898	0.0140	0.0018	
Difference	0.0084	0.0027	-0.0888	0.0000	0.0006	0.0064	0.4565	-0.0281	0.2804	0.0844	0.0577	0.0447	0.0116	0.1620	0.0143	0.0115	-0.0297	0.0422	0.0514	-0.0014	-0.1681	-0.2581	0.0722	0.0598	
BellSouth	0.0186	0.0123	0.0017	0.0032	0.0118	0.0607	0.1724	0.4406	0.2229	0.3368	0.0576	0.0942	0.2058	0.1120	0.0441	0.0738	0.0469	0.0043	0.0075	0.0389	0.0286	0.0384	0.1458		
CLEC	0.0011	0.0002	0.0281	0.0002	0.0000	0.0033	0.0474	0.1776	0.1129	0.0261	0.0285	0.0289	0.0323	0.0437	0.0391	0.0908	0.0438	0.0134	0.0379	0.0129	0.0430	0.7147	0.0191	0.0022	
Difference	0.0176	-0.0184	-0.0159	0.0015	0.0032	0.0084	0.0133	-0.0053	0.3278	0.1967	0.0276	0.0169	0.1621	0.0729	-0.0466	0.0300	0.0335	-0.0337	-0.0054	-0.3842	-0.6851	0.0194	0.1436		
BellSouth	0.0134	0.0148	0.0008	0.0025	0.0003	0.0016	0.8764	2.4455	1.5437	1.0445	0.3587	0.7137	0.1433	1.5530	0.3835	1.7737	0.5931	0.5860	0.2281	0.1479	0.0852	0.1097	0.0867	0.0491	
CLEC	0.0212	0.0254	0.0031	0.0527	0.0147	0.1085	0.8483	2.0681	1.0243	0.4535	0.2820	0.3322	0.2075	0.3746	0.3025	0.7683	0.5379	0.5164	0.3239	0.2819	0.4918	0.5768	0.2963	0.0564	
Difference	-0.0078	-0.0106	-0.0023	-0.0503	-0.1045	-0.1068	0.0281	0.3775	0.5194	0.6410	0.0737	0.3805	-0.0642	1.1784	0.0810	1.2954	0.0552	0.0396	-0.1048	-0.1341	-0.4067	-0.4671	-0.1395	-0.0073	
BellSouth	0.1582	0.0204	0.0074	0.0062	0.0082	0.0706	2.2964	0.6359	0.4719	0.0758	0.3709	0.2270	0.4039	1.1950	0.1824	0.2839	0.3667	1.2912	0.8064	0.0369	0.0069	0.0371	2.9830	0.2204	
CLEC	0.0311	0.0072	0.0026	0.0055	0.0051	0.0029	0.0313	0.1908	0.0886	0.0651	0.0449	0.0417	0.0358	0.0598	0.0327	0.0320	0.0444	0.0804	0.1018	0.0835	0.2103	0.1457	0.0162	0.0161	
Difference	0.1271	0.0132	0.0049	0.0007	0.0031	0.0707	2.2552	0.4452	0.3833	0.0107	0.3280	0.1852	0.3681	1.1352	0.1497	0.2519	0.3223	1.2108	0.7046	-0.0466	-0.2034	-0.1086	2.9367	0.2043	
BellSouth	0.0425	0.0037	0.0007	0.0013	0.0019	0.0149	0.2083	0.2367	0.3048	0.1945	0.0766	0.0315	0.2827	0.3590	0.0738	0.0602	0.0546	0.0247	0.0663	0.0025	0.0022	0.0116	0.0412	0.1123	
CLEC	0.0050	0.0003	0.0049	0.0006	0.0016	0.0120	0.0266	0.1436	0.1194	0.0481	0.1130	0.1084	0.0511	0.0286	0.0332	0.0291	0.0731	0.0852	0.0562	0.0663	0.2062	0.1114	0.0112	0.0025	
Difference	0.0375	0.0033	-0.0043	0.0007	0.0003	0.0930	0.1817	0.0930	0.1854	0.1464	-0.0243	-0.0779	0.2316	0.3323	0.0406	0.0311	-0.0165	-0.0605	-0.0439	-0.0638	-0.2040	-0.1097	0.0300	0.1098	
BellSouth	0.0050	0.0014	0.0064	0.0071	0.0025	0.3169	0.1855	0.0767	0.1043	0.0160	0.1113	0.1504	0.3565	0.0098	0.6682	0.0273	0.4908	0.4667	0.0205	0.0044	0.0026	2.0178	0.0948		
CLEC	0.0453	0.0502	0.0432	0.0391	0.0014	0.0056	0.1685	0.0716	0.1263	0.0725	0.0659	0.0282	0.1653	0.5347	0.0177	0.5667	0.0425	0.0862	0.0556	0.0013	0.0082	0.0044	0.1681	0.0284	
Difference	-0.0403	-0.0488	-0.0368	-0.0320	-0.0674	-0.0874	0.2504	0.1124	-0.1084	-0.0112	0.0687	-0.0162	0.0686	0.1149	0.3148	-0.0338	-0.0441	-0.0853	0.3927	0.3498	-0.1322	-0.4747	-0.3520	1.9731	0.0536
BellSouth	0.0160	0.0021	0.0034	0.0009	0.0006	0.0065	0.4853	0.1111	0.2342	0.1037	0.0480	0.0978	0.2507	0.1806	0.1420	0.0239	0.0938	0.2198	0.1666	0.0037	0.0067	0.0249	0.5616	0.0275	
CLEC	0.0020	0.0004	0.0005	0.0003	0.0001	0.0005	0.0285	0.1249	0.0963	0.0234	0.0254	0.0336	0.0321	0.0233	0.0459	0.0575	0.0662	0.0815	0.1765	0.3189	0.6817	0.3264	0.0113		
Difference	0.0140	0.0071	0.0029	0.0006	0.0006	0.0061	0.4568	-0.0138	0.1379	0.0963	0.0226	0.0642	0.2187	0.1514	0.0961	-0.0336	0.0277	0.1382	-0.0398	-0.3152	-0.6750	-0.2335	-0.5442	-0.0263	
BellSouth	0.0064	0.0043	0.0009	0.0000	0.0001	0.0133	0.6500	0.1758	0.0643	0.0168	0.2672	0.1795	0.2800	0.7795	0.2278	0.5256	0.2185	0.8843	0.0310	0.0025	0.0029	0.0060	0.0448	2.8002	0.0437
CLEC	0.0009	0.0012	0.0006	0.0002	0.0001	0.0005	0.0564	0.0001	0.0007	0.0553	0.0395	0.0263	0.0559	0.0446	0.0450	0.0446	0.0559	0.0598	0.0541	0.1106	0.0736	0.0267	0.0279	0.0142	
Difference	-0.0054	-0.0010	-0.0049	-0.0005	-0.0034	-0.0003	-0.0001	0.0127	0.0531	-0.2177	-0.1057	-0.0188	0.2387	0.1344	0.2354	0.7236	0.1680	0.4715	0.1080	0.0107	-0.0188	-0.0755	-0.2790	0.0280	0.0295
BellSouth	0.0051	0.0004	0.0000	0.0000	0.0012	6.3169	7.4932	0.5002	0.1317	0.0498	0.4471	0.3662	0.8102	0.1450	0.3898	0.2437	0.3603	0.1585	0.0050	0.0108	0.0048	0.0448	2.8002	0.0437	
CLEC	0.0061	0.0053	0.0005	0.0001	0.0054	0.0001	0.0005	0.0002	0.0007	0.0553	0.5385	0.2637	0.0559	0.0679	0.0563	0.1181	0.0833	0.0212	0.0267	0.0690	0.0992	0.0495	0.0178		
Difference	-0.0054	-0.0010	-0.0049	-0.0005	-0.0034	0.0001	0.0011	6.3162	7.4379	-0.0383	-0.1320	-0.0061	0.1098	0.3653	0.2754	0.7016	0.0771	0.1255	0.2770	0.1373	-0.0207	-0.0582	-0.0345	2.7507	0.0626
BellSouth	0.0062	0.0008	0.0006	0.0002	0.0001	0.0060	0.0400	0.0766	0.0491	0.0766	0.0290	0.0375	0.0733	0.0895	0.4859	0.0602	0.0386	0.0687	0.3005	0.1453	0.0072	0.0093	0.1010	0.5068	0.0395
CLEC	0.0881	0.4622	0.0000	0.0057	0.0001	0.0010	0.0400	0.1337	0.0874	0.0679	0.0835	0.0706	0.0491	0.0568	0.0630	0.0393	0.0491	0.0368	0.0127	0.0213	0.0495	0.0811	0.0430		
Difference	-0.0819	-0.4614	0.0006	-0.0054	0.0000	0.0049	0.0049	0.0709	-0.0846	-0.0108	-0.0389	-0.0460	0.0027	0.0404	0.4291	-0.0007	0.0196	0.2637	0.1326	-0.0141	-0.0402	-0.0710	0.4637	0.0223	

CERTIFICATE OF SERVICE

I hereby certify that on September 6, 2001, a copy of the foregoing document was served on the parties of record, via hand delivery, facsimile, overnight or US Mail, addressed as follows:

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